

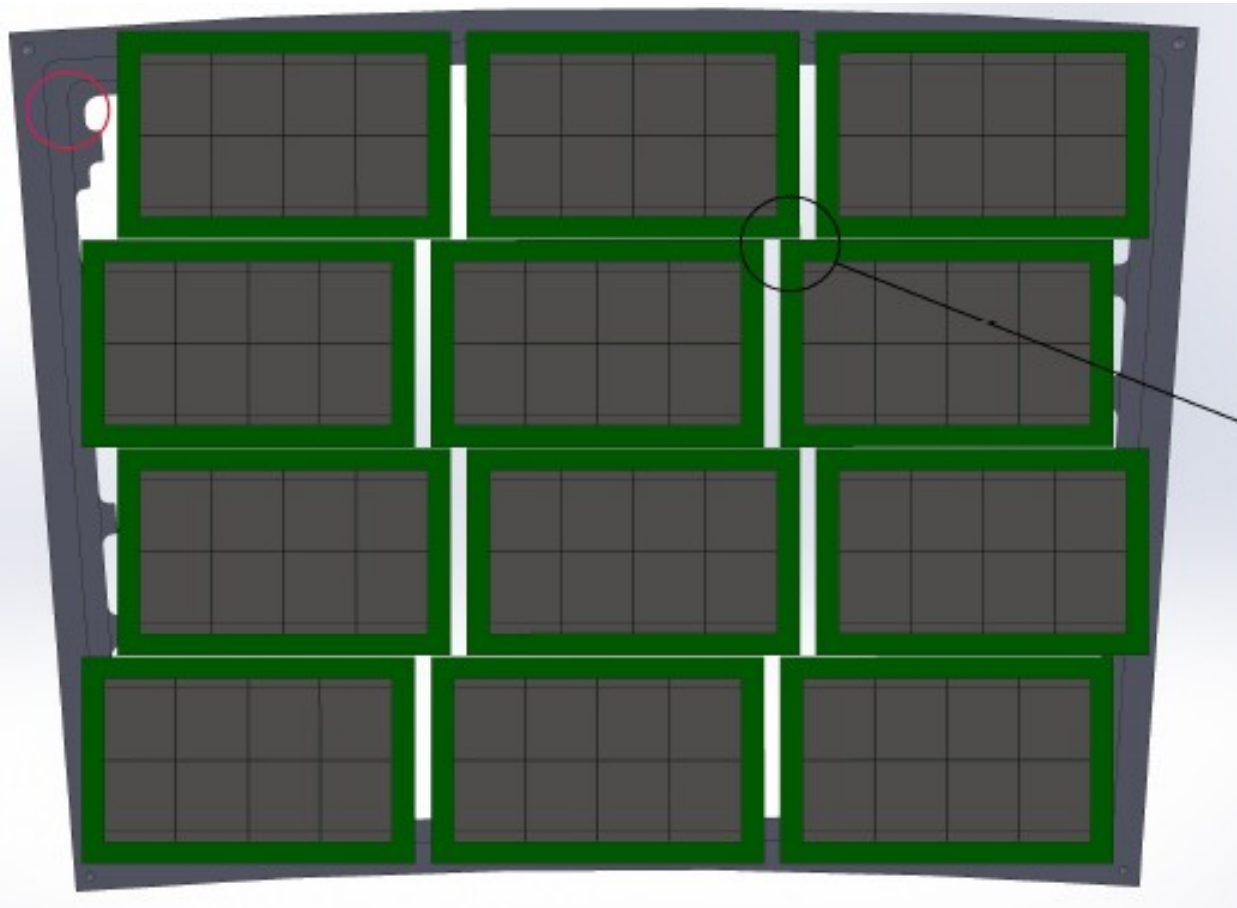
Status update of pixel readout

Michael Lupberger
University of Bonn

LCTPC-pixel Meeting 02.10.2014

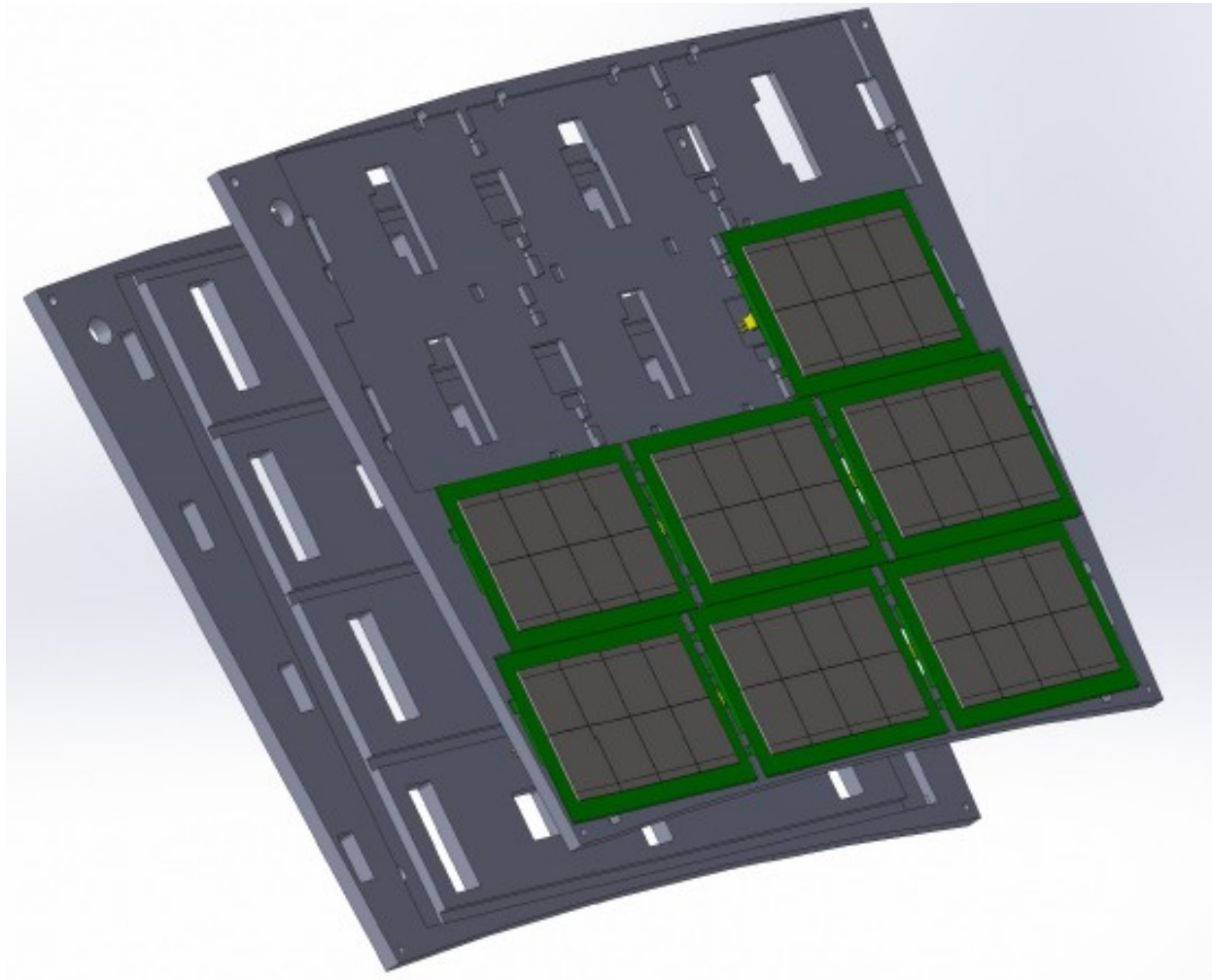
Module construction

Johann finished design in SolidWorks



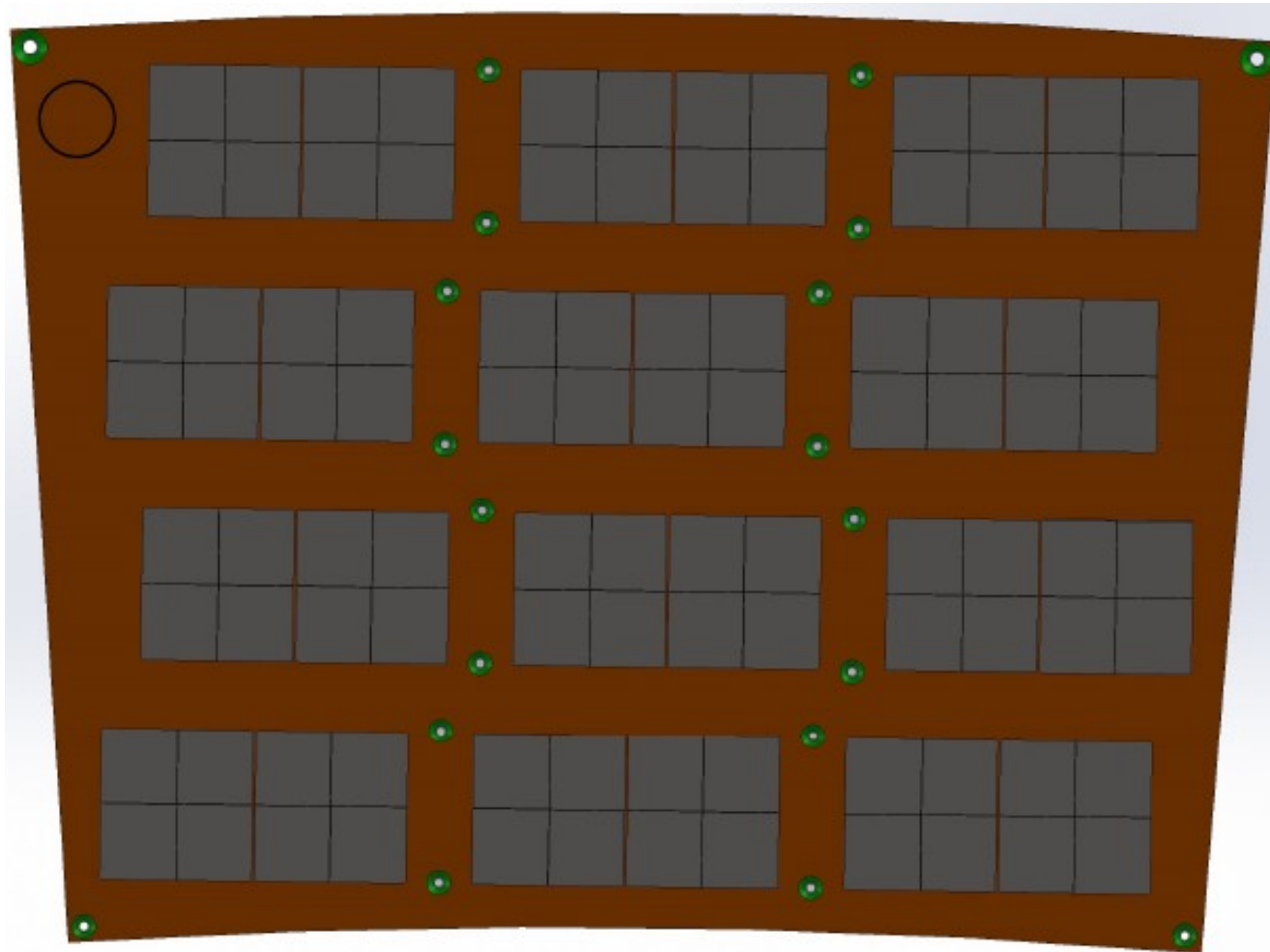
Module construction

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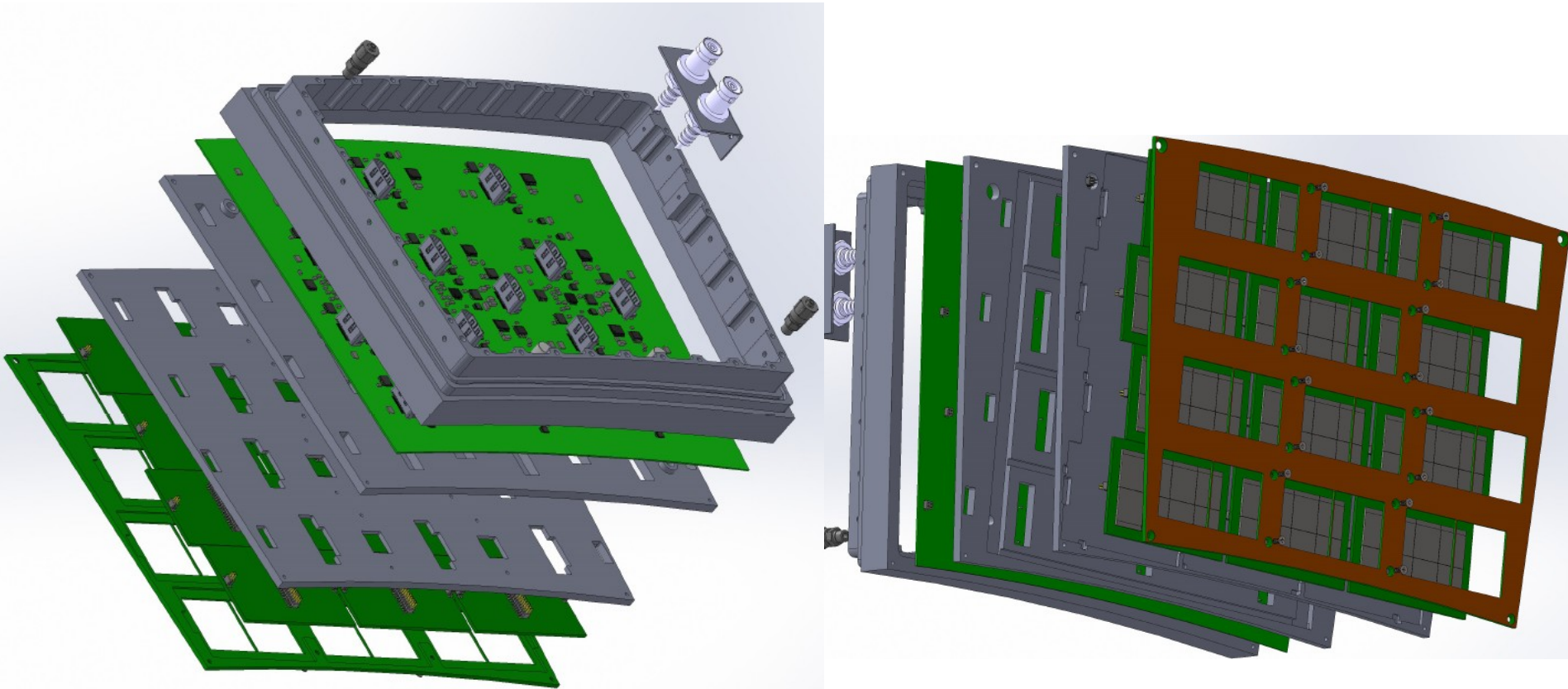
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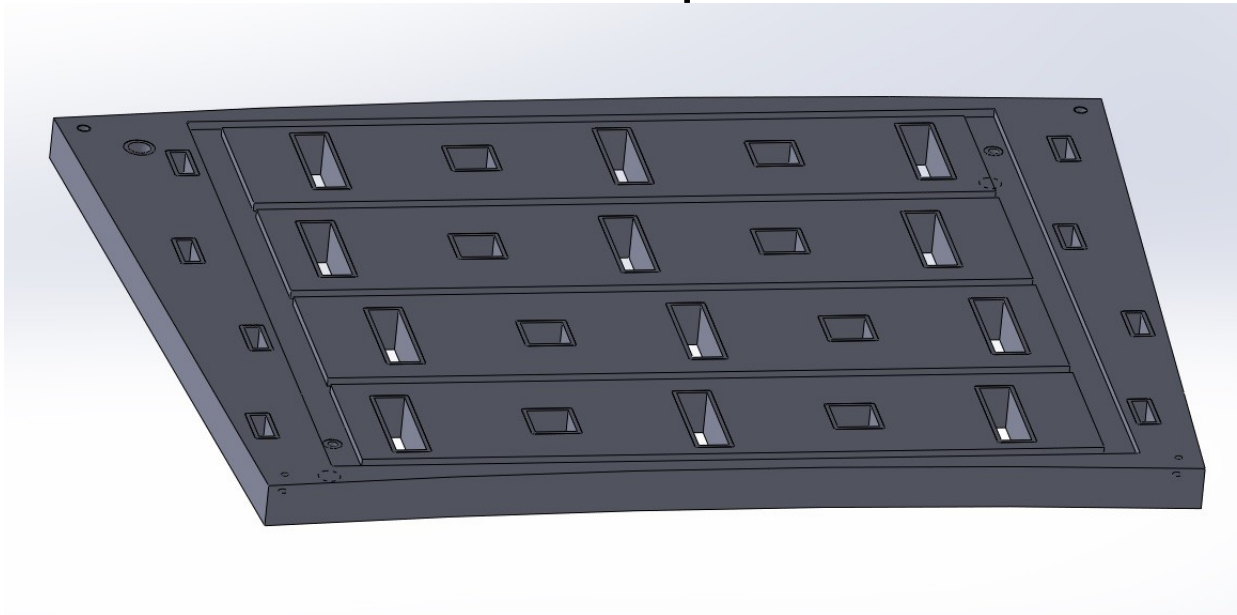
Module construction



Johann finished design in SolidWorks

Our workshop started the production of first components

- Two main plates with cooling pipes
- They will be welded together in FAZ Jülich
- After this our workshop will mill the more detailed parts



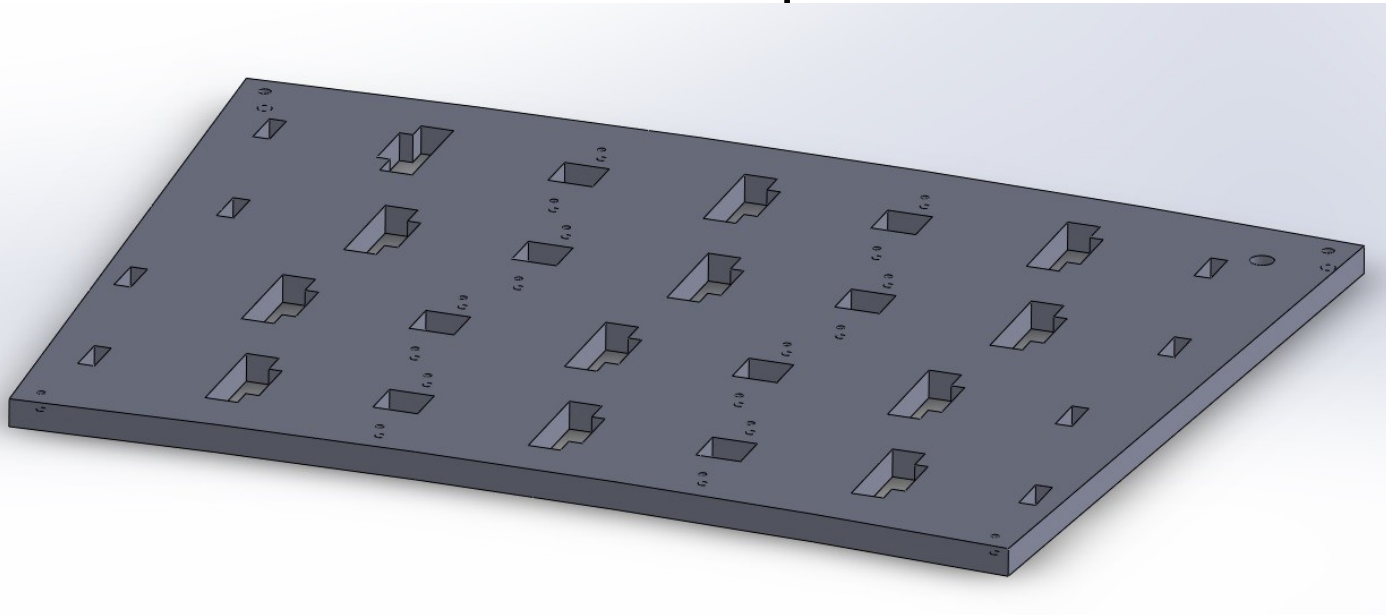
Module construction



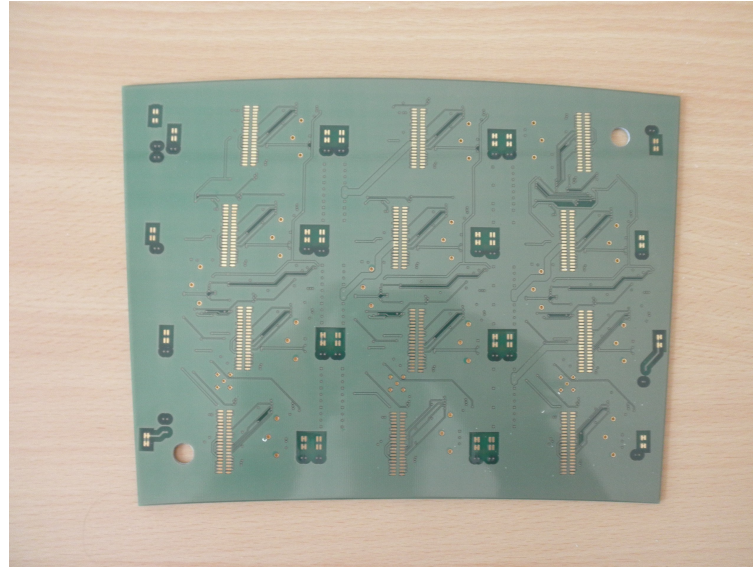
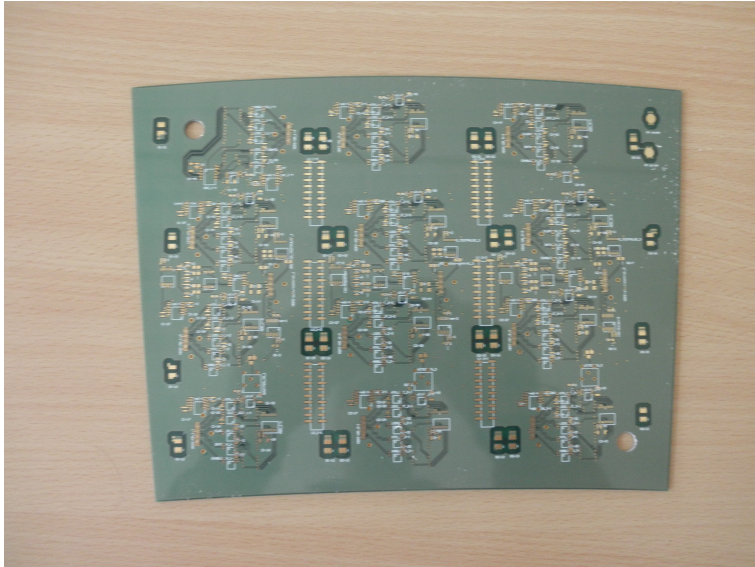
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Module construction



- Jochen finished design of Intermediate board
 - High component density
 - PCB arrived on Tuesday
 - Electronics workshop will start soldering components needed for one octoboard

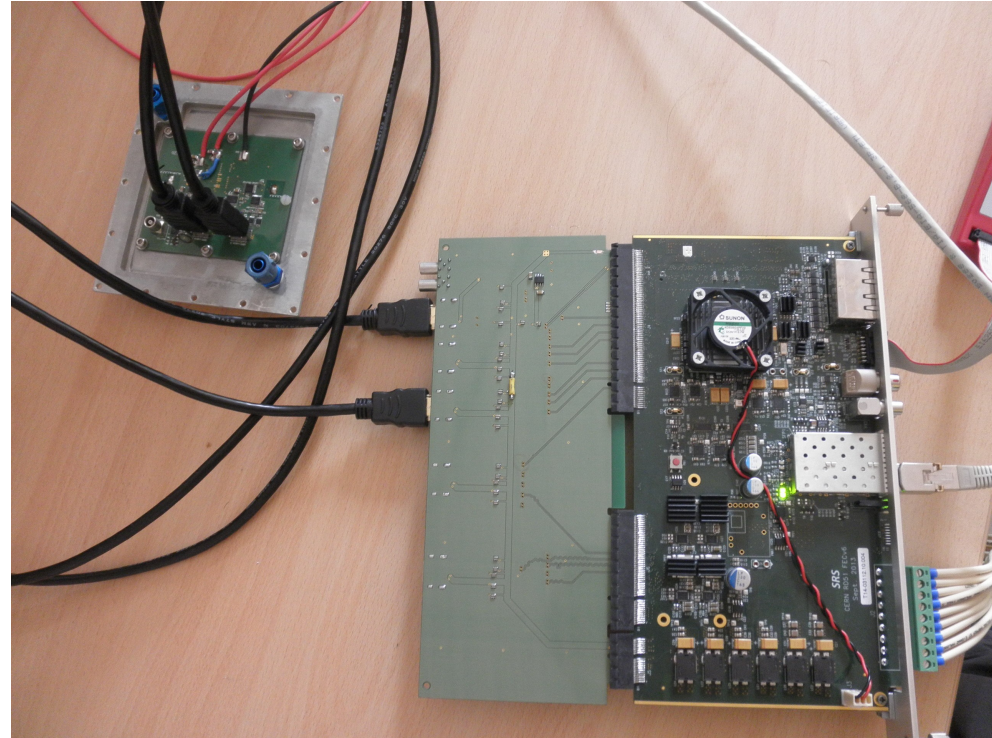
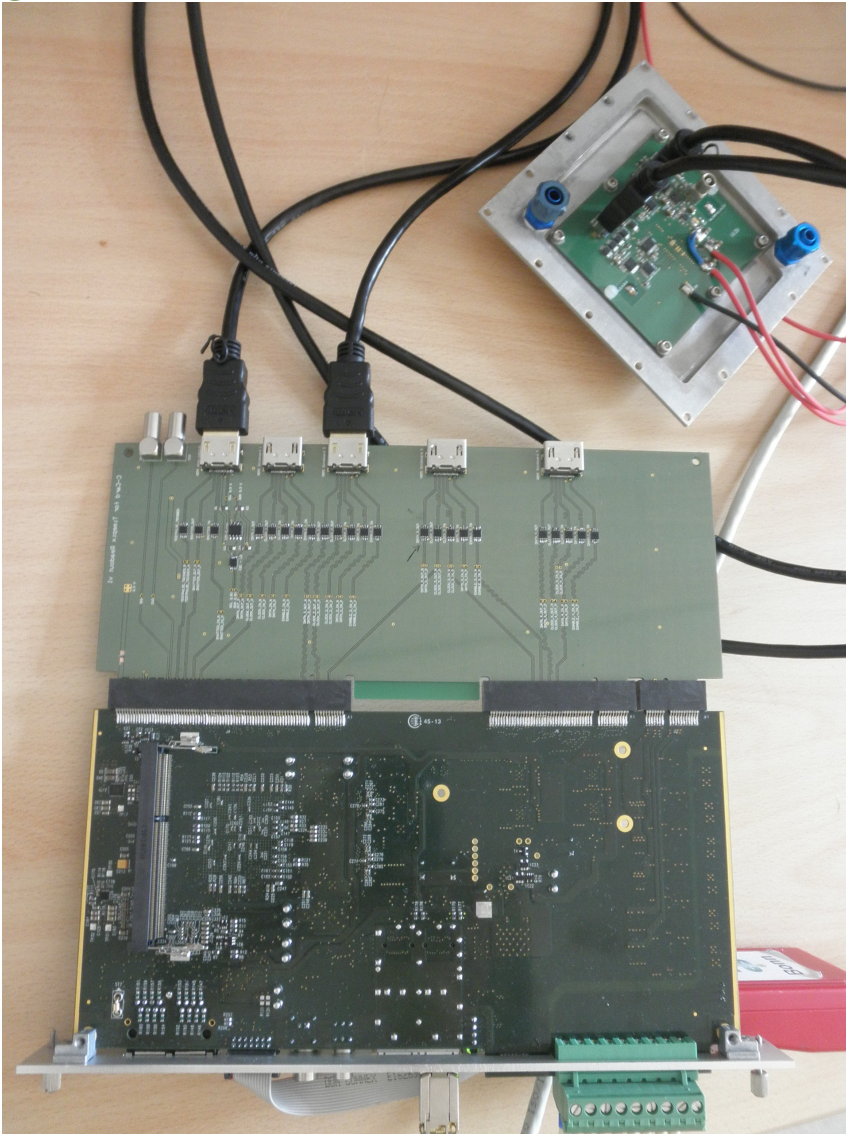
Readout system



SRS C-Card + FEC6 in operation

- 1 design error found (powering of LVDS drivers) due to difference between FEC5 and FEC6
- 1 LVDS driver defect
- Development of firmware ongoing
 - Test with one octoboard of naked Timepix chips connected to one of the four slots
 - Set DACs (FSR) in chip and read back chip ID ✓
 - Set matrix and read it back ✓
 - Zero suppressed readout ✓
 - Zero suppressed readout while sending data of last frame
- Software: needs extension for large amount of data
 - Until now: only one octoboard (select the one connected)

Readout system



Staff news



Johann finished Bachelor project, but will stay in group as student assistant

Martin is about to finish Master thesis.

Alexander joined group for diploma thesis last week.

Subject: Octoboard powering

Daniel joined group for master thesis this week.

Subject: Readout software