

ILD Technical Task Forces Meeting

Tuesday, 8 November 2016

VT Task Force Session II - Bldg. 200, Room 101 (11:00 - 13:00)

| time | [id] title | presenter |
|-------|---|--|
| 11:00 | [14] TESLA-Structure: Signal Paths and Electronics Accessibility/Reliability | KRUEGER, Katja |
| 11:30 | [15] Videau-Structure: Signal Paths and Electronics Accessibility/Reliability | IANIGRO, Jean Christophe LAKTINEH, imad |
| 12:00 | [9] Effects of phi and z Cracks | SEFKOW, Felix |
| 12:15 | [18] Effects of phi and z Cracks | LAKTINEH, imad |