

SiW-ECAL for CALICE, LUXE and Higgs Factories

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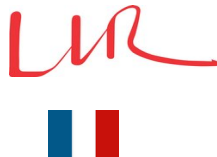


IN2P3
Les deux infinis



INSTITUT
POLYTECHNIQUE
DE PARIS

Institut Polytechnique de Paris
for the **CALICE SiW-ECAL groups**



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Situation in a nutshell

Situation is progressing slowly... but it progresses

- Analysis fo 2022 BT data:
 - improvement of response at medium (10 GeV) energies thanks to inclusion of the masked channels in the simulation → See pres. by Yuichi Okugawa in Analysis session

Preparation of DRD6 ↔ IN2P3

- “master document” for IN2P3 Master Project, formatted & detailed input for next 3 years
 - includes Gantt table and ressources demands
 - Articulation of ILD('), FCC-ee, CALICE and LUXE plans in Work Packages

Preparation of SiW-ECAL 'final' demonstrator

Material is ready:

- 30 FEV2.1 produced
 - 4 cabled @ IJClab
 - 2 being tested @ ICJLab & LLR & IFIC
- 440 ASICs packaged and being tested (→ Vincent's presentation earlier)
- 60 Wafers available (@LLR, IJCLab, IFIC, KEK, Kyushu)
 - Ideally will be tested at IFIC & CERN (→ Adrián's presentation tomorrow morning)
- HV Kapton to be re-produced...

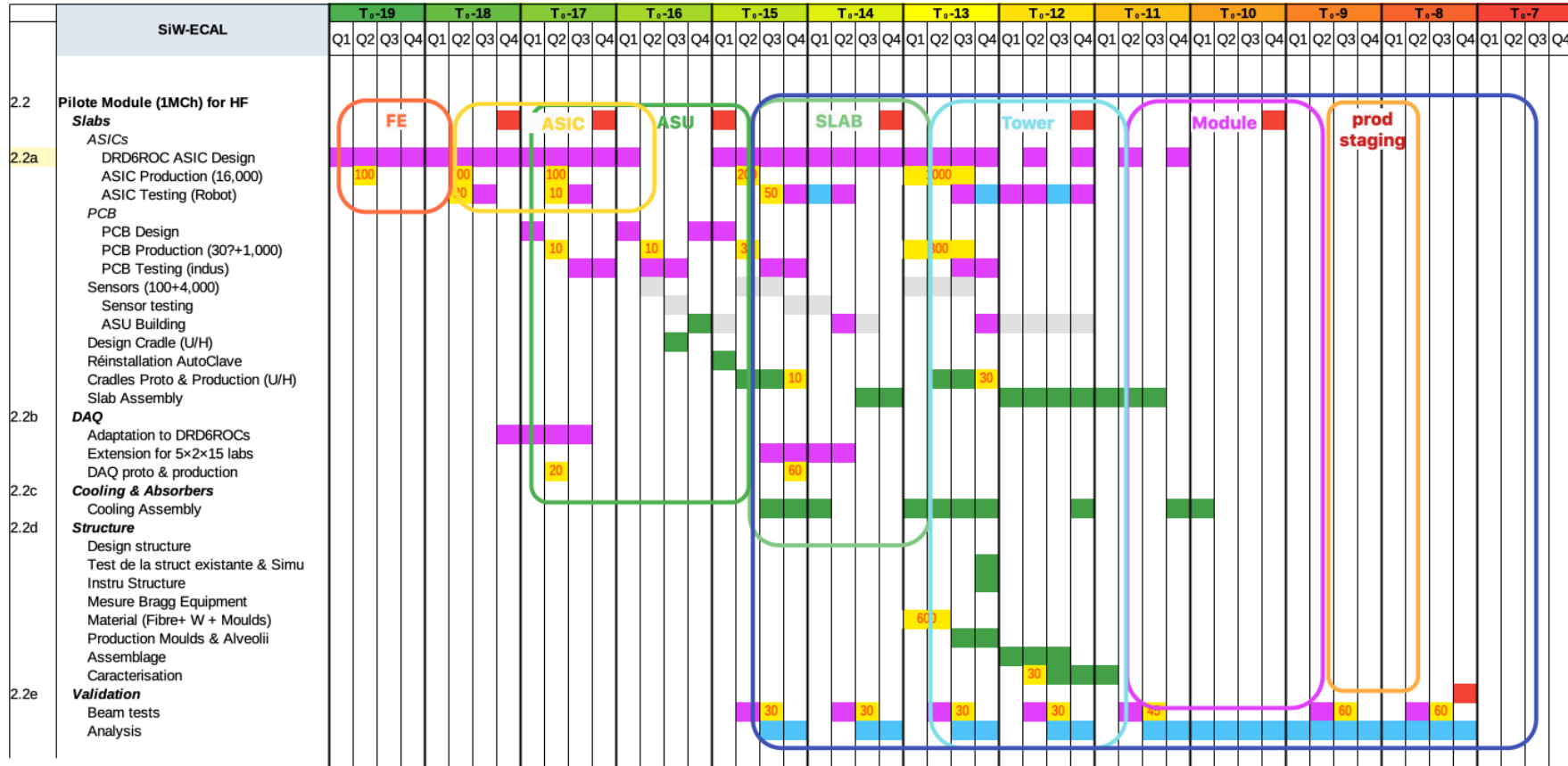
Main issue: Gluing Wafers onto FEV's (→ Adrián's presentation tomorrow morning)

- > thermal stress studies on FEV's
- several options (conductive and non-conductive glue types, tapes, ...) being investigated

Ideally : methods works → several ASUs in BT at DESY bef. summer 2024 → Full stack at CERN late 2024

Indicative planning for prototypes

Planning towards a pilot module... *just in case*



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FCCee	2045-48 ?
CEPC	2035 ?
ILC	2040 ?