



# **Pixelated Readout TPC Development for the Circular $e^+e^-$ Collider**

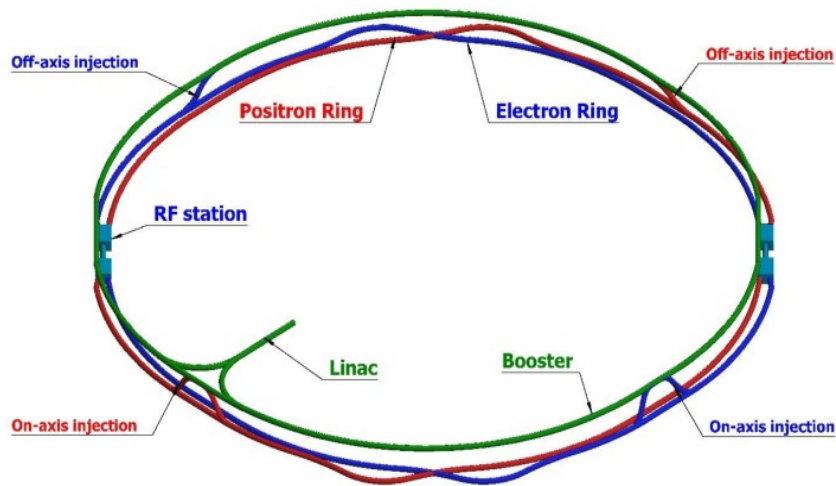
**Jochen Kaminski, Maxim Titov and Huirong Qi**  
**On behalf of the LCTPC Collaboration**

**ILD group meeting, 12 November, 2024**

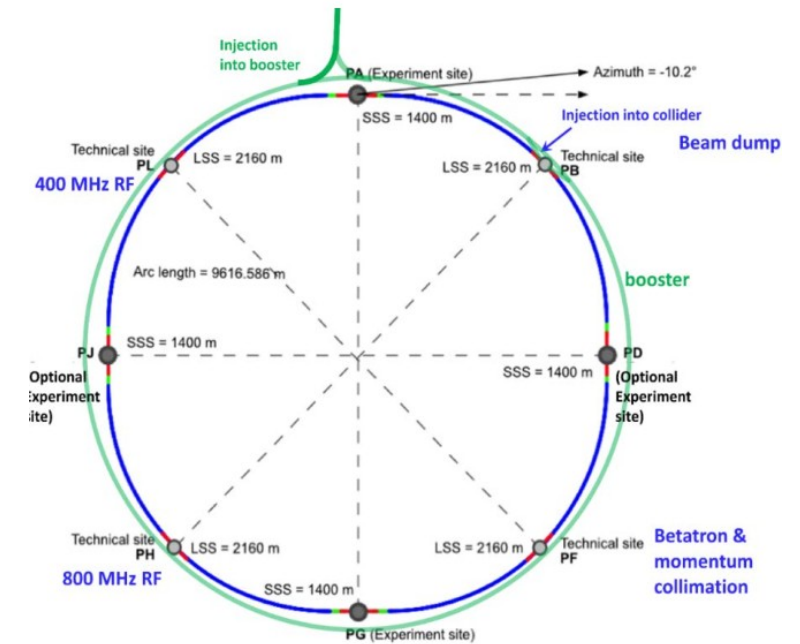
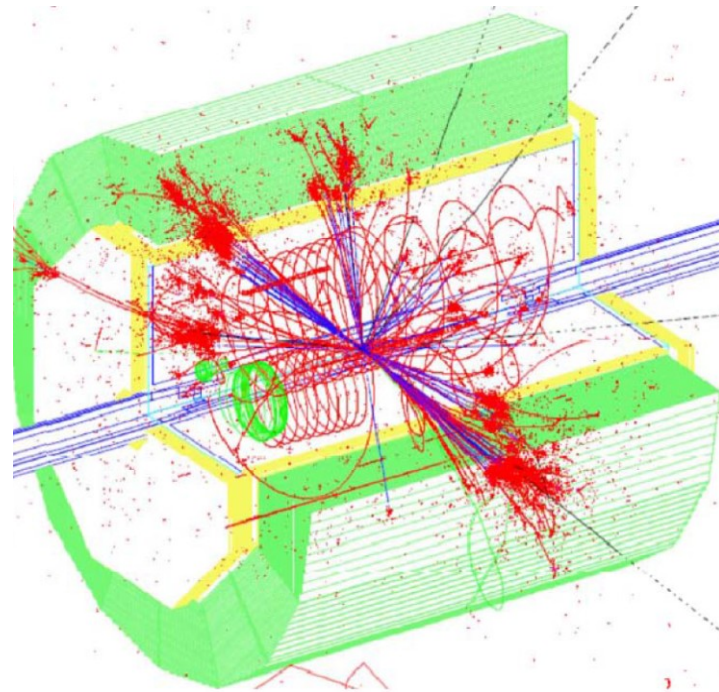
- **Motivation and physics requirements**
- **Status of TPC in LCTPC and CEPC**
- **Pixelated readout TPC for Higgs and Z**
- **Work plan and Summary**

# Motivation and physics requirements

- A TPC is the main track detector for some candidate experiments at future  $e^+e^-$  colliders.
  - **Baseline detector concept** of ILD at ILC and CEPC
- TPC technology can be of interest for other future colliders (EIC, FCC-ee)
- Pixelated readout TPC can improve **PID requirements of Flavor Physics at  $e^+e^-$  collider.**



Circular Electron Positron Collider (CEPC)

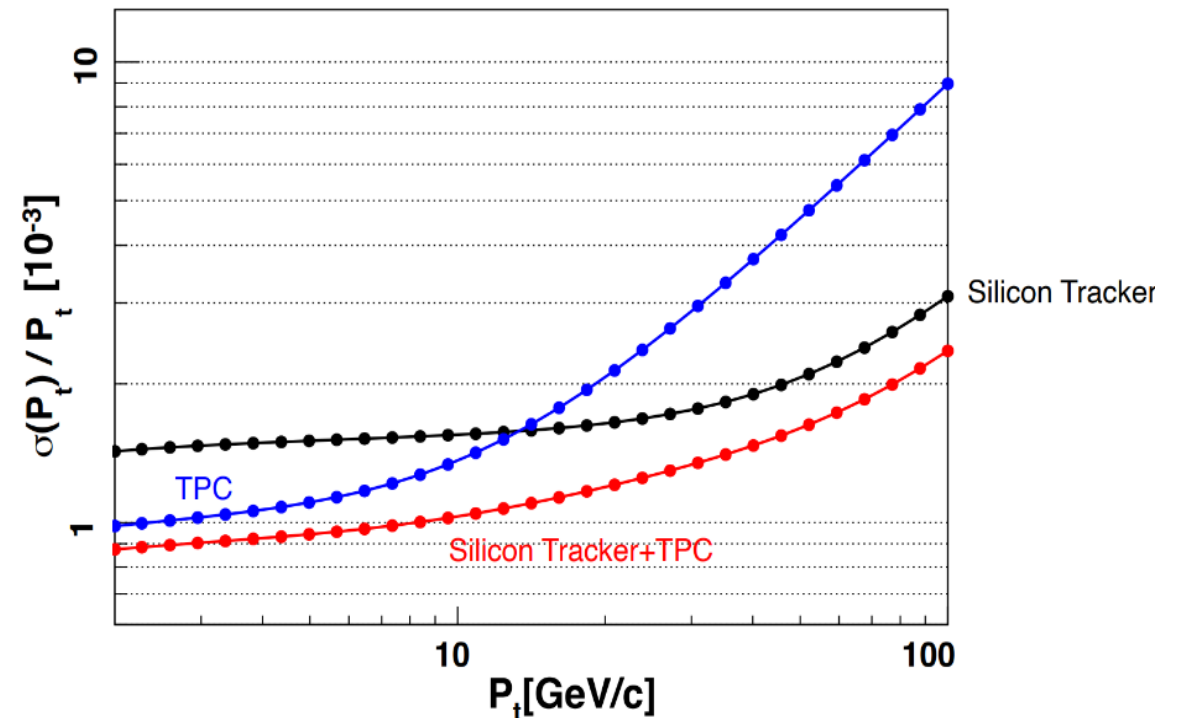
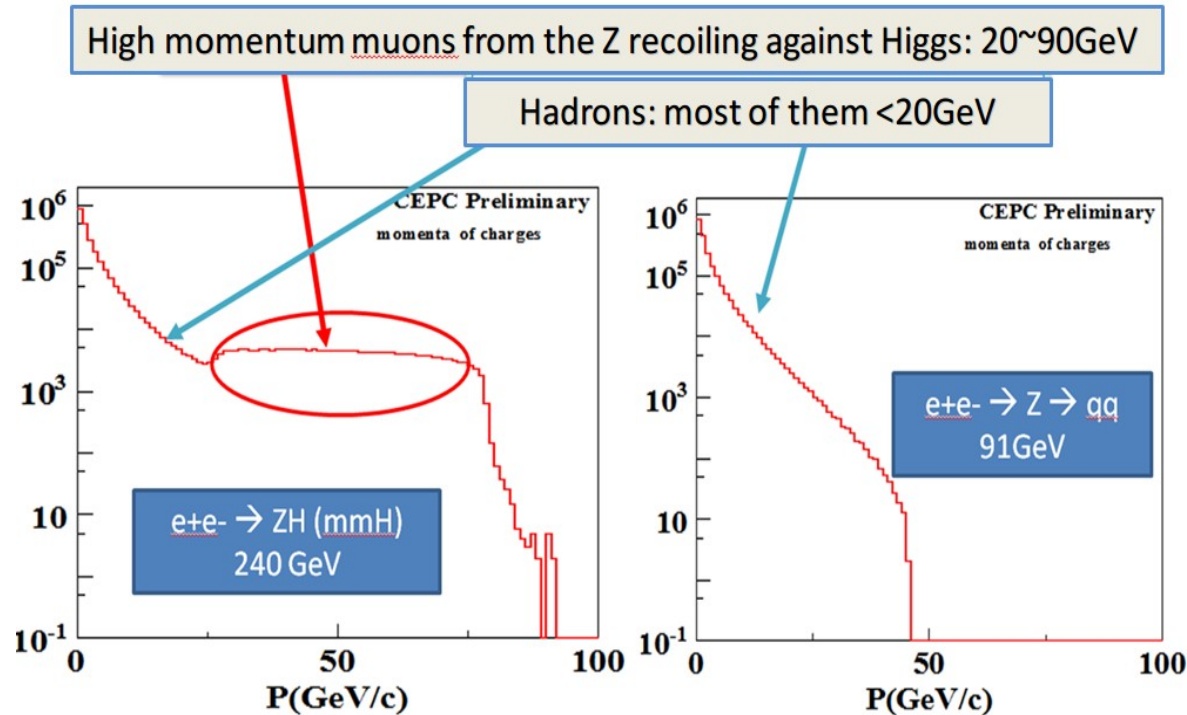


Future Circular Collider (FCCee)

# Motivation and physics requirements

- Circular e+e- collider operation stages in TDR: **10-years Higgs @3T** → **2-years Z pole** → **1-year W**
- Physics Requirements of the tracker
  - High momentum resolution for Higgs and Z
  - PID for the flavor physics and jet substructure

**Calibration: Low luminosity Z at 3T**  
 Approximately  $10^{35} \text{cm}^{-2}\text{s}^{-1}$   
 1%-20% of high luminosity Z

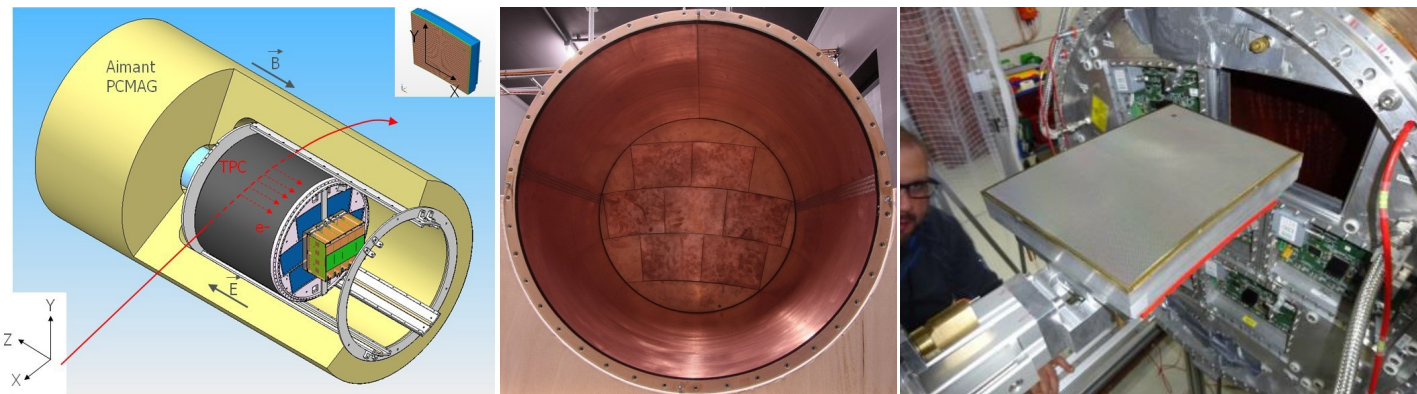
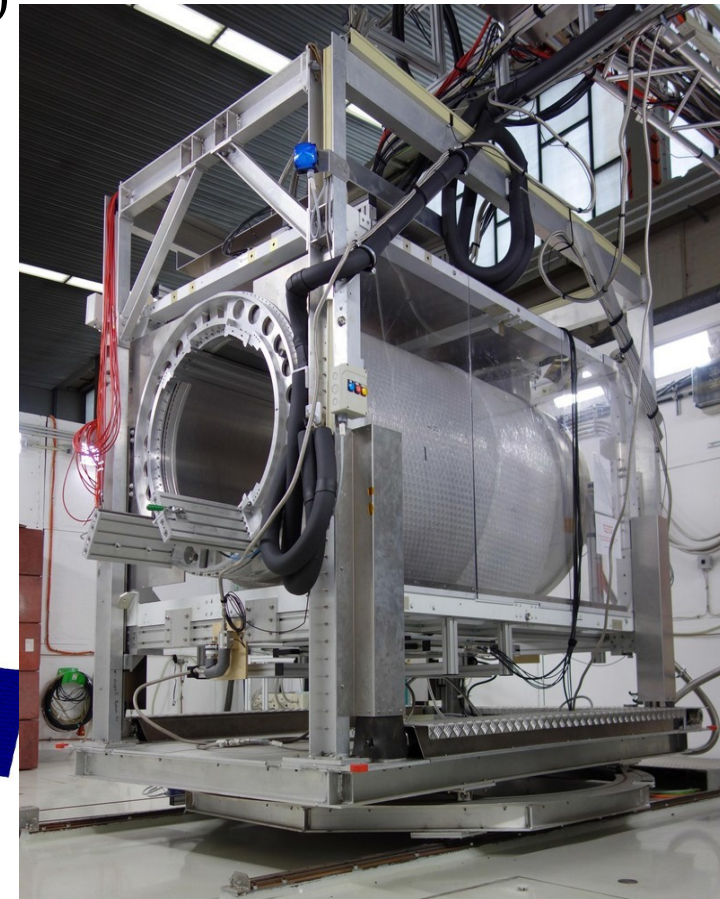




# Status of TPC in LCTPC and CEPC

- **Large Prototype** setup has been built to compare different detector readouts under identical conditions and to address integration issues.
  - PCMAG:  $B < 1.2\text{T}$ , bore  $\text{\O} = 85\text{cm}$
  - LP support structure (3D movable) Beam and cosmic trigger
    - Silicon tracker inside PCMAG LYCORIS (single point res.:  $7\mu\text{m}$ )
- **LP Field Cage Parameter**
  - Length =  $61\text{cm}$ , inner  $\text{\O} = 72\text{cm}$  drift field up to  $E \approx 350\text{V/cm}$
  - Made of composite materials:  $1.24\% X_0$
- **Modular End Plate**
  - Two end plates for the LP made from Al with 7 module windows
  - ALTRO based readout electronics (7212 channels)

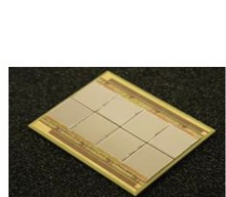
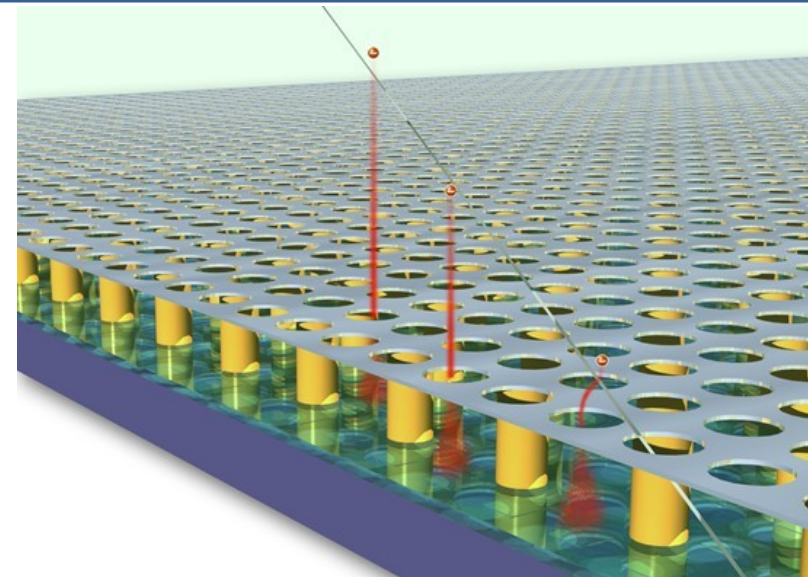
JINST 5: P10011, 2010  
JINST 16: P10023, 2021



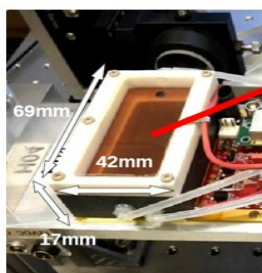


# Status of TPC in LCTPC and CEPC

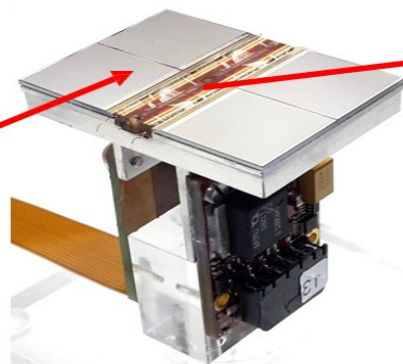
- GridPix detector have moved from Timepix to Timepix3 ASICs. Tests with single and quad devices have been successfully done.
  - A module **with 32 GridPixes has been constructed** and was in a test beam in B=1.0T at DESY in 2021 and 2022.
- Very high detection efficiency results in **excellent tracking and dE/dx performance**. Timepix4 development is ongoing.
  - During the test beam  $\sim 10^6$  events were successfully collected, all results showed that **a pixel TPC is realistic**.



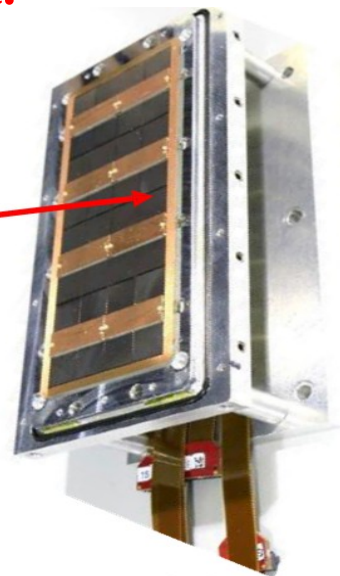
(Octopuce)



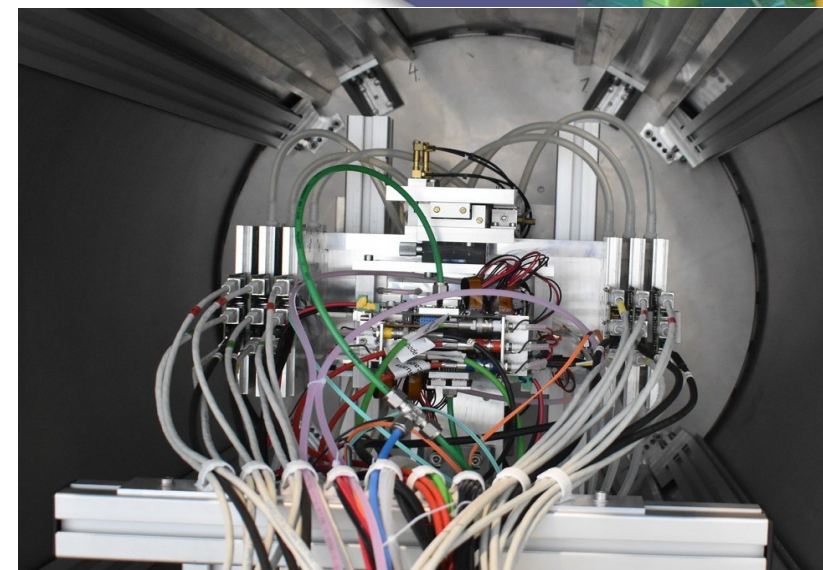
TPX3 chip  
2017



Quad  
2018



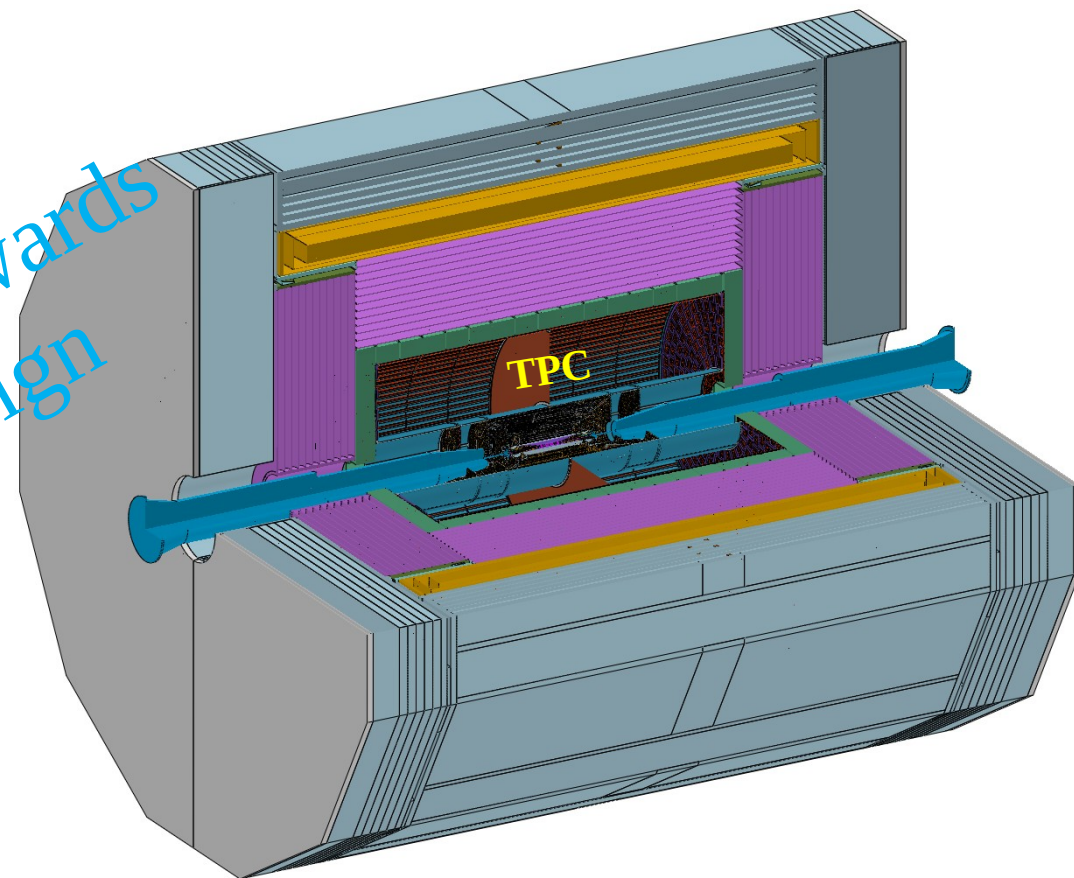
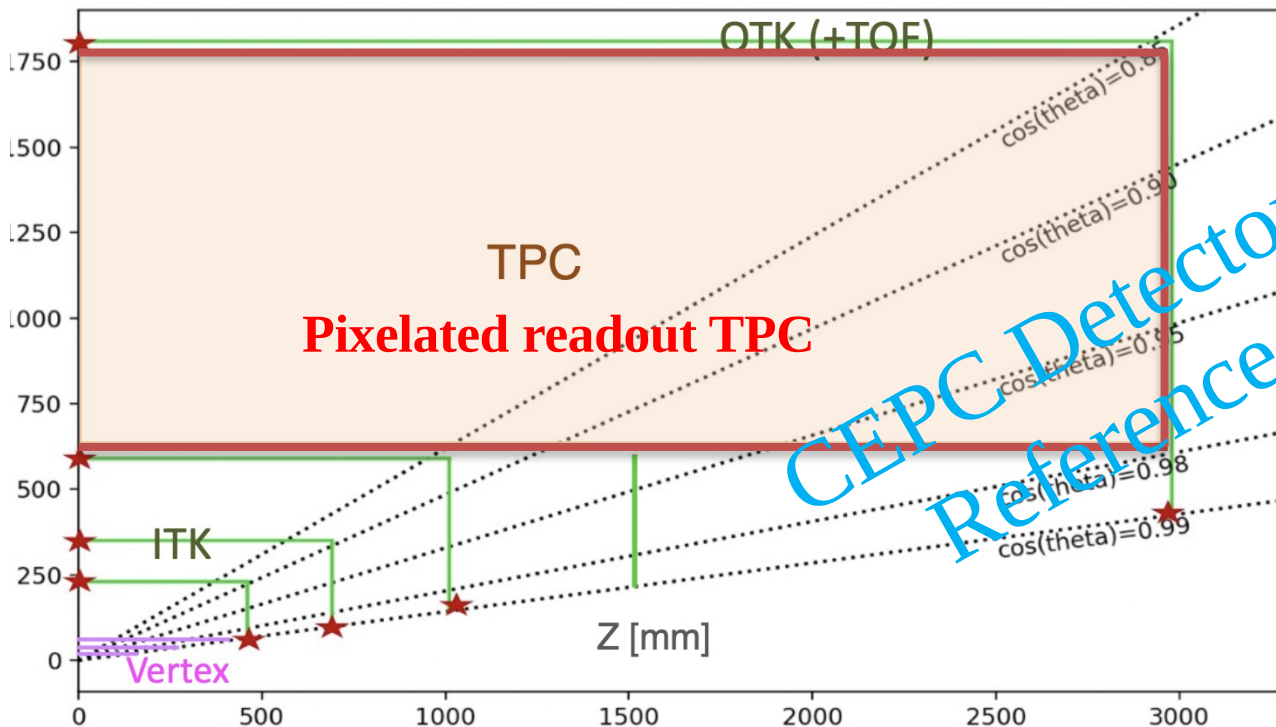
Module  
2019



NIM A535 (2004) 506-510  
NIM A845 (2017) 233-235

# Status of TPC in LCTPC and CEPC

- Tracking system: Silicon combined with gaseous chamber for the tracking and PID
  - Pixelated readout TPC as the **baseline gaseous detector** in the CEPC ref-TDR
    - Radius of TPC from 0.6m to 1.8m

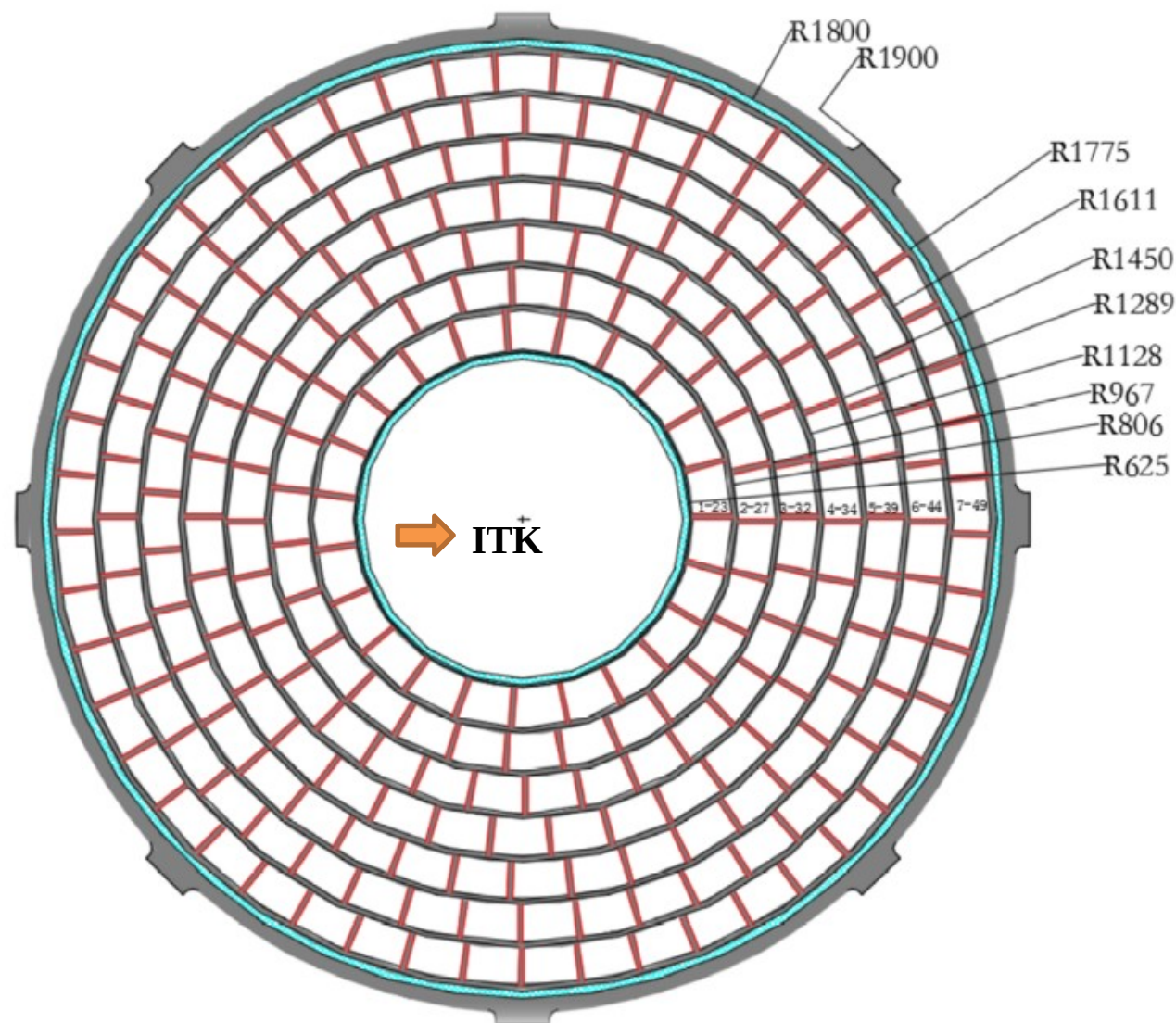
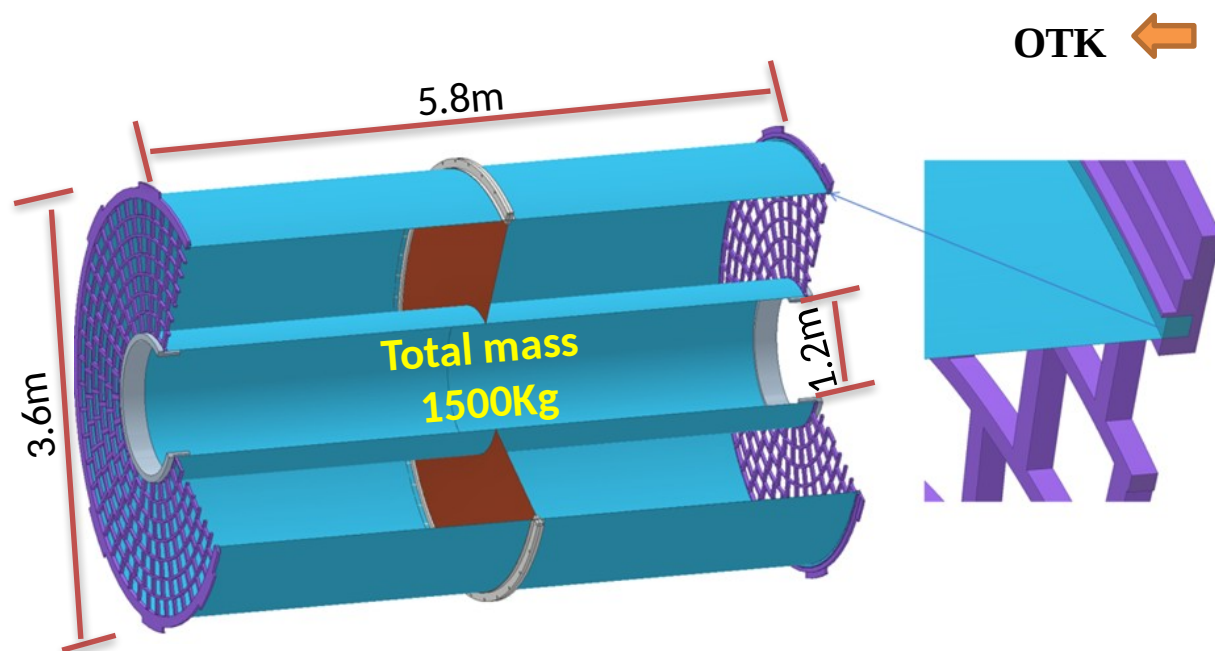


Geometry of the tracking detector system of the CEPC TDR



# Status of TPC in LCTPC and CEPC

TPC detector	Key Parameters
Modules per endcap	248 modules /endcap
Module size	206mm×224mm×161mm
Geometry of layout	Inner: 1.2m Outer: 3.6m Length: 5.9m
Potential at cathode	- 62,000 V
Gas mixture	T2K: Ar/CF <sub>4</sub> /iC <sub>4</sub> H <sub>10</sub> =95/3/2
Maximum drift time	34μs @ 2.75m
Detector modules	Pixelated Micromegas



Detailed design of TPC detector in ref-TDR



## Status of TPC in LCTPC and CEPC

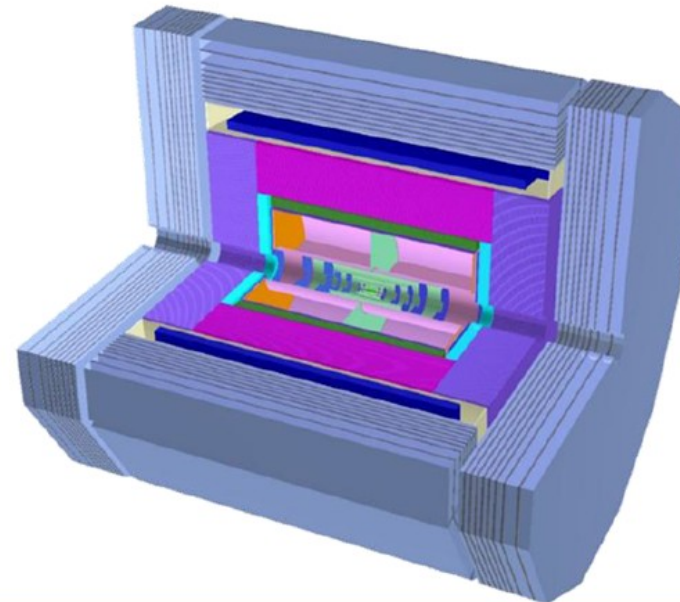
- International Detector Review Committee (IDRC) held its inaugural meeting at IHEP, Oct 21-23, 2024 , to review the status and plan of CEPC Ref-TDR .



# Status of TPC in LCTPC and CEPC

- From January 2024, the CEPC community initiated the technical comparison and selection, balancing factors including **R&D efforts, detector performance, cost, power consumption and construction risks.**

System	Technologies	
	Baseline	For comparison
Beam pipe	Φ20 mm	
LumiCal	SiTrk+Crystal	
Vertex	CMOS+Stitching	CMOS Pixel
Tracker	CMOS SiDet ITrk	
	Pixelated TPC	PID Drift Chamber
	AC-LGAD OTrk	SSD / SPD OTrk
		LGAD ToF
ECAL	4D Crystal Bar	PS+SiPM+W, GS+SiPM, etc
HCAL	GS+SiPM+Fe	PS+SiPM+Fe, etc
Magnet	LTS	HTS
Muon	PS bar+SiPM	RPC
TDAQ	Conventional	Software Trigger
BE electr.	Common	Independent



## Foundations:

- CEPC Instrumentation R&D
- LHC detector upgrade projects
- other HEP experiments
- progress in HEP worldwide R&D
- development in industry



# Status of TPC in LCTPC and CEPC

- The CEPC study group is in process to produce TDR of a reference detector(ref-TDR) by June 2025, aiming mainly for domestic endorsement at one IP of the accelerator.
- CEPC community will **continue to aligned the technologies**, and decide the final detectors within the CEPC international collaborations.

Date	Actions and/or Expectations
Jan 1, 2024	Start the process by comparing different technologies
Jun 30, 2024	Baseline technologies, general geometric configuration and key issues are decided
Oct 31, 2024	Discuss the ref-TDR at the CEPC workshop, report progresses to the CEPC IAC
Dec 31, 2024	The first draft of the ref-TDR is ready for internal reviews
Apr 15, 2025	international review
Jun 30, 2025	The ref-TDR for ready for public reviews
Oct 30, 2025	Submit the ref-TDR for publication

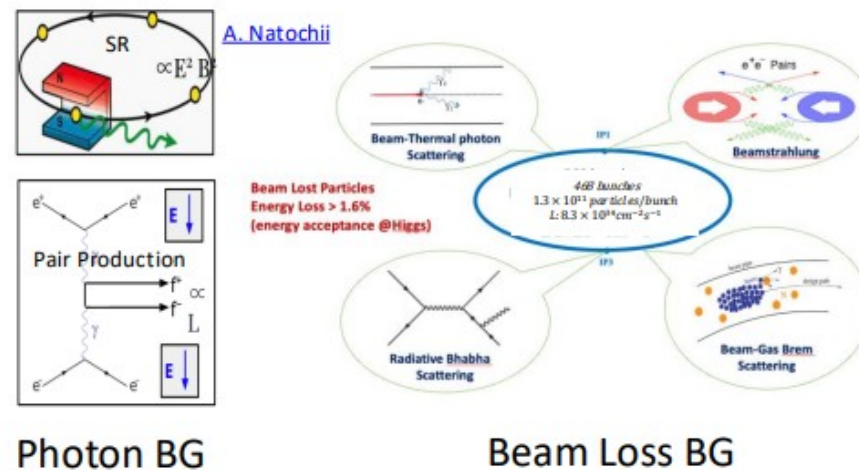
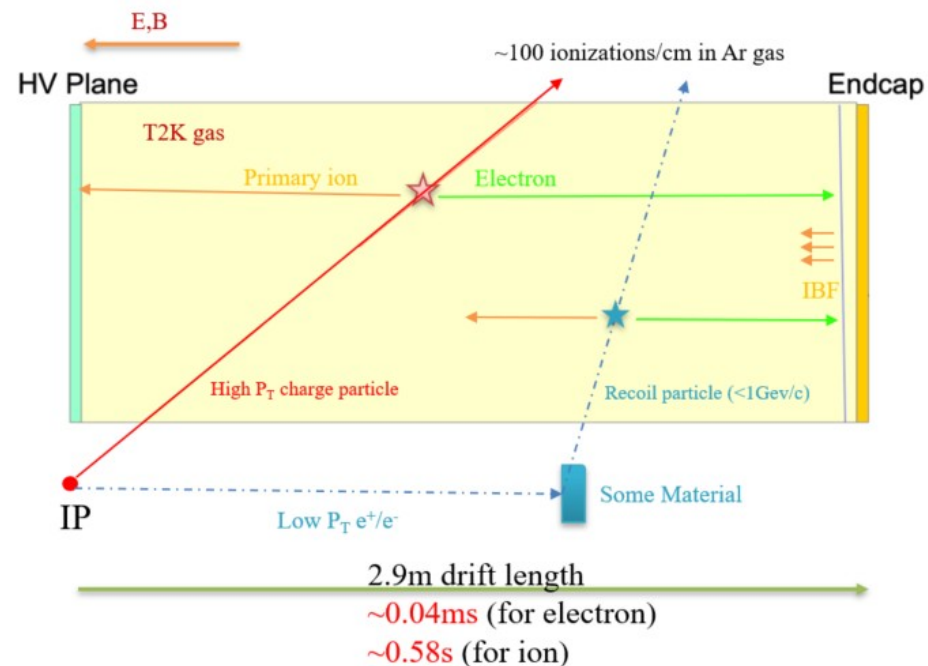
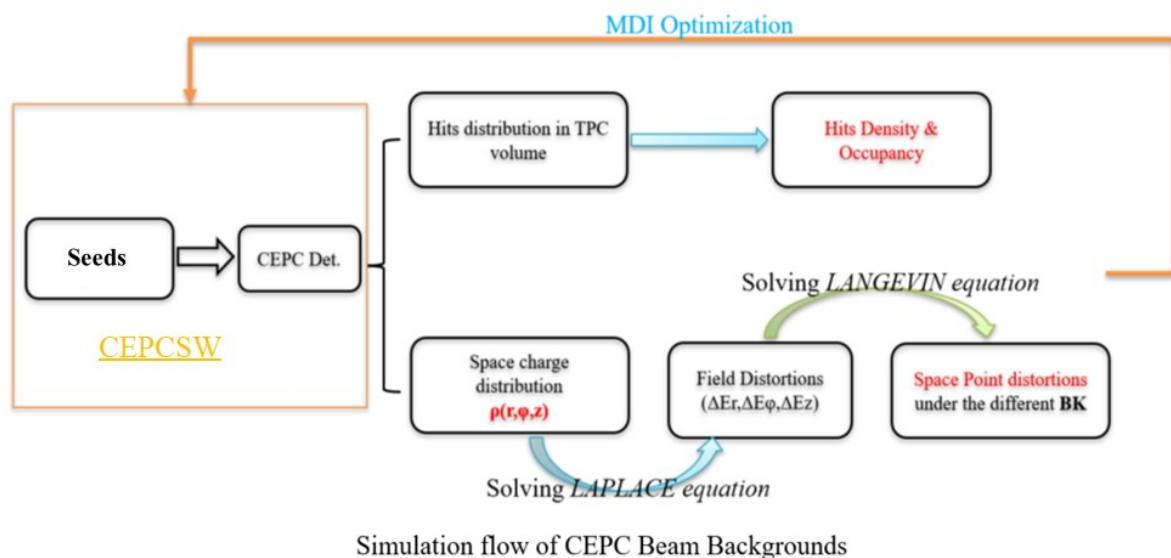
Jianchun Wang's talk  
in CEPCWS at Hangzhou.



- **Pixelated readout TPC for Higgs and Z**

# Pixelated readout TPC for Higgs and Z

- Space charge in TPC chamber
  - Physics events:  $H \rightarrow ss/cc/sb$ ,  $Z \rightarrow qq\dots$  (High  $P_T$ )
  - **Beam background:** (Low  $P_T$ )
    - Beamstrahlung (Luminosity related)
    - Beam-Gas, Beam Thermal Photon, SR...(Single Beam)
    - Injection background
  - IBF at the MPGDs
- Simulation framework



# Background Sources at Higgs @3T

- Higgs Mode background sources
  - I. Pair production (Luminosity related)
  - II. Single Beam (BGB, BGH, Touschek Scatter...)
  - III. Synchrotron Radiation
  - IV. Injection background
- At present, only types I and II backgrounds have been generated.
  - Pair production background is about **two orders of magnitude higher** than the Single Beam.
- For Higgs, it is necessary to **optimize the MDI to shield gamma rays of approximately MeV level.**

**IBF×Gain=1, same primary ion level**

Bkg type	Space charge density(steady)	Remark	Optimization strategy
Pair + Single Beam	$\rho_{sc0} \sim 0.06\text{nC/m}^3$ (R=60cm) 20um, 2.75m Drift Length @ inner radius	Without low $P_T$ e-/e+ (<10MeV) in TPC caused by ~MeV $\gamma$	<b>Acceptable</b>
Pair + <b>Single Beam</b>	$\rho_{sc1} \sim 60 \times \rho_{sc0}$	With low $P_T$ e-/e+ (<10MeV) in TPC caused by ~MeV $\gamma$	Analysis initial position distribution of ~MeV $\gamma$ ( <b>Main contributions</b> ) and Add shielding





# Background Sources at Tera-Z @3T

- Tera-Z Mode background sources

- I. Pair production (Luminosity related)
- II. Single Beam (BGB, BGH, Touschek Scatter...)
- III. Synchrotron Radiation
- IV. Injection background

**IBF×Gain=1, same primary ion level**

- At present, only types I and II backgrounds have been generated.

- At Z-pole, the Single Beam background loss rate increases, with Single Beam being about one order of magnitude higher than Pair.
- Taking into account all MeV-level low-energy gamma rays (**with types III and IV**), the beam background will cause cm-level distortions. **It is necessary to optimize the MDI.**

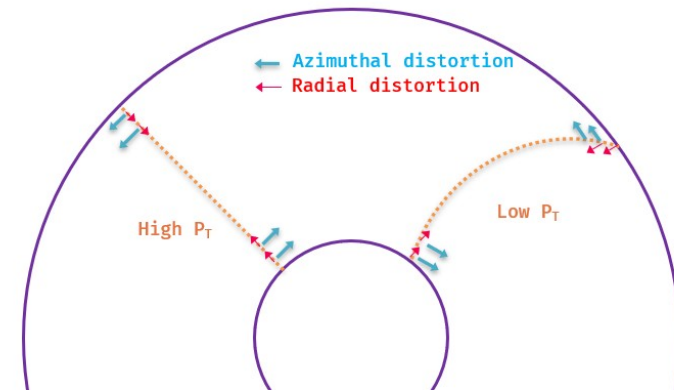
Bkg type	Space charge density(steady)	Remark	Optimization strategy
Pair production	$\rho_{sc0} \sim 0.32\text{nC/m}^3$ (R=60cm) <b>150um</b> , 2.75m Drift Length @ inner radius	Without low $P_T$ $e^-/e^+$ (<10MeV) in TPC caused by $\sim\text{MeV } \gamma$	
Pair + Single Beam	$\rho_{sc1} \sim 15-20 \times \rho_{sc0}$ <b>2500um</b> , 2.75m Drift Length @ inner radius	Without low $P_T$ $e^-/e^+$ (<10MeV) in TPC caused by $\sim\text{MeV } \gamma$	<b>Loss rate control</b>
Pair + Single Beam	$\rho_{sc2} \sim 500-1000 \times \rho_{sc0}$ $\sim\text{cm}$ distortion	With low $P_T$ $e^-/e^+$ (<10MeV) in TPC caused by $\sim\text{MeV } \gamma$	<b>Loss rate control and <math>\sim\text{MeV } \gamma</math> shielding</b>

# TPC distortion caused by primary ions

- Radial distortion ( $\Delta_r$ ) is much smaller than azimuthal distortion, almost imperceptible when along the track for most  $P_T$  track

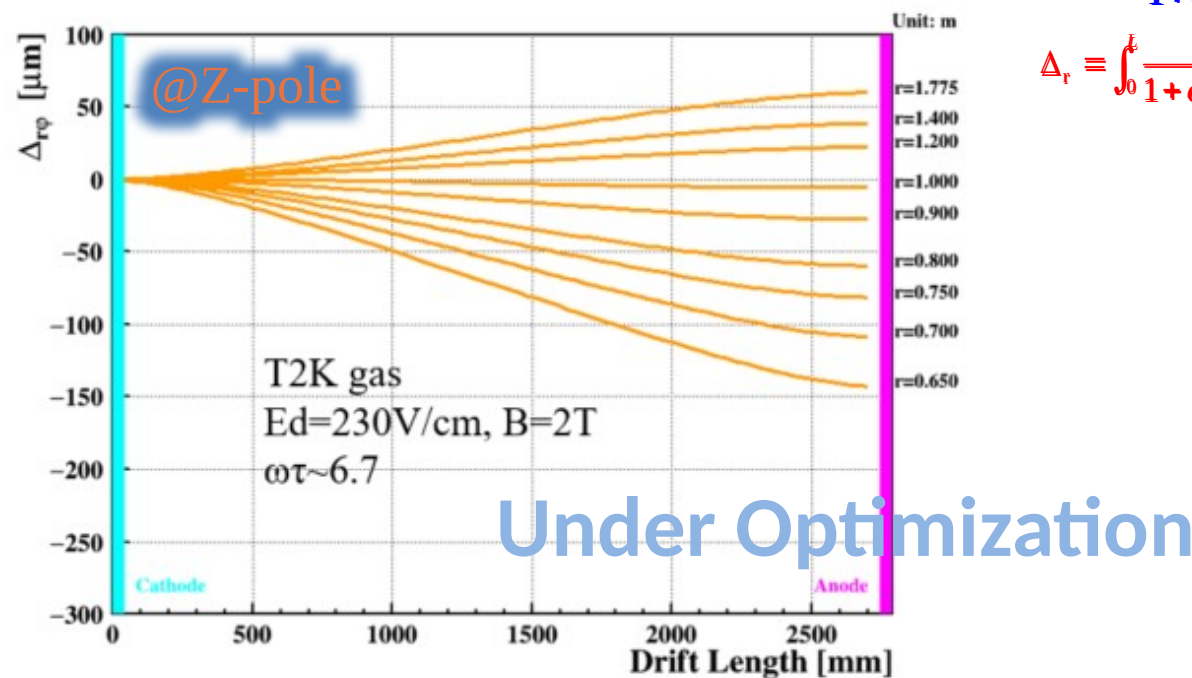
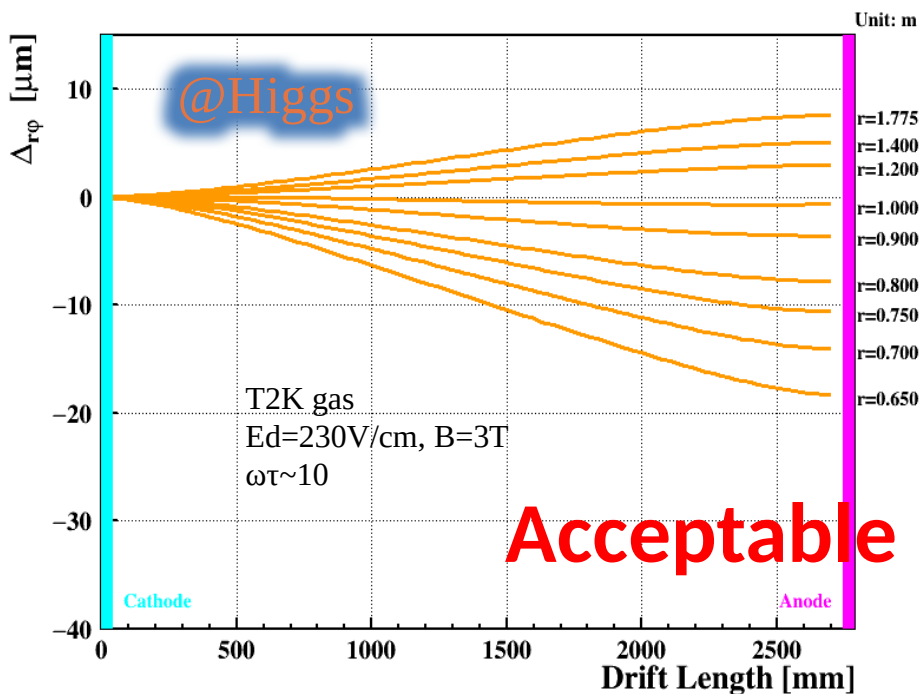
**IBF×Gain=1, same primary ion level**

- Azimuthal distortion ( $\Delta_{r\phi}$ ) has much more serious impact both on high/low  $P_T$  track
- The maximum  $\Delta_{r\phi}$  is  $20\mu\text{m}@$ Higgs (**acceptable**)
- The maximum  $\Delta_{r\phi}$  is  $150\mu\text{m}@$ Z-pole (**need to optimization of MDI**)
  - Including Pair + Single Beam



$$\Delta_{r\phi} = \int_0^L \frac{\omega\tau}{1+\omega^2\tau^2} \times \frac{E_r}{E_z} dz$$

$$\Delta_r = \int_0^L \frac{1}{1+\omega^2\tau^2} \times \frac{E_r}{E_z} dz$$

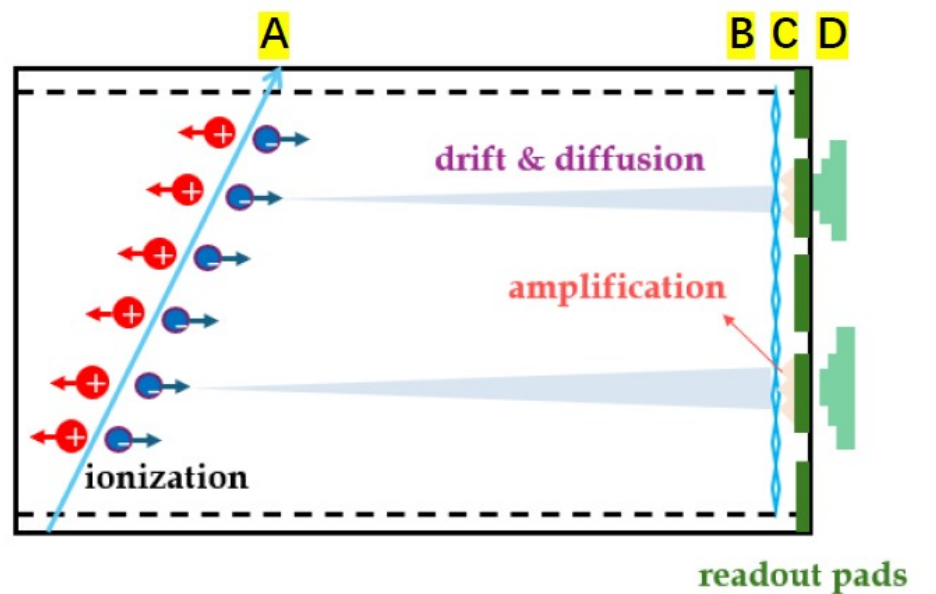


Numerical calculation results of TPC distortion based on Green's function

# Full Simulation of Pixelated readout TPC

## Simulation:

- With the full TPC geometry
- Ionization simulated with Garfield++
- Drift and diffusion from parameterized model based on Garfield++

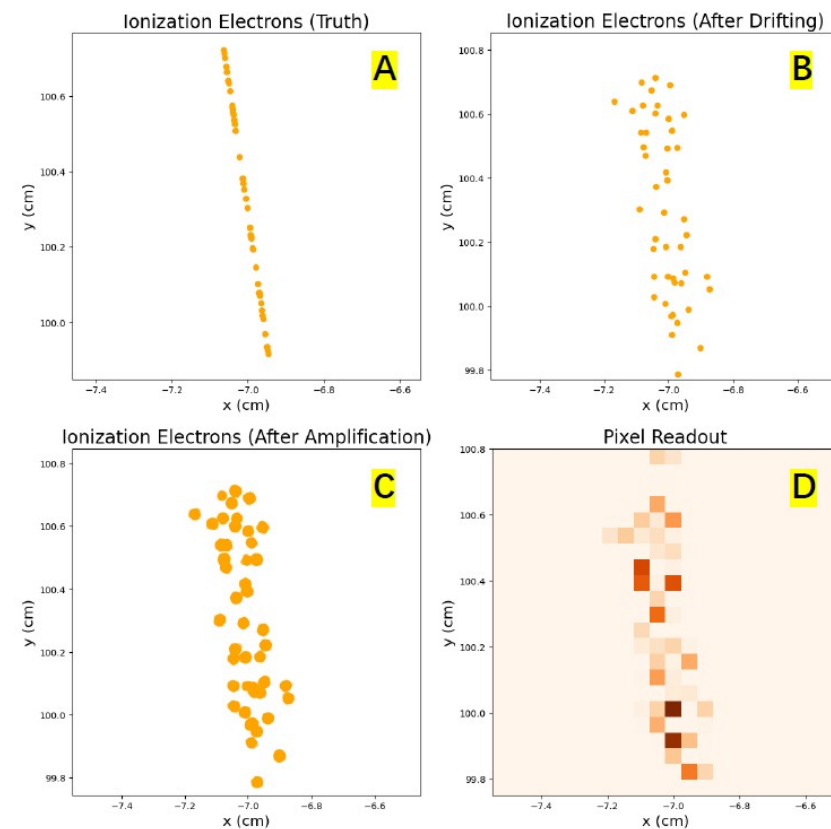


## Garfield++-Based Simulation / Digitization Framework



## Digitization (Refer to the TPC module and prototype):

- Electronic noise: 100 e<sup>-</sup>
- Amplification:
  - Number of electrons: 2000
  - Profile of signal size : 100 $\mu$ m



Simulation of TPC detector under 3T/2T and T2K mixture gas



# Full Simulation of Pixelated readout TPC – Readout size

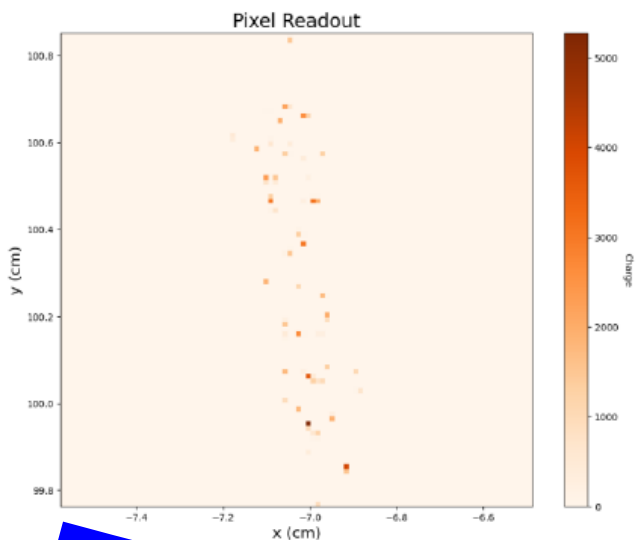
- Simulation of the readouts in pixel sizes
  - Actually, TPX3/4 option existing and the power consumption will be optimized.
  - Optimization started in this ref-TDR at IHEP to meet **Higgs/Z at 3T**.

## ■ Concerning pixel sizes for a TPC

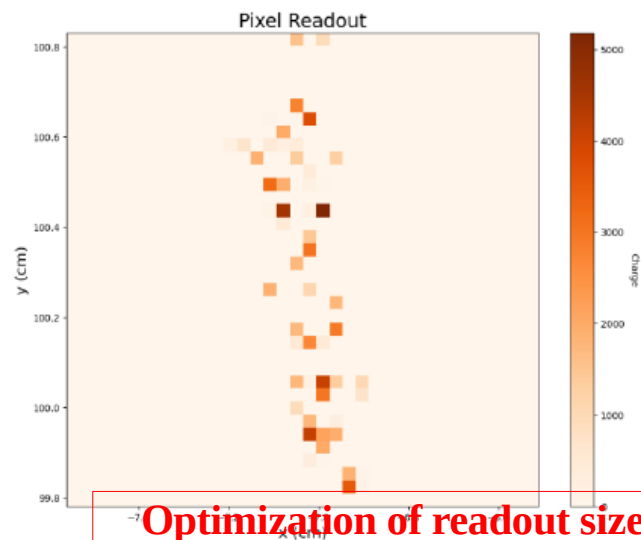
- A pixel size of 55 (110) microns is optimal; one can profit from cluster counting and high precision tracking
- Larger pixel/pad sizes have larger occupancies and one should question whether they can handle the very high beam-beam rate

Peter's comment  
in CEPCWS at Hangzhou.

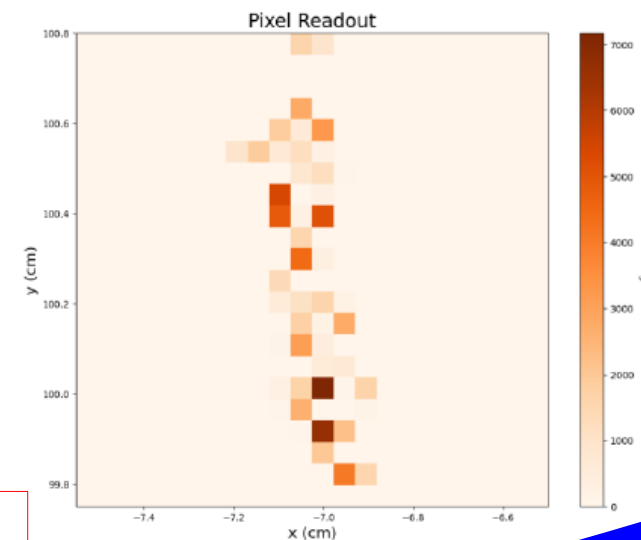
Pixel size = 110 um



Pixel size = 300 um



Pixel size = 500 um



Optimization of readout size  
Balancing of performance, cost  
power consumption, etc.

# Full Simulation of Pixelated readout TPC - Reconstruction

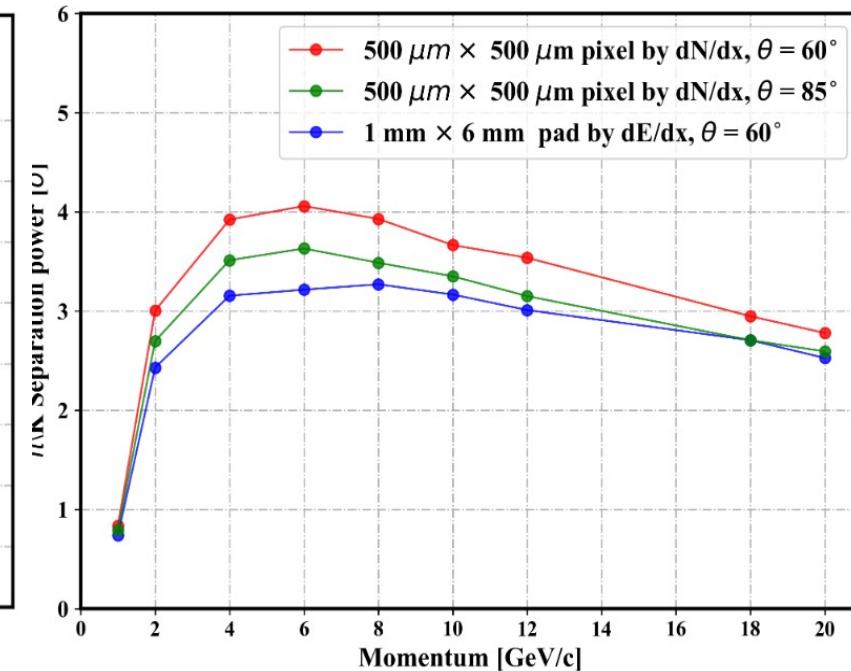
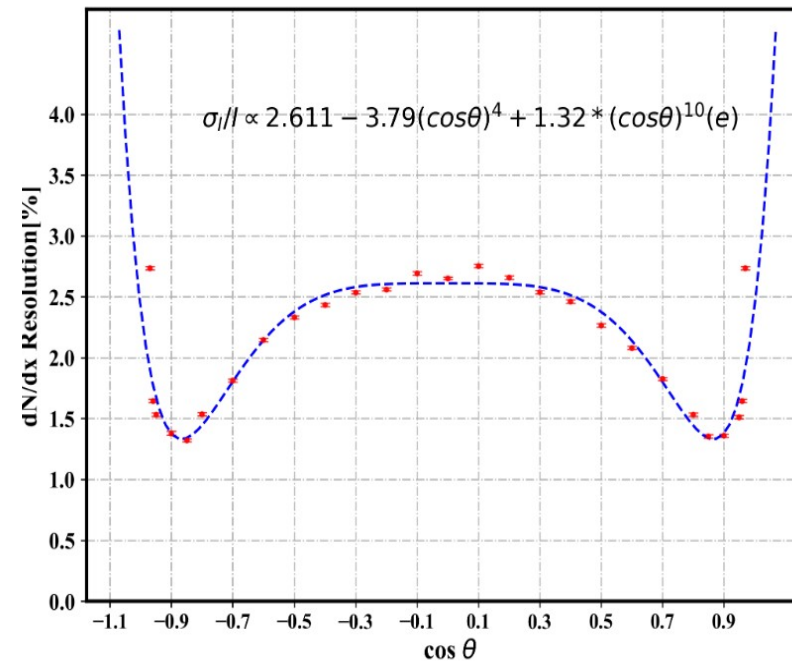
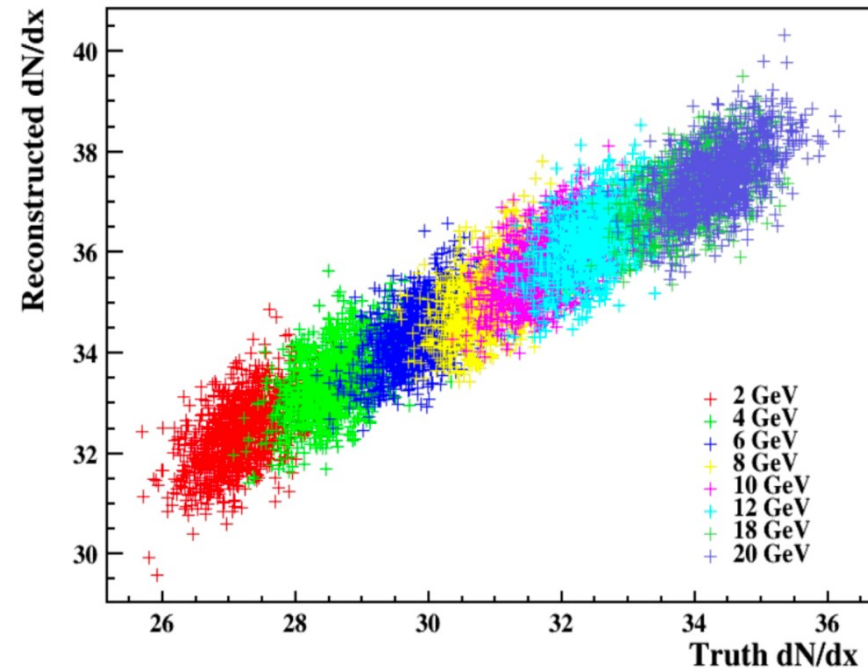
## Reconstruction:

- Reconstruction by counting the number of fired pixels over threshold
- **Reconstruction with good linearity and reliability**

## Preliminary PID performance:

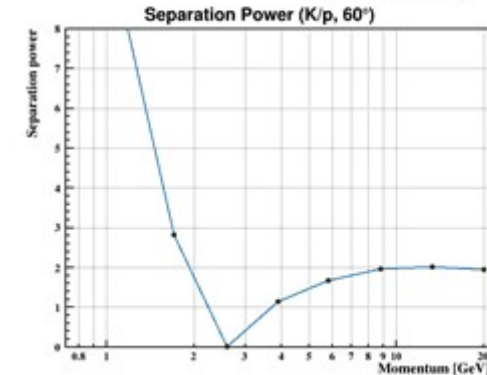
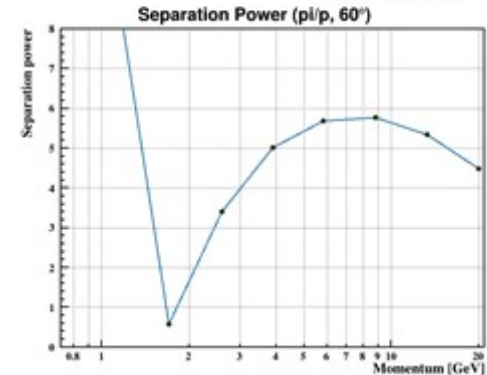
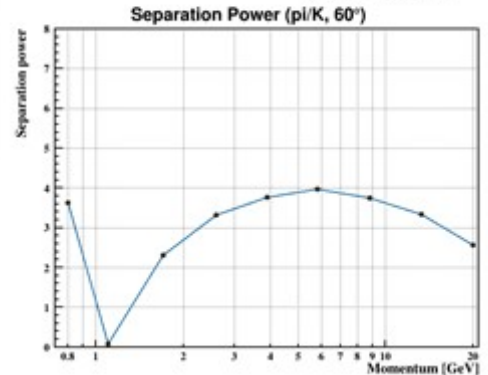
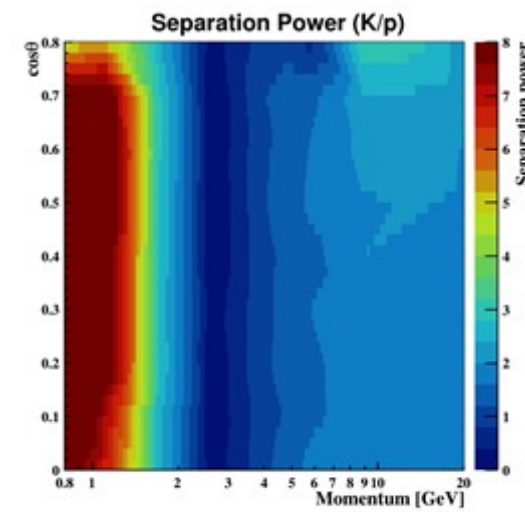
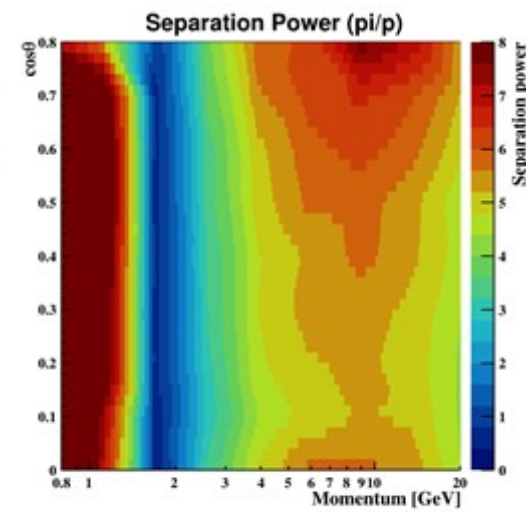
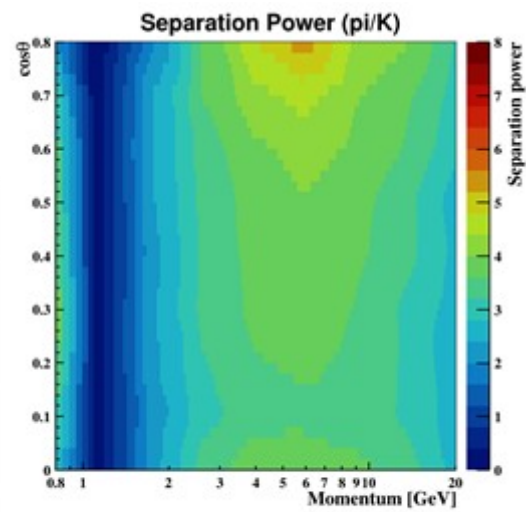
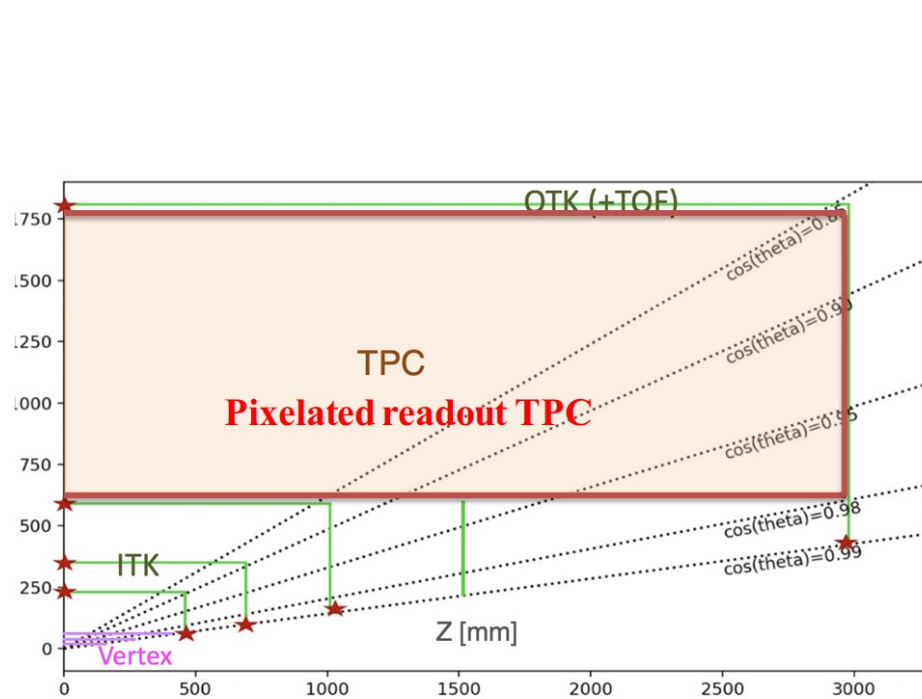
- $\pi/k$  separation power simulation with different momentum

## Separation power:



# Full Simulation of Pixelated readout TPC – PID performance

- Performance of the pixelated readout TPC
  - Simulation of  $\pi/K$ ,  $\pi/p$ , and  $K/p$  separation power with varying momentum and  $\cos\theta$

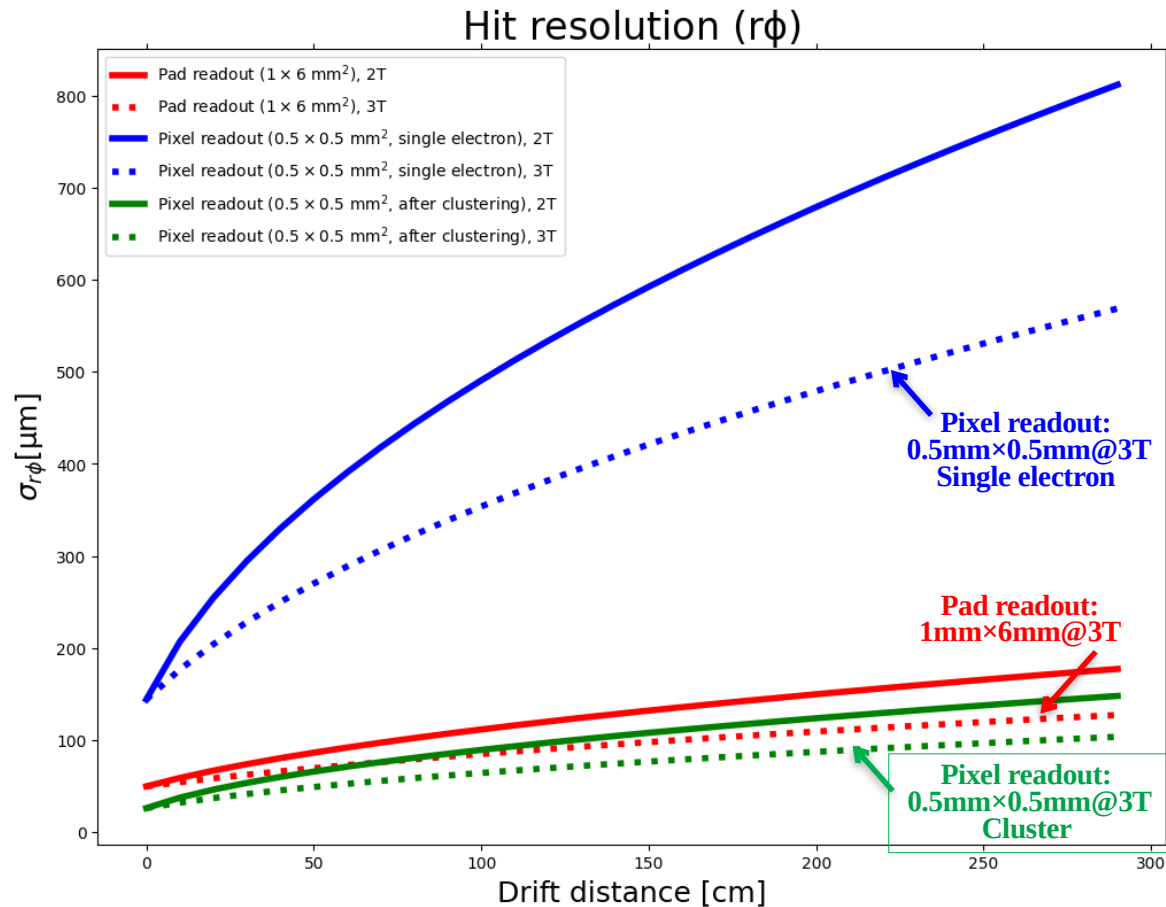




# Full Simulation of Pixelated readout TPC – Spatial resolution

Estimation of the **spatial resolution using pixelated readout**.

- The granularity readout and the transverse diffusion are also taken into consideration.
- TPC can operate effectively at 3T B-field.
- Pixelated readout TPC can achieve superior spatial resolution at 3T compared to 2T.



Pad readout:

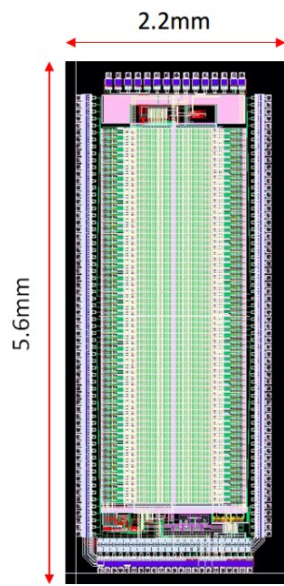
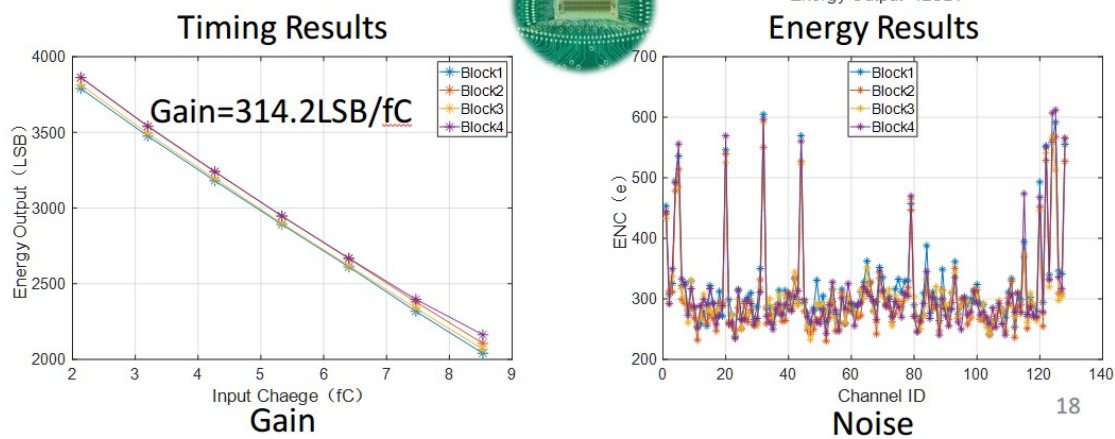
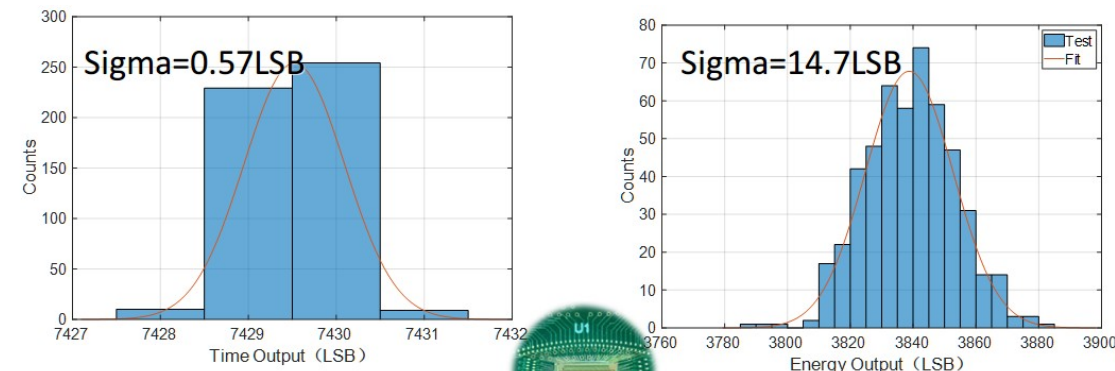
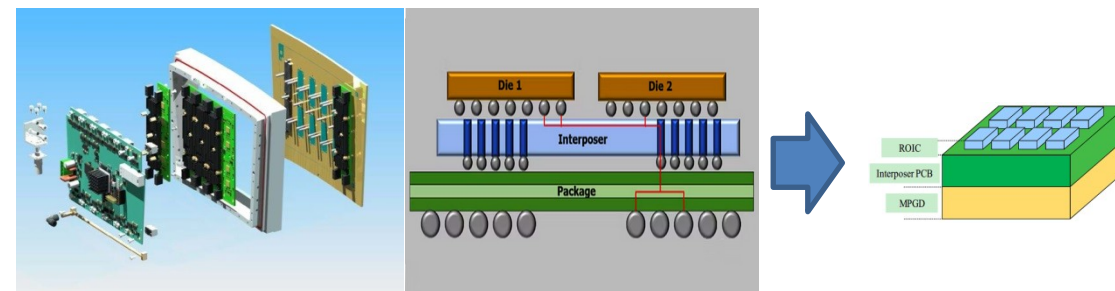
$$\sigma_{r\phi}^{\text{pad}} = \sqrt{(\sigma_{r\phi 0}^{\text{pad}})^2 + \sigma_{\phi 0}^2 \sin^2(\phi_{\text{track}}) + L \frac{D_{r\phi}^2}{N_{\text{eff}}} \sin(\theta_{\text{track}})}$$

Pixel readout:

$$\sigma_{r\phi}^{\text{pixel}} = \sqrt{(\sigma_{r\phi 0}^{\text{pixel}})^2 + LD_{r\phi}^2}$$

# Full Simulation of Pixelated readout TPC – **TEPix with 500 $\mu\text{m}$ $\times$ 500 $\mu\text{m}$**

- Pixelated Readout Electronics: TEPix development
  - Multi-ROIC chips + Interposer PCB as RDL
  - Four-side bootable
- TEPix: Low power Energy/Timing measurement
  - Low power consumption: 0.5mW/ch@2nd Chip
  - Timing: 1 LSB(<10ns)
  - Noise: 300e- (high gain)

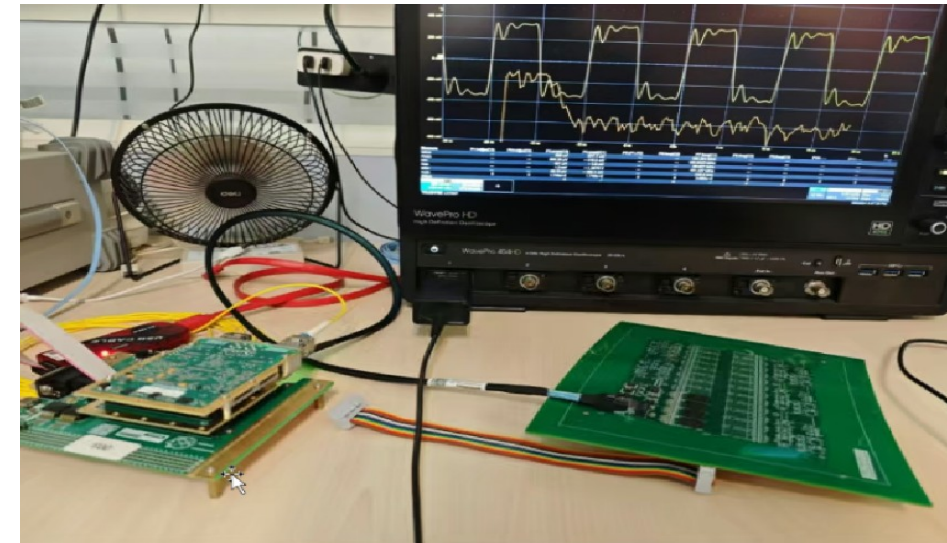
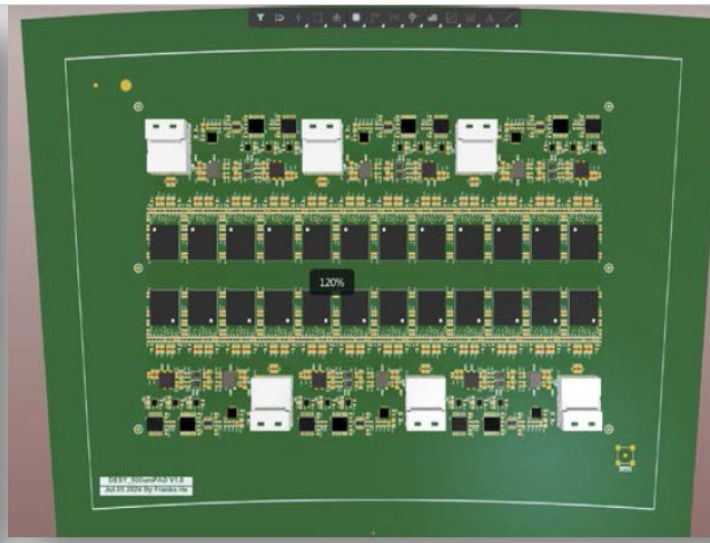
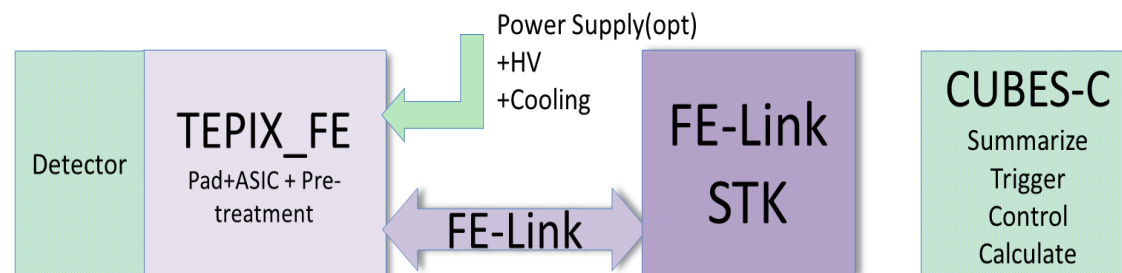


Parameter	Spec
Number of channels	128
Power Consumption	Analog<30mW
	Digital<30mW
ENC	~300 e(high gain)
Dynamic Range	25fC(high gain)
	150fC(low gain)
INL	<1%
Time Resolution	<10ns

FEE ASIC: TEPix—Test Results in May

# Validation and commissioning of TPC prototype

- **R&D on Pixelated TPC readout for CEPC TDR.**
  - ASIC chip developed and **2<sup>nd</sup> prototype wafer has been done** and tested.
  - The TOA and TOT can be selected as the initiation function in the ASIC chip
- **Beam test of the pixelated readout TPC prototype in preparation. (January and March, 2025)**

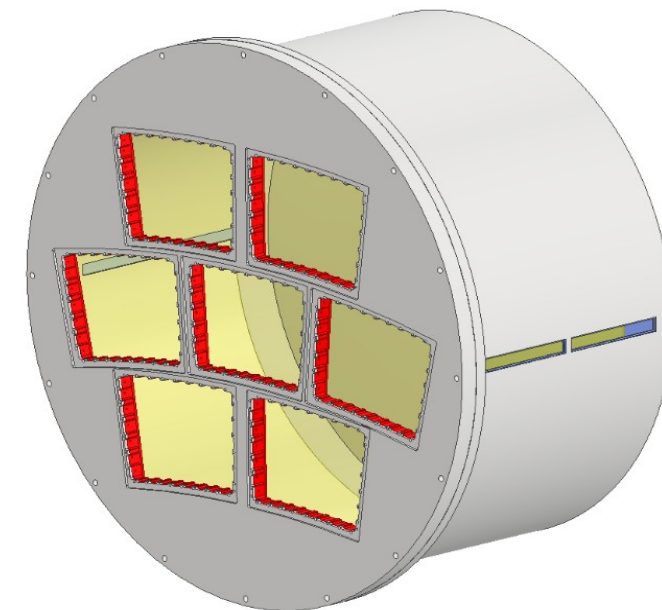


Photos TPC modules assembled for the beam test



# Work plan

- **Short term work plan (before June, 2025)**
  - Optimization of TPC detector for CEPC ref-TDR
  - Prototyping R&D and validation with the test beam
    - mechanics, manufacturing, beam test, full drift length prototype
  - Performance of the simulation and Machine Learning algorithm
- **Long term work plan (next 3-5 years)**
  - Development of TPC prototype with low power consumption FEE
    - Collaboration with LCTPC collaboration on beam test
  - Development of the full drift length prototype
    - Drift velocity. Attachment coefficient, T/L Diffusion, etc.



Milestones achieved	Before June, 2025	Beyond TDR
Ion backflow suppression	<b>IBF×Gain&lt;1 (Gain=2000)</b>	<b>Graphene technology</b>
Pixelated readout prototype	<b>Validation with beam test</b>	<b>Prototype with Multi-modules</b>
Power consumption ASIC	<b>~100mW/cm<sup>2</sup> (60nm ASIC)</b>	<b>Optimization 330μm - 500μm</b>
PID resolution	<b>3% (dN/dx)</b>	<b>&lt;3% (dN/dx)</b>
Material budget (barrel)	<b>Carbon Fiber</b>	<b>Full size prototype</b>

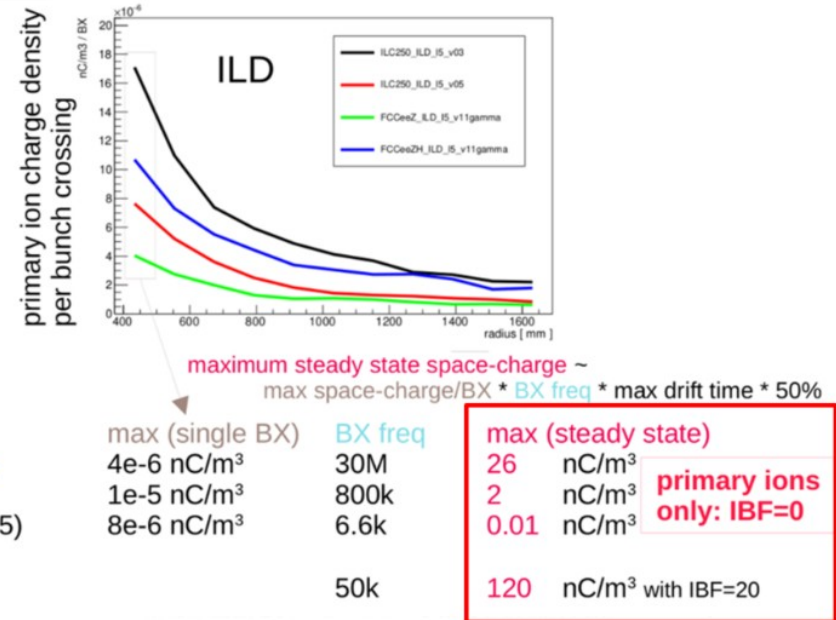
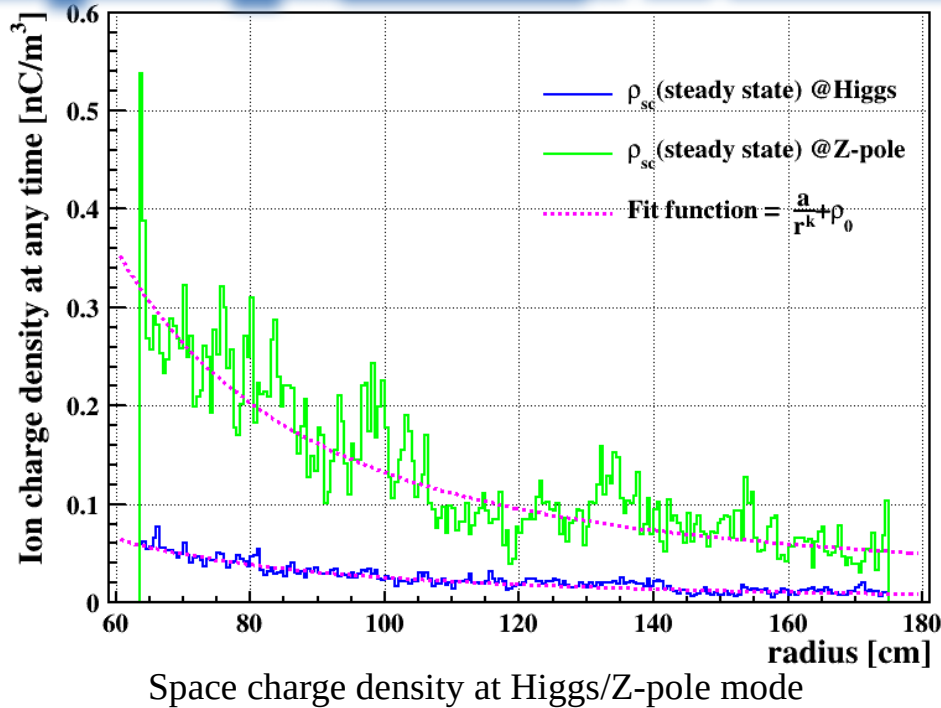
- In LCTPC collaboration, TPC detection technology R&D using the pad readout towards the pixelated readout for Higgs and Z run at the future  $e^+e^-$  collider.
- Pixelated TPC is chosen as the baseline gaseous tracker in CEPC ref-TDR. The simulation results show that both of PID performance and the momentum resolution are good. Validation with TPC prototype in preparation before TDR.
- Synergies with CEPC/FCCee/EIC/LCTPC allow us to continue R&D and ongoing with the significant international collaboration. All of contributions will input to CEPC ref-TDR in next few months.

**Many thanks!**



# Backup1: Space charge density in TPC (only Pair)

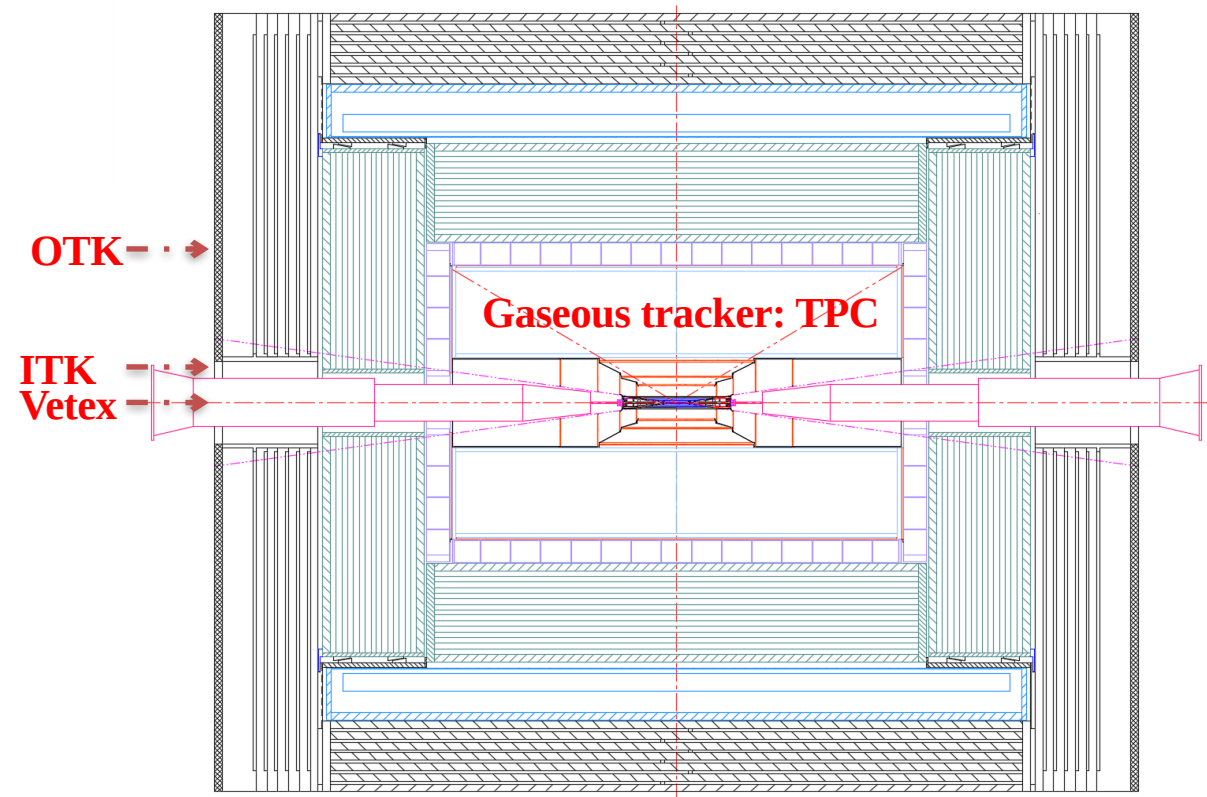
- ◆ Number of primary ions can be calculated by Edep/effective ionization potential of Ar [26 eV]
- ◆ The ion charge density at any time is given by: ~0.55s assuming 5m/s ion drift velocity Ref Daniel Jeans' slides
  - ◆  $\rho_{sc} \sim$  primary ions/BX  $\times$  BX frequency  $\times$  max drift time of ion  $\times$  50% [ion already reached cathode]
  - ◆ BX frequency: 1/23ns @Z-pole, 1/355ns @Higgs
- ◆ The space charge in CEPC TPC is ~50 $\times$  smaller than FCCee, ref KEK *Daniel Jeans* simulation results



[Beam background in a TPC at a circular collider](#)

# Large gaseous TPC in ILD-like concept

- The detector adopts a hybrid tracking system in ILD-like concept.
- **Large gaseous tracker: TPC**
  - PID of charged hadrons:
    - Benefit flavor tagging and jet substructure study
    - Reduce combination background
  - Quasi-continuous tracking: track finding
  - Ultra light material budget
    - Improved performance at the low momentum ( $<15\text{GeV}/c$ )
    - The total materials ( $\sim 0.65\% X_0$ ) is equivalent to **about 1 layer of a silicon tracker detector**.



Schematic diagram of the detector