

Mini-Workshop on "Design and Technical Challenges of the ILC Small-Angle Interaction Region"

Report of Contributions

Contribution ID: 0

Type: **not specified**

Welcome

Friday 20 October 2006 09:30 (5 minutes)

Session Classification: Morning Session: Review of Design and Hardware Challenges

Contribution ID: 1

Type: **not specified**

ILC Interaction Regions: the GDE perspective

Friday 20 October 2006 09:35 (25 minutes)

Presenter: Mrs ANGAL-KALININ, Deepa (CCLRC Daresbury)

Session Classification: Morning Session: Review of Design and Hardware Challenges

Contribution ID: 2

Type: **not specified**

Head-on Scheme: Design and Key Hardware Challenges

Friday 20 October 2006 10:00 (25 minutes)

Presenter: Dr NAPOLY, Olivier (CEA)

Session Classification: Morning Session: Review of Design and Hardware Challenges

Contribution ID: 3

Type: **not specified**

2 mrad Scheme: Design and Key Hardware Challenges

Friday 20 October 2006 10:25 (25 minutes)

Presenter: BAMBADÉ, Philip (LAL)

Session Classification: Morning Session: Review of Design and Hardware Challenges

Contribution ID: 4

Type: **not specified**

SC magnet developments at LBL

Friday 20 October 2006 11:20 (25 minutes)

Presenter: Dr SABBI, Gian Luca (LBL)

Session Classification: Morning Session: Review of Design and Hardware Challenges

Contribution ID: 5

Type: **not specified**

SC magnet developments at CEA-Saclay

Friday 20 October 2006 11:45 (30 minutes)

Presenters: Mrs FELICE, Helene (CEA); Mrs DURANTE, Maria (CEA)

Session Classification: Morning Session: Review of Design and Hardware Challenges

Contribution ID: 6

Type: **not specified**

Electro-static separator limits and R&D

Friday 20 October 2006 12:15 (25 minutes)

Presenter: Dr BORBURGH, Johannes (CERN)

Session Classification: Morning Session: Review of Design and Hardware Challenges

Contribution ID: 7

Type: **not specified**

Permanet magnets

Friday 20 October 2006 12:40 (20 minutes)

Presenter: Dr IWASHITA, Yoshihisa (Kyoto University)

Session Classification: Morning Session: Review of Design and Hardware Challenges

Contribution ID: 8

Type: **not specified**

Discussion on Technical Solutions

Friday 20 October 2006 14:00 (45 minutes)

Session Classification: Afternoon Session: Hardware Challenges

Contribution ID: 9

Type: **not specified**

Discussion on Harware R&D Plans

Friday 20 October 2006 14:45 (45 minutes)

Session Classification: Afternoon Session: Hardware Challenges

Contribution ID: **10**

Type: **not specified**

Future design progress and design plans: organisation of task

Friday 20 October 2006 16:00 (45 minutes)

Session Classification: Afternoon Session: Hardware Challenges

Contribution ID: 11

Type: **not specified**

Concluding Remarks

Friday 20 October 2006 16:45 (15 minutes)

Session Classification: Afternoon Session: Hardware Challenges