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Development of a Pixel Based TPC

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Abstract: The Bonn TPC group is working on the development of a TPC readout module based on the TimePix chip and a triple GEM gas amplification structure. The TimePix chip has a pixel size of 55 by 55 square microns and allows to determine either the amount of charge arriving on one pixel or the arrival time of the charge. Operating the pixels alternating in either mode in a checkerboard pattern allows to obtain all the information required for a three dimensional track reconstruction.

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