

CALICE week at Prague - AGENDA

Wednesday 12 September 2007

VFE & DAQ (09:00 - 12:30)

time	[id] title	presenter
09:00	[30] R&D on SiW VFE in Clermont	MANEN, Samuel
09:20	[50] LOw power and high speed 12 bits pipeline ADC	DANIEL DZAHINI (LPSC GRENoble)
09:40	[31] R&D on ECAL Calibration ASIC at LPSC	VESCOVI, Christophe
10:00	[32] Status of ROC chips	JULIEN FLEURY (IN2P3-LAL ORSAY)
10:20	[33] ASU and standalone test setup	REMY CORNAT (IN2P3-LPC CLERMONT)
10:40	break	
11:00	[34] DAQ status and DIF	HOMMELS, Bart
11:20	[44] DIF working group and DIF for DHCAL	JULIE PRAST (LAPP ANNECY)
11:30	[45] DIF for AHCAL	MATHIAS REINECKE (DESY) PRESENTED BY PETER GOETTLICHER
11:40	[46] DIF for ECAL	REMY CORNAT
11:50	[56] ECAL DIF design	GOODRICK, Maurice
12:10	[48] DAQ software	TAO WU