

VI SiLC Collaboration Meeting

Monday, 17 December 2007

R&D on mechanics Prototypes and starting of sensor R&D session - Aula Magna (10:00 - 14:30)

time	[id] title	presenter
10:00	[0] Construction of Si Modules: the CMS experience	L.DEMARIA
10:30	[1] The LCTPC + Silicon Envelop prototype	S.HAENSEL(HEPHY)
10:50	[2] Thermal and mechanical issues in the design of thin CMOS MAPS module for SVT for the SuperB factory	F.BOSI (INFN-PISA)
11:20	Coffee break	
11:40	[3] Alignment status report	I.VILA (IFCA/CSIC)
12:00	[4] The 4-layers Prototype and related integration issues	G.DAUBARD/A. SAVOY-NAVARRO (LPNHE)
12:20	[5] Silicon Strip Sensor R&D and results from HPT sensor measurements	TH. BERGAUER (HEPHY)
12:40	[6] Sensor design for dual metal layer readout	M.DRAGICEVIC (HEPHY)
13:00	Lunch break	