

VI SiLC Collaboration Meeting

Tuesday, 18 December 2007

Tests bench and Beam tests - Aula Magna (14:00 - 18:20)

time	[id] title	presenter
14:00	[83] The SiD Concept and towards a SiD LOI	Y.KARIOTAKIS (LAPP)
14:40	[84] Talk on Microelectronics (tba)	A.MARCHIORO(CERN)
15:10	[85] CERN test beam results	W.DASILVA(LPNHE),P.KVASNI CKA(CU PRAGUE)
15:40	[86] New DAQ for beam tests (hard&software)	J.SANCHEZ-GONZALEZ(IFCA) /J.DAVID(LPNHE)
16:10	Coffee break	
16:30	[87] The LSTFE Front end status	B.SHUMM(SCIPP&UCSC) (VRVS)
16:50	[88] Test at CERN in 2008:proposal & goals	A.SAVOY-NAVARRO(LPNHE)
17:10	[89] Test facility at FNAL:Review of FNAL Silicon activities for ILC	M.DEMARTEAU(FNAL)
17:40	[90] The 4th Concept	G.P.YEH(FNAL)