VI SiLC Collaboration Meeting

Tuesday, 18 December 2007

R&D on Electronics - Aula Magna (09:00 - 14:00)

time [id] title	presenter
09:00 [65] What did we learnt from SiTr 130-v1-2?	T.H.PHAM(LPNHE)
09:30 [66] SiTr_130-128present status and prospects. Part1(analog)	J.F.GENAT(LPNHE)
09:50 [67] SiTr_130-128present status and prospects. Part2(digital)	A.COMERMA(BARCELONA U.)
10:10 [68] FE chip connection to the strips: from FE boards to on-sensor wiring	TBS(LPNHE)
10:30 [69] The beyond FE electronics chain	TBA(B.U.)-TBA(LPNHE)
11:00 Coffee break	
11:30 [70] Reference Electronics: VA1 vs APV25	J.DAVID(LPNHE)-TBA(HEPHY)
11:50 [71] The beam test at CERN in October 2007	J.DAVID(LPNHE)-M.FERNAND EZ(IFCA)
12:15 [72] Building a SiLC pixel telescope for T.B.'s	DISCUSSION
12:30 Lunch break	