

CALICE week at Argonne

Tuesday, 18 March 2008

ECAL W-Si (09:00 - 10:45)

time	[id] title	presenter
09:00	[27] SiW Prototype - Status before Fermilab	Mr REINHARD, Marcel
09:30	[28] Status of EUDET Module - An attempt to give/get an overview	POESCHL, Roman
10:00	[29] Silicon R&D for EUDET Module	MORISSEAU, Francois
10:25	[52] Mechanical R&D Ecal	GRONDIN, Denis Pierre