

electronics for technological prototypes EUDET and other

Monday, 2 June 2008

ECAL electronics (10:30 - 13:00)

time	[id] title	presenter
10:30	[0] Introduction	C DE LA TAILLE
10:40	[1] SKIROC ADC measurements and cyclic ADC development at Clermont	LAURENT ROYER (LPC CLERMONT)
11:00	[2] SKIROC measurements at Orsay and PCB developments	Mr FLEURY, Julien
11:20	[3] results on 12bit 25Mhz pipeline ADC	DANIEL DZAHINI (LPSC GRENOBLE)
11:30	[4] results on 16bit sigma delta DAC for calibration	DZAHINI, Daniel
11:40	[11] digital interface inside ASICs	DULUCQ, Frederic
12:00	[12] ECAL slab interconnect	MAURICE GOODRICK (U. CAMBRIDGE)
12:20	[13] ECAL DIF	BART HOMMELS (U CAMBRIDGE)
12:40	[20] ECAL next steps	CORNAT, remy