

Session Program

2 July 2008

7th SiLC Meeting at CERN

Electronics R&D

CERN

Wednesday 2 July

09:40

Electronics R&D

Session | **Location:** CERN

09:40–10:05

Status report on the new chip

Speaker

LPNHE+U.B

10:05–10:20

Development of the new DAQ-DUT hardware

Speaker

Jacques David + Alexandre Charpy (LPNHE)

10:20–10:30

The new VA1 boards

Speaker

Jacques David-LPNHE

10:30–10:50

Discussion for the next steps

10:50