

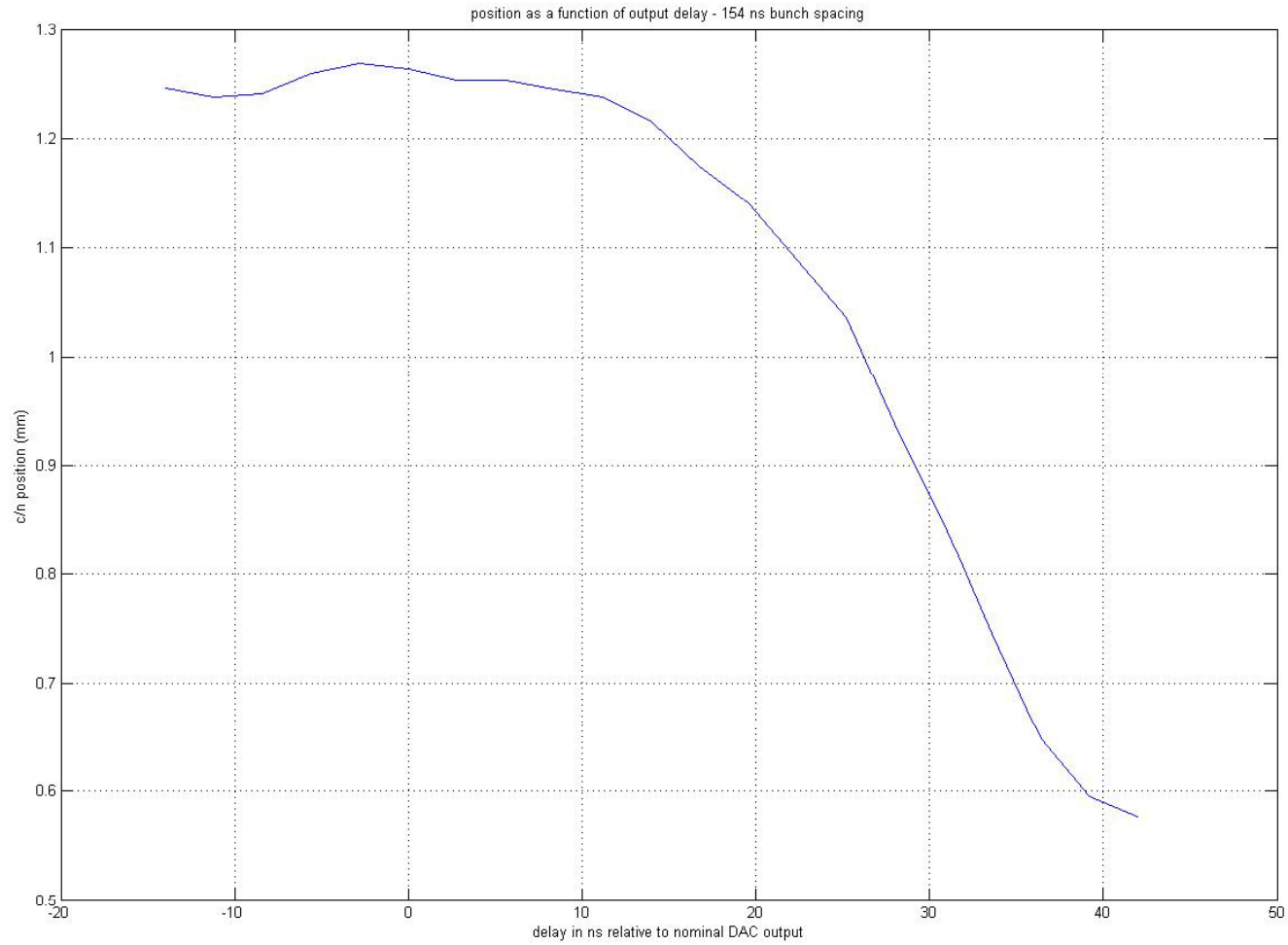
Comparison of amplifier rise-time with historical data

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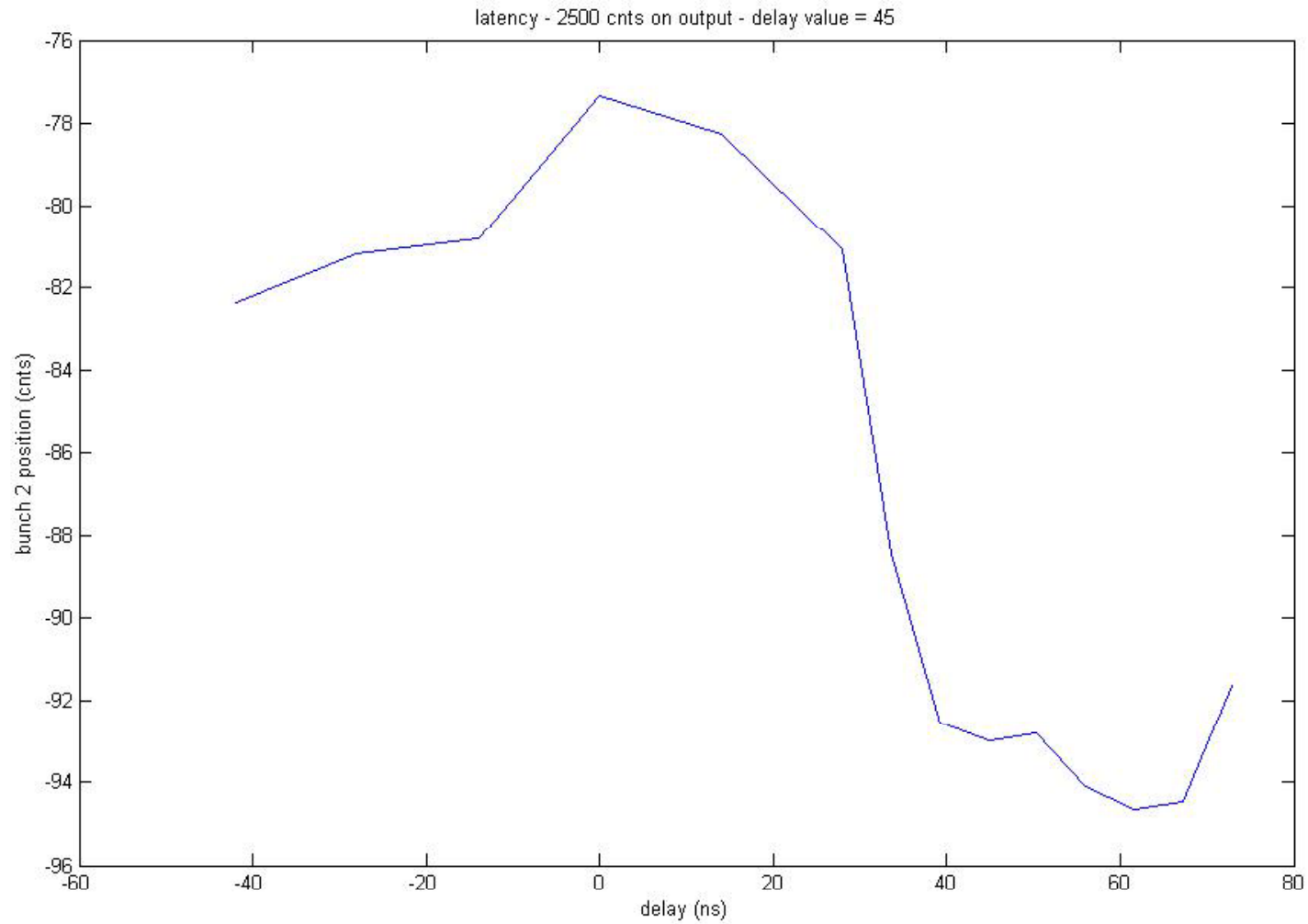
16 June 2008

- Aim to compare present rise-time of the amplifier with previous latency measurements and current monitor data (May 07)
- Latency:
 - Dec06 and Feb07 overdriving amplifier during latency scan
 - Feb07 estimated ~135 ns latency with 4(5) clock cycles in FPGA

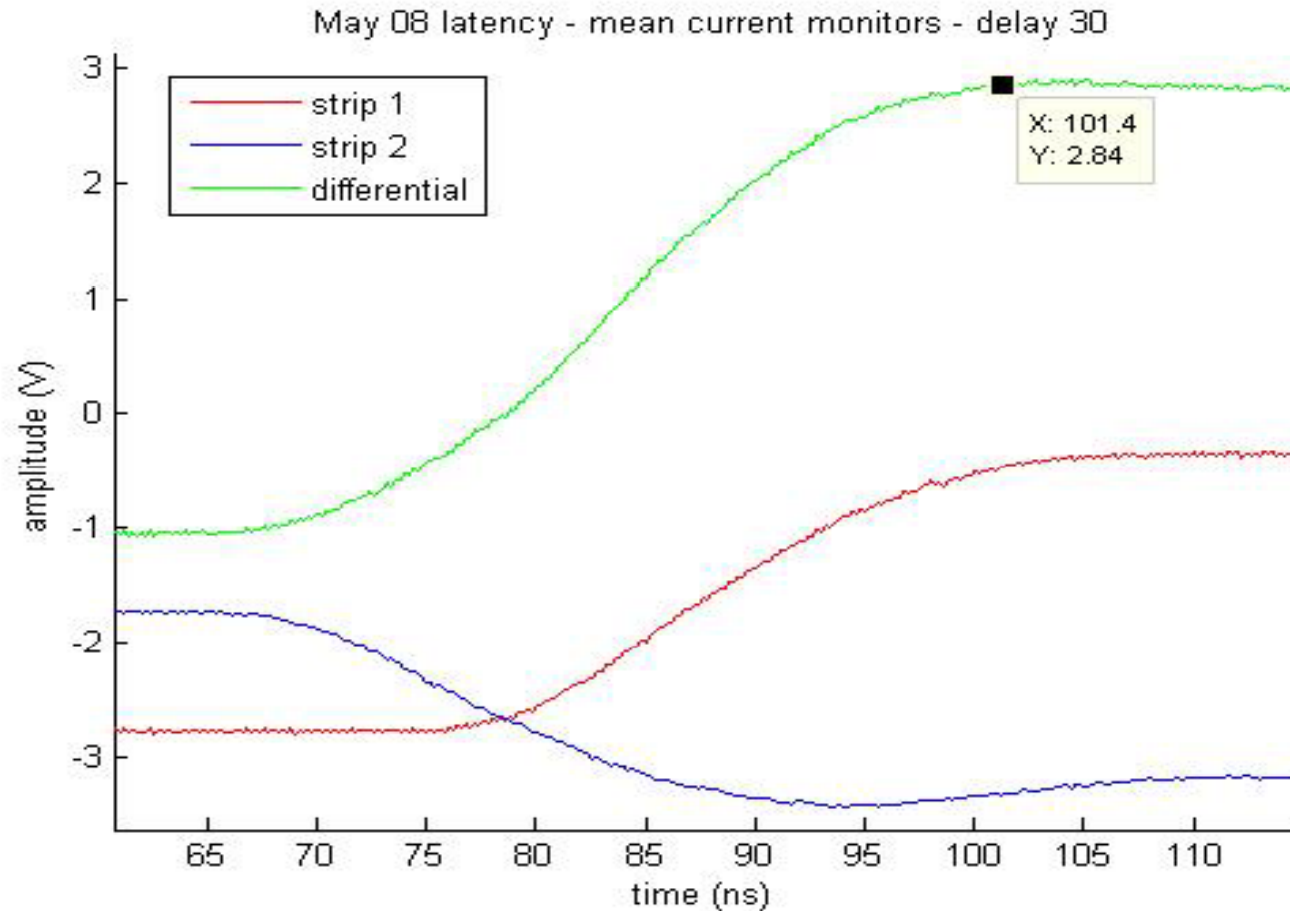
May07 Latency



May08 Latency – ILA (rough)



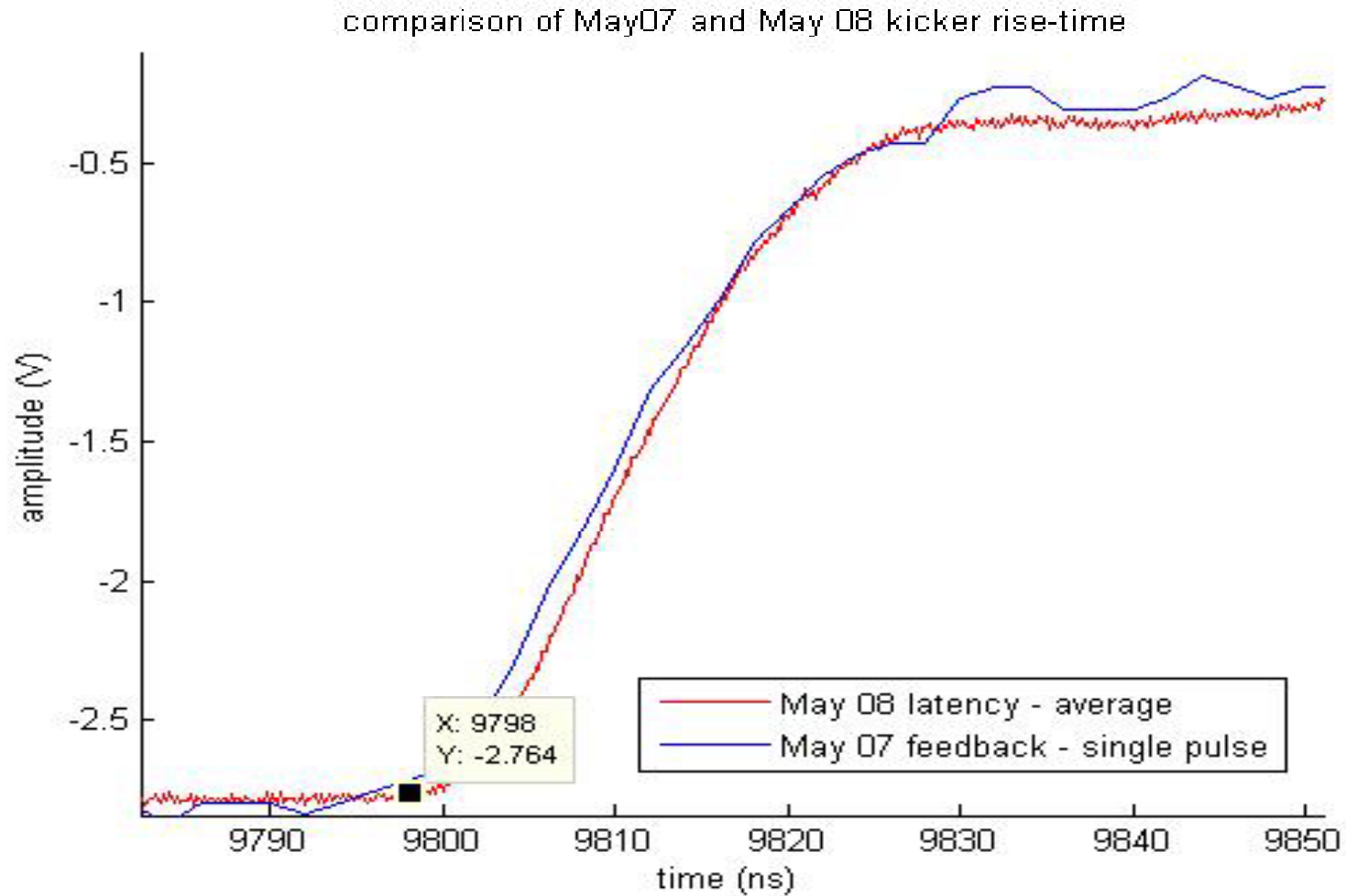
Current Monitors (May08)



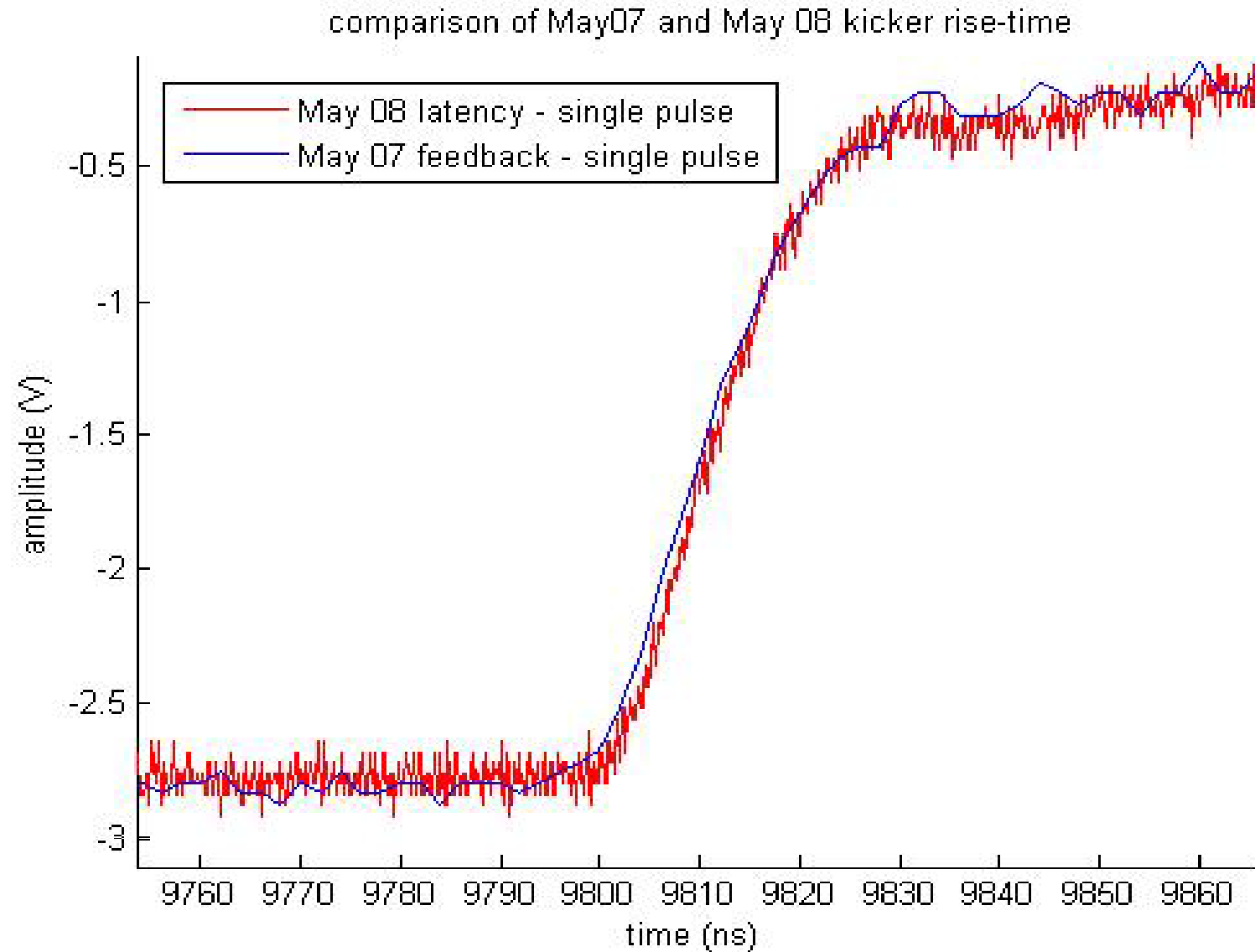
-1 V across strips with feedback off? Bending magnet?

Overdriving in strip2?

May07-08 comparison (1)



May07-08 comparison (2)



conclusions

- No difference between kicker rise-time now and last year
- Latency measurement seems to be okay
- Perhaps overdriving amp slightly in recent measurement – skews the measurement