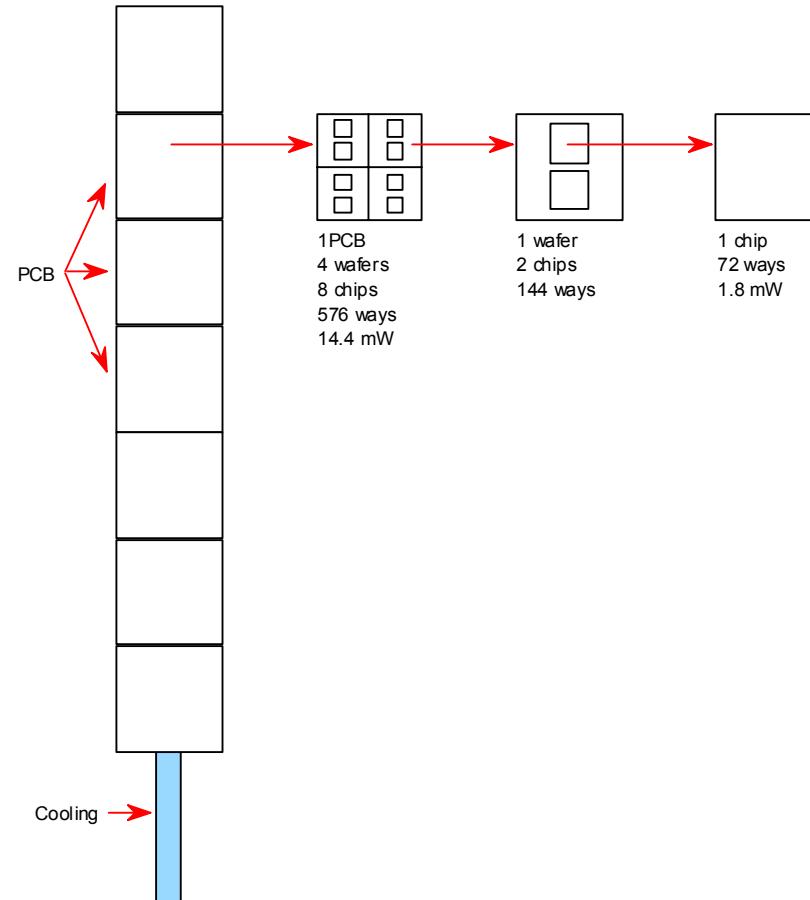


Realization of slab model, to simulate the heat dissipation
of chips in a slab EUDET
in order to validate the future cooling system



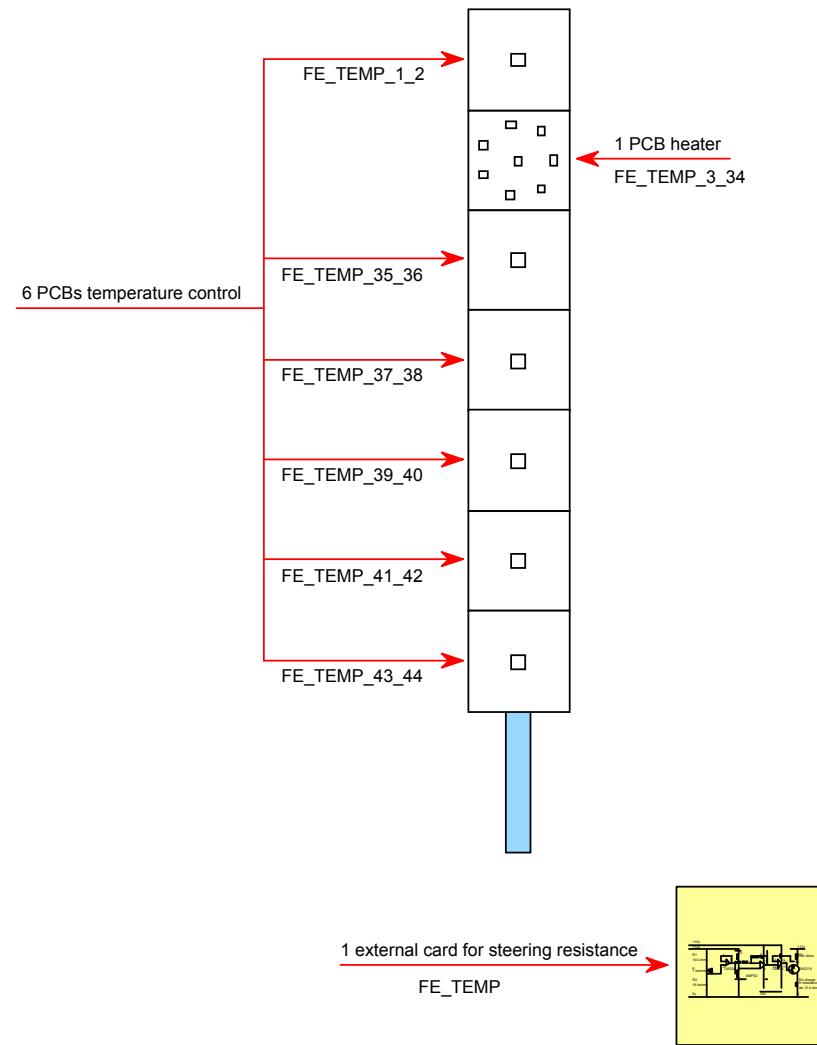
To simulate the slab heat dissipation, we decided to use resistances with a temperature control

The choice is to manufacture:

1 PCB able to simulate the power down to 14.4 mW (1PCB) and power up to 1 W (>7 PCBs)

6 PCBs for temperature control

1 external card for steering resistance



PCB heater

1 card: FE_TEMP_3_34

Dimensions:

Thickness = 0.8 mm

Width = maximum 124 mm

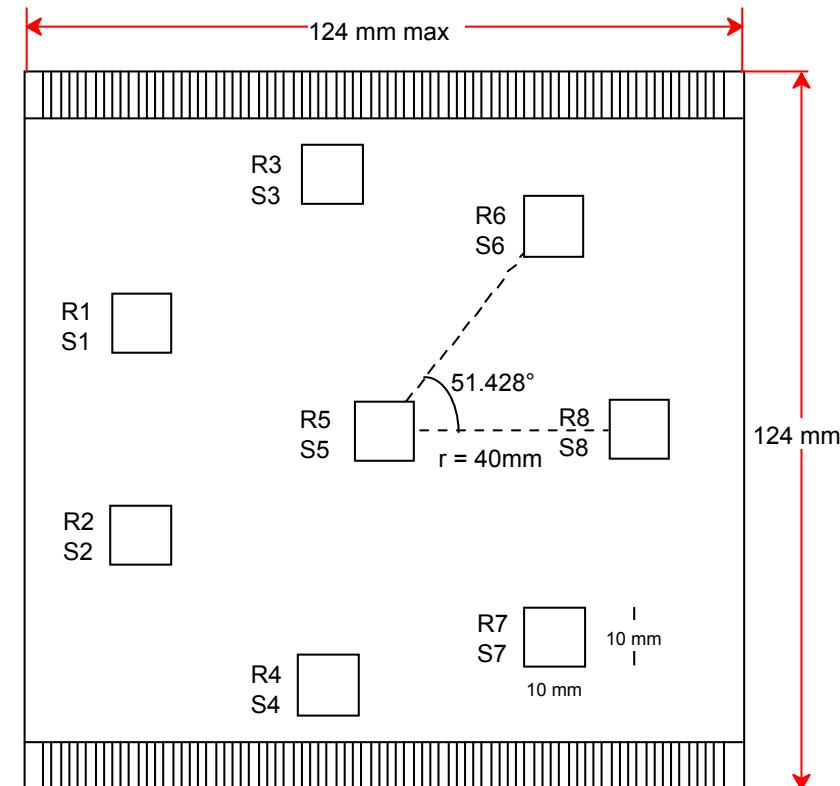
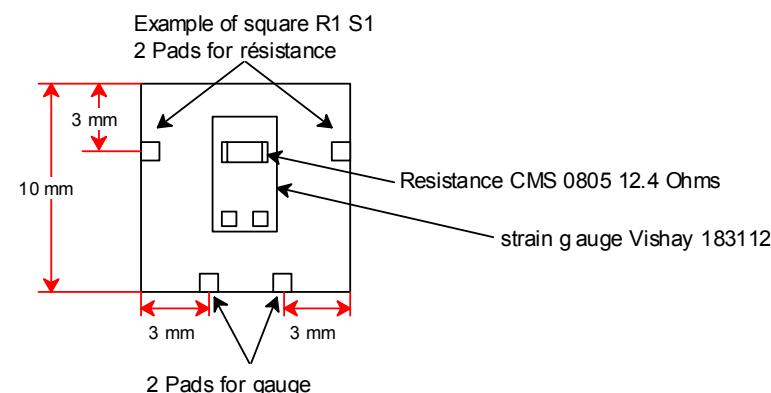
Length = 124 mm

With 8 squares de 10 x 10 mm max as depth as possible

4 electric tracks by square,

2 for resistance

2 for gauge



PCBs temperature control

6 cards:

- FE_TEMP_1_2
- FE_TEMP_35_36
- FE_TEMP_37_38
- FE_TEMP_39_40
- FE_TEMP_41_42
- FE_TEMP_43_44

Dimensions:

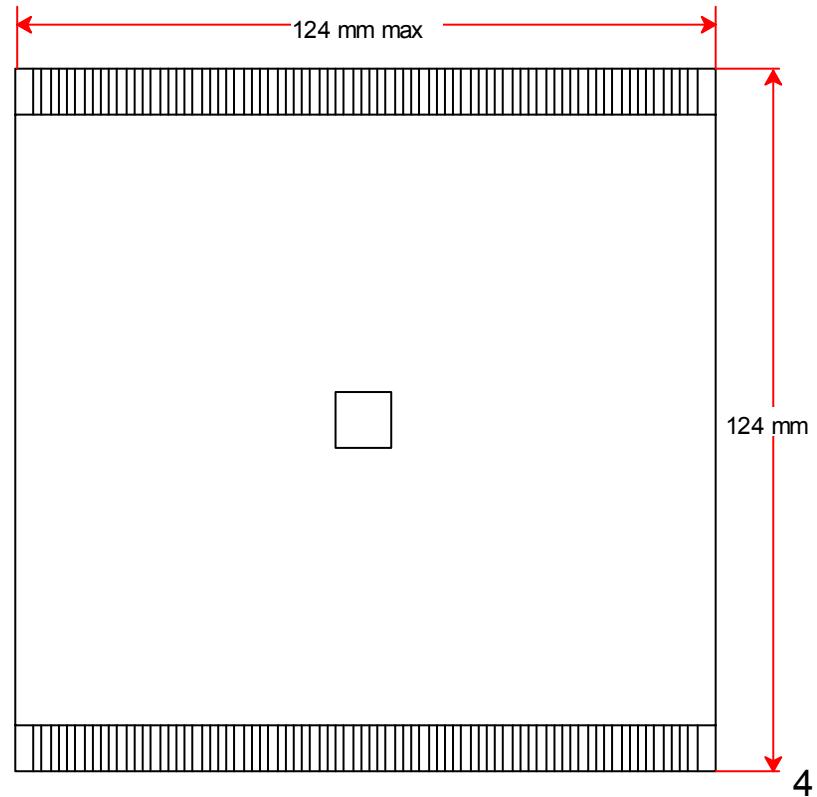
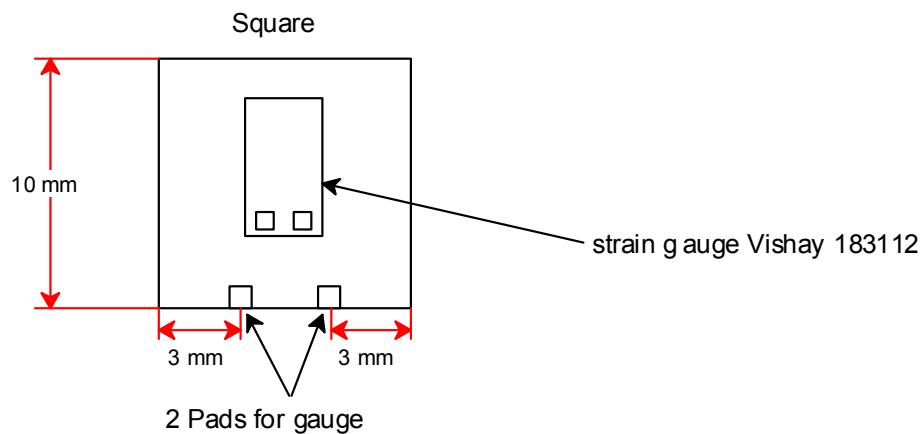
Thickness = 0.8 mm

Width = maximum 124 mm

Length = 124 mm

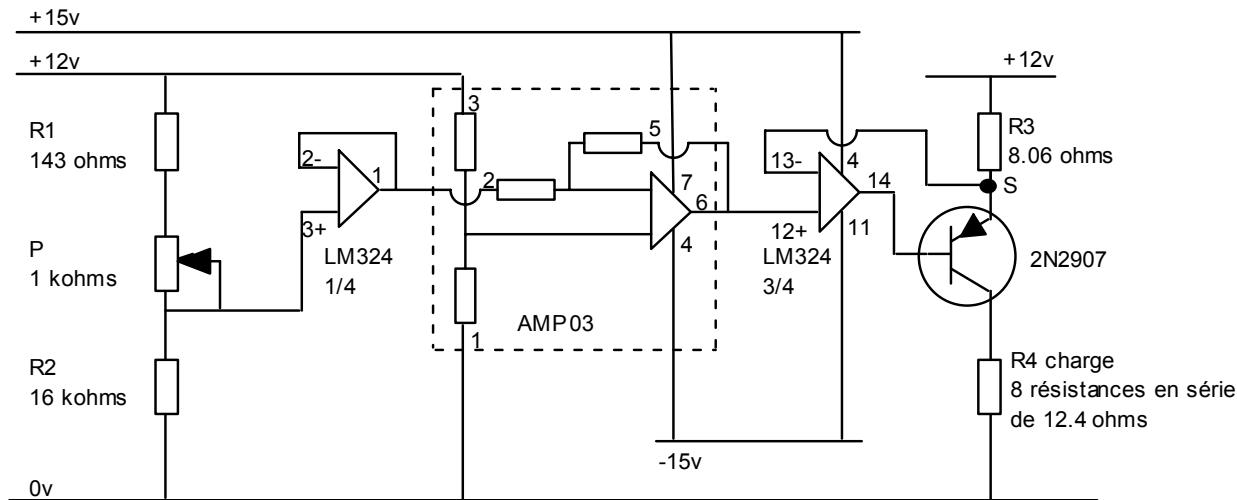
With 1 square de 10 x 10 mm max as depth as possible

2 electric tracks for gauge



Steering card for resistances

1 external card: FE_TEMP



Measures carried out between S point and 0v, the power is adjust by the potentiometer (P)

U for $I_{max} = 11.9v$

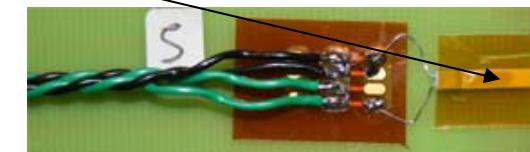
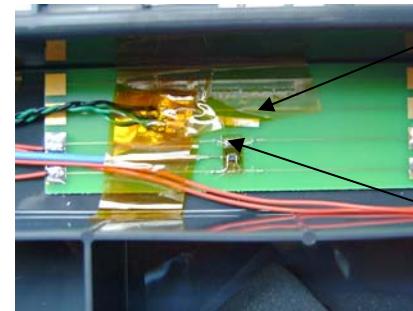
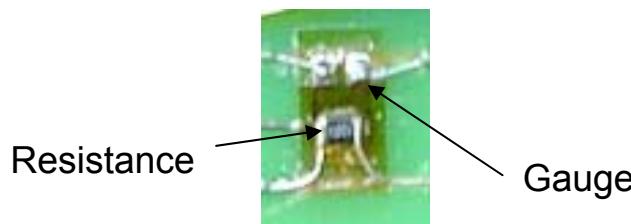
U for $I_{min} = 11.2v$

Pmax for PCB 8 chips = 1W

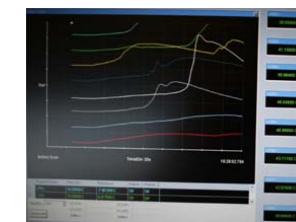
Pmin for PCB 8 chips = 14.4 mW

Test and measurement

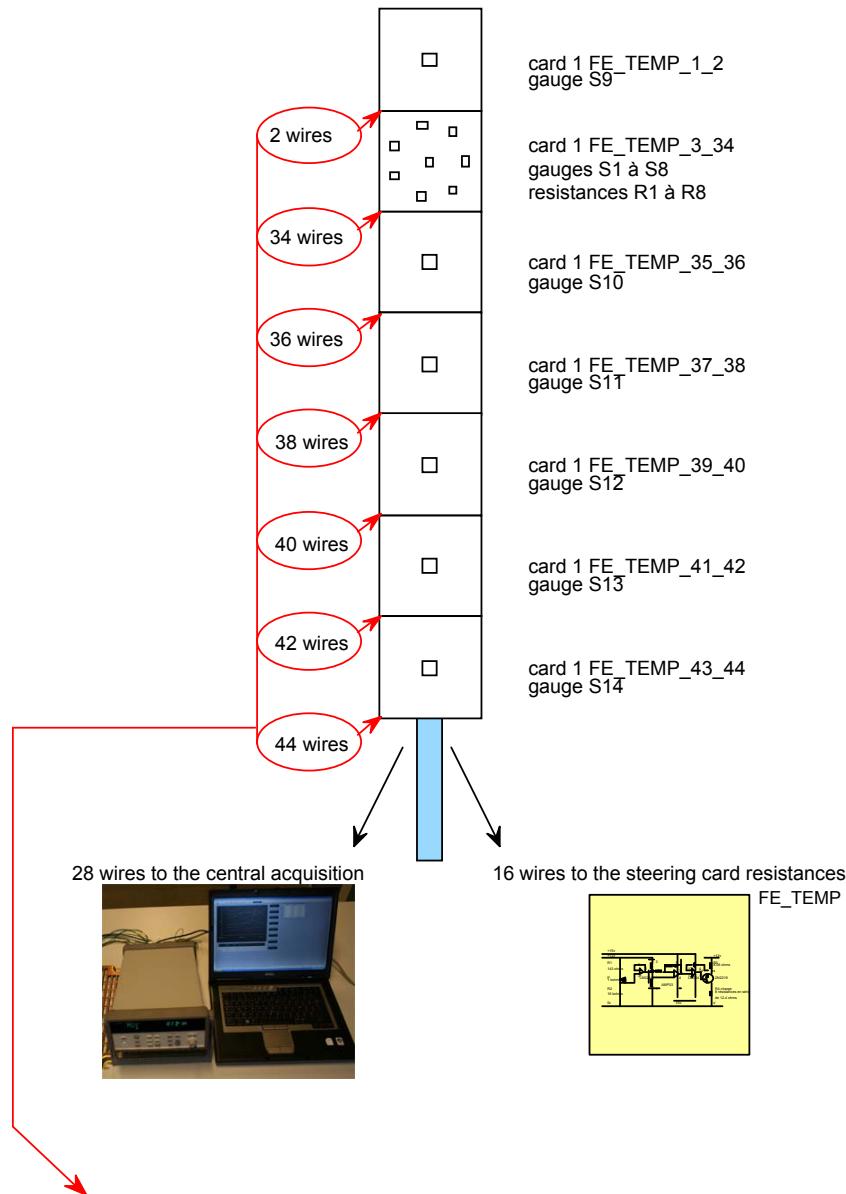
A strain gauge Vishay (reference 183112) is glued on the PCB and the resistance which simulates the chip, is directly bonded to the gauge.



Temperature acquisition system



Cards assembling in slab model



To achieve the interconnections between the PCBs,
we use captone combs

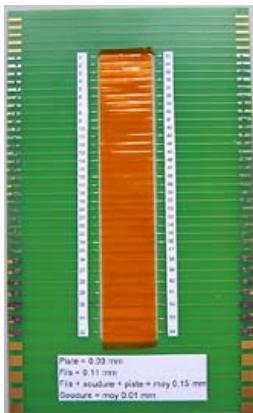
Tests of capton combs

The combs are made by positioning the wires on a layer of capton scotch and covered by another layer of capton scotch.

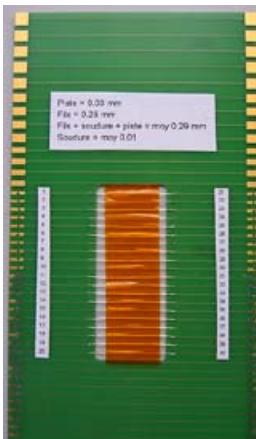
Then the wire are solder alternately starting with wire n° 1 and n° 10, 20, 2, 11, 21...

Soldering iron used: Weller ws50 at 350°C

The temperature is controlled step by step with PT100 placed on the top and on the bottom (wafers side)



Step 2
Comb 32 ways
Capton width = 19 mm
Wire = 0.11 mm
Track = 0.03 mm
Wire + solder + track = moy 0.15 mm
Solder = moy 0.01 mm



Step 1
Comb 20 ways
Capton width = 19 mm
Wire = 0.25 mm
Track = 0.03 mm
Wire + solder + track = moy 0.29 mm
Solder = moy 0.01 mm



Step 3
Comb 20 ways
Capton width = **5 mm**
Wire = 0.11 mm
Track = 0.03 mm
Wire + solder + track = **moy 0.15 mm**
Solder = moy 0.01 mm
T max relieved on the top = **45.6 °C**
T max relieved on the lower side wafers = **42.5 °C**

Resistors load

Parameters	Value parameter
Enclosure type	0805
Technologie	Thin Film
Value of resistance	12,4Ω
Tolerance resistance	±0.1%
Rated power at 70 °C	0.1W
Temperature coefficient	±10 ppm/°C
Using temperature	-55°C à +125°C
Power use	100 V (max.)
Tension overload	200 V (max.)
Length	2mm
Width	1,25mm

Documents annexes Steering card for resistances

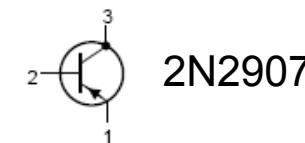
Resistance R3

Parameters	Value parameter
Enclosure type	0805
Technologie	Thin Film
Value of resistance	8,06Ω
Tolerance resistance	±0.1%
Rated power at 70 °C	0.125W
Temperature coefficient	±10 ppm/°C
Using temperature	-55°C à +125°C
Power use	100 V (max.)
Tension overload	200 V (max.)
Length	2mm
Width	1,25mm

R2= resistance 16K OHM 0.1% 10PPM 0805

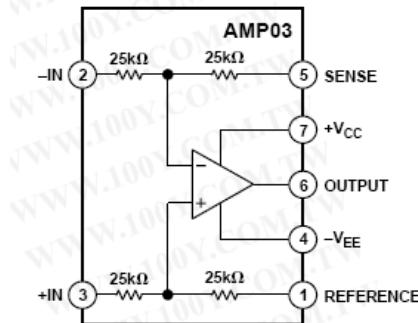
R1=143Ω resistance 0805 143R 0.1% 15PPM

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	-	-60	V
V_{CEO}	collector-emitter voltage 2N2907 2N2907A	open base	-	-40 -60	V V
I_C	collector current (DC)		-	-600	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25^\circ C$	-	400	mW
h_{FE}	DC current gain	$I_C = -150 \text{ mA}; V_{CE} = -10 \text{ V}$	100	300	
f_T	transition frequency	$I_C = -50 \text{ mA}; V_{CE} = -20 \text{ V}; f = 100 \text{ MHz}$	200	-	MHz
t_{off}	turn-off time	$I_{Con} = -150 \text{ mA}; I_{Bon} = -15 \text{ mA}; I_{Boff} = 15 \text{ mA}$	-	300	ns



Analog Devices AMP03

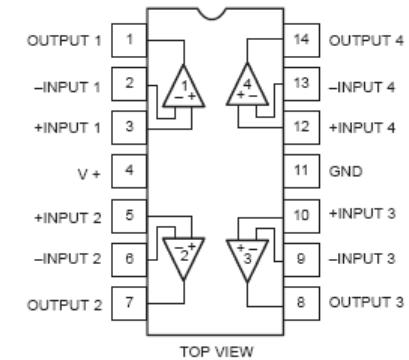
Parameter	Symbol	Conditions	AMP03F			AMP03B			AMP03G			Units
			Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
Offset Voltage	V _{os}	V _{CM} = 0 V No Load, V _{IN} = ±10 V, R _S = 0 Ω	-400	10	400	-700	20	700	-750	25	750	μV
Gain Error		(Note 1)	0.00004	0.008		0.00004	0.008		0.001	0.008		%
Input Voltage Range	IVR	V _{CM} = ±10 V	±10		±10	±10		±10		±10		V
Common-Mode Rejection	CMR	V _{CM} = ±10 V	85	100		80	95		80	95		dB
Power Supply Rejection Ratio	PSRR	V _S = ±6 V to ±18 V		0.6	10		0.6	10		0.7	10	μV/V
Output Swing	V _o	R _L = 2 kΩ	±12	±13.7		±12	±13.7		±12	±13.7		V
Short-Circuit Current Limit	I _{sc}	Output Shorted to Ground		+45/-15			+45/-15			+45/-15		mA
Small-Signal Bandwidth (-3 dB)	BW	R _L = 2 kΩ		3			3			3		MHz
Slew Rate	SR	R _L = 2 kΩ	6	9.5		6	9.5		6	9.5		V/μs
Capacitive Load Drive Capability	C _L	No Oscillation		300			300			300		pF
Supply Current	I _{sy}	No Load		2.5	3.5		2.5	3.5		2.5	3.5	mA



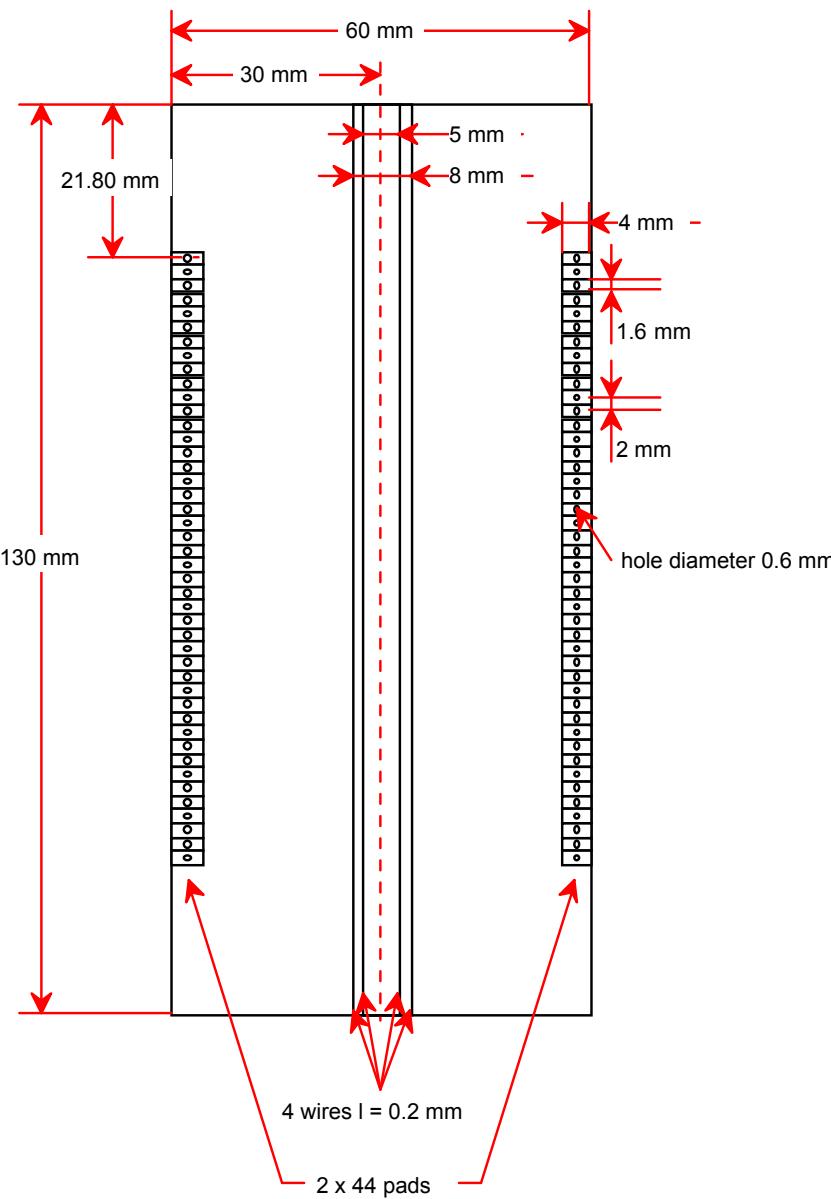
LM324

SYMBOL	PARAMETER	RATING	UNIT
V _{CC}	Supply voltage	32 or ±16	V _{DC}
V _{IN}	Differential input voltage	32	V _{DC}
V _{IN}	Input voltage	-0.3 to +32	V _{DC}
P _D	Maximum power dissipation, T _{amb} = 25 °C (still-air) ¹		
	N package	1420	mW
	D package	1040	mW
	DH package	762	mW
	Output short-circuit to GND one amplifier ²	Continuous	
	V _{CC} < 15 V _{DC} and T _{amb} = 25 °C		
I _{IN}	Input current (V _{IN} < -0.3 V) ³	50	mA
T _{amb}	Operating ambient temperature range		
	LM324/324A	0 to +70	°C
	LM224	-25 to +85	°C
	SA534	-40 to +85	°C
	LM2902	-40 to +125	°C
	LM124	-55 to +125	°C
T _{stg}	Storage temperature range	-65 to +150	°C
T _{sld}	Lead soldering temperature (10 sec max)	230	°C

D, DH, and N Packages

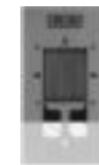


Creation of a special card for making combs



Control temperature Gauge Vishay temperature Sensors

GAGE PATTERN AND DESIGNATION Approximate Size Shown See Note 1	DIMENSIONS						
	GAGE LENGTH	OVERALL LENGTH	GRID WIDTH	OVERALL WIDTH	MATRIX		
					Length	Width	millimeters
ETG-50A/Option E ETG-50A/Option W ETG-50B/Option E ETG-50B/Option W	0.060 1.52 0.125 3.18	0.148 3.76 0.235 5.97	0.100 2.54 0.125 3.18	0.100 2.54 0.125 3.18	0.28	0.20	
					7.0	4.8	
					0.33	0.19	
					8.3	4.7	
WTG-50A WTG-50A/Option W WTG-50B WTG-50B/Option W	0.060 1.52 0.125 3.18	0.148 3.76 0.235 5.97	0.100 2.54 0.125 3.18	0.100 2.54 0.125 3.18	0.28	0.20	
					7.0	4.8	
					0.33	0.19	
					8.3	4.7	
WWT-TG-W200B-050 For weldable temperature sensor, see appropriate datasheet.	0.20 5.08	(shim length) 0.71 18.03	0.200 5.08	(shim width) 0.43 10.92	0.52	0.26	
					13.1	6.6	



50B/E

Acknowledgments

A Thiebault to manage temperatures

M Quentin for carrying out the solder on the capton combs

M Gaspard for the current generator

J Fleury for schemas

D Cuisy for the CAO

M Lacroix