

## **Time schedule**

### *The PCA16 chips*

1.9	Submission of the chips to the packaging company
25.9	Receipt of the chips
25.9 - 1.10	Inspection of the chips
1.10	Delivery of chips to Lund
6.10 – 17.10	Tests of chips

### *The front end boards*

5.9	Submission for production of final design (100 boards)
20.9	Receipt of boards
22.9	Submission for mounting (100 boards)
6.10	Receipt of mounted boards
6.10 – 13.10	Testing

### *Kapton cables*

5.9	Submission for production of the cables
17.10	Receipt of cables
20.10	Submission of cables for mounting of connectors
3.11	Receipt of mounted cables

### *Readout units*

8.9	Order of 2 D-RORC, 4 SIU, 4 optical cables from CERNTECH
29.9	Delivery of these modules
10.10	Delivery of RCU and DCS from Helsinki
13.10 – 17.10	Tests of the units
??	The distribution box