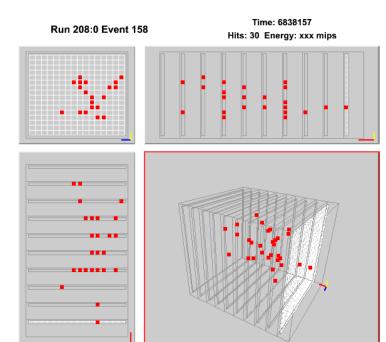


... for a brighter future

Status of RPC based DHCAL

H. Weerts, Argonne Nat. Lab. for DHCAL RPC group







UChicago ► Argonne_{uc}

A U.S. Department of Energy laboratory managed by UChicago Argonne, LLC

TILC09, Tsukuba, Japan, April 2009

CALL CE

Outline

Vertical Slice Test-- Intro

RPC characteristics

Performance

Rate capability Positron showers Pion data Environmental dependence

Future

Conclusions



Vertical Slice Test

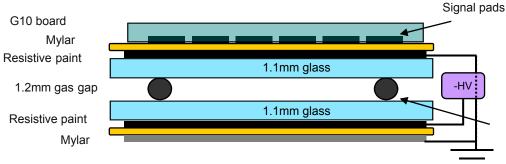
Two-glass design



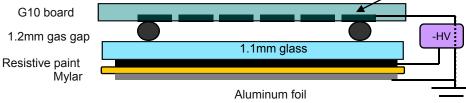
Up to 10 RPCs, each 20 x 20 cm² (Up to 2560 channels)

RPCs

Up to 9 two-glass designs 1 one-glass design Only use RPCO - RPC5 in analysis of e^+ , π^+ Only use RPCO - RPC3 for rate dependence







Signal pads

Vertical Slice Test

Test of whole system with

Up to 10 RPCs, each 20 x 20 cm² (Up to 2560 channels)

RPCs

Up to 9 two-glass designs 1 one-glass design Only use RPC0 – RPC5 in analysis of e^+ , π^+ Only use RPC0 – RPC3 for rate dependence

Absorber

For cosmic rays, muon, pions, electrons: Steel (16 mm) + Copper (4 mm) Rate capability measurement (120 GeV protons): 16 mm PVC with whole cut out in center

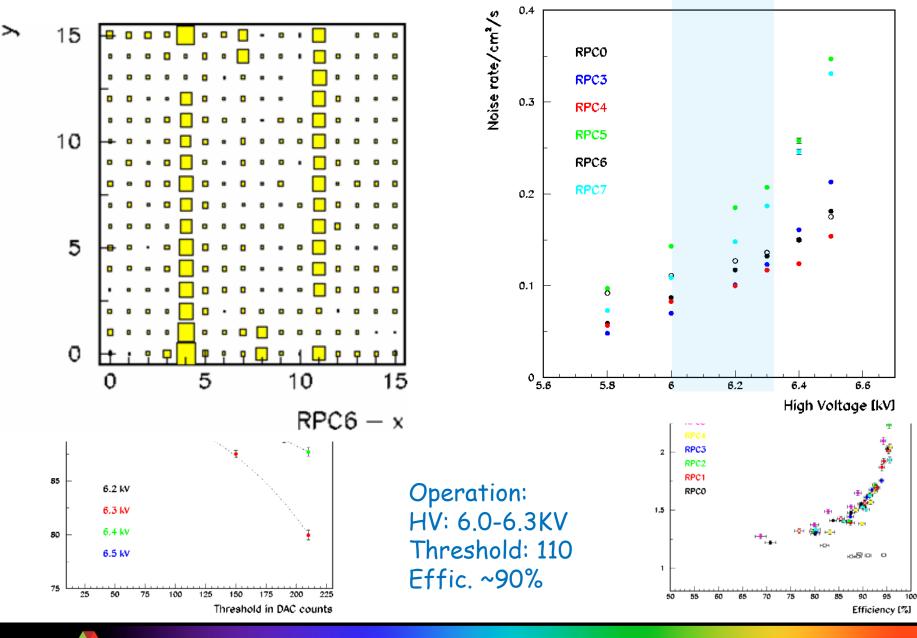
Test beam Collected data in Fermilab's MT6 beam line

Cosmic Rays Collected data for ~18 months Slice of a "complete" system



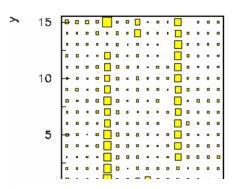


RPC performance

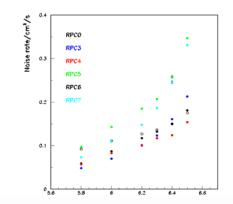


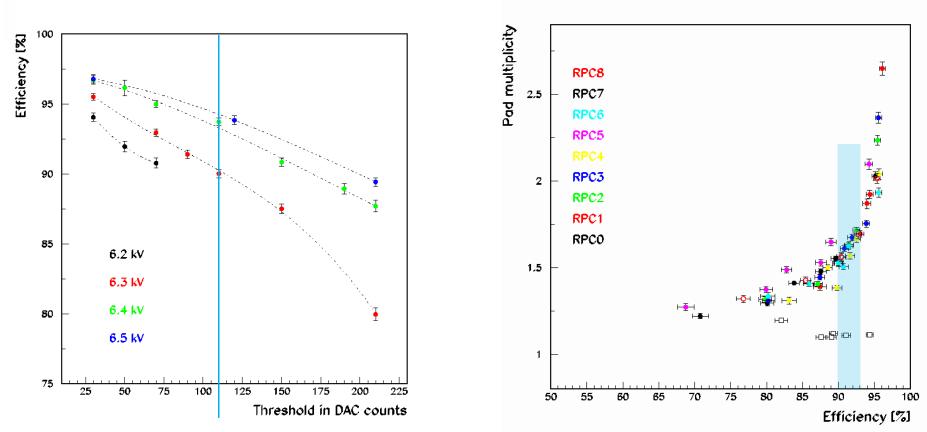
Argonne H. Weerts

RPC performance



Operation: HV: 6.0-6.3KV Threshold: 110 Effic: 90-95%



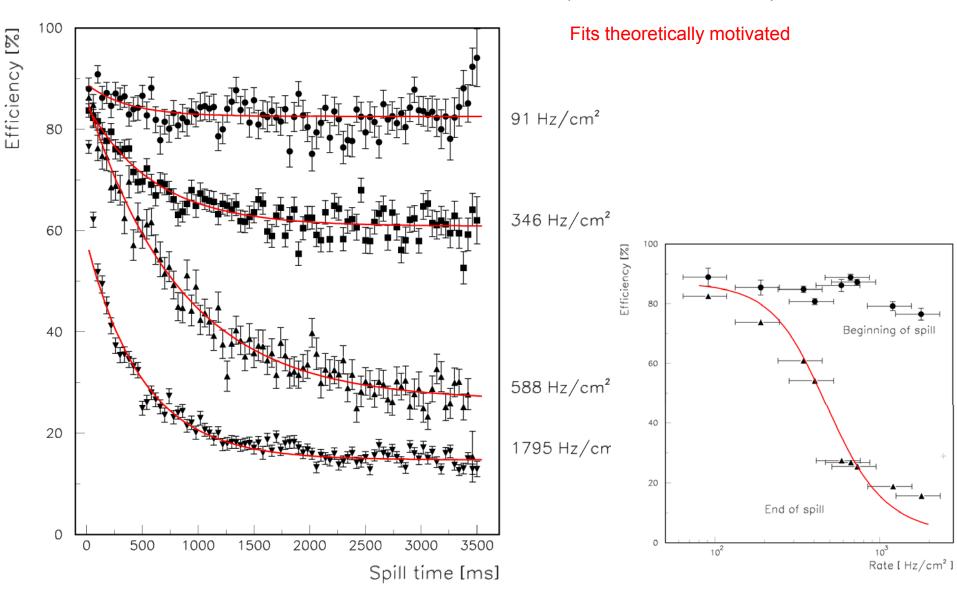




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Rate Capability

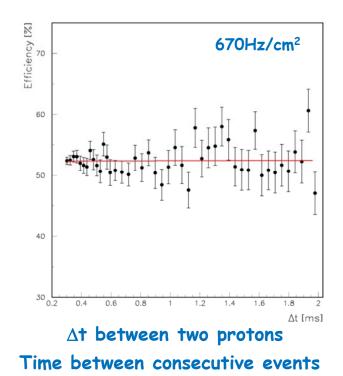
Use time and intensity within test-beam spill



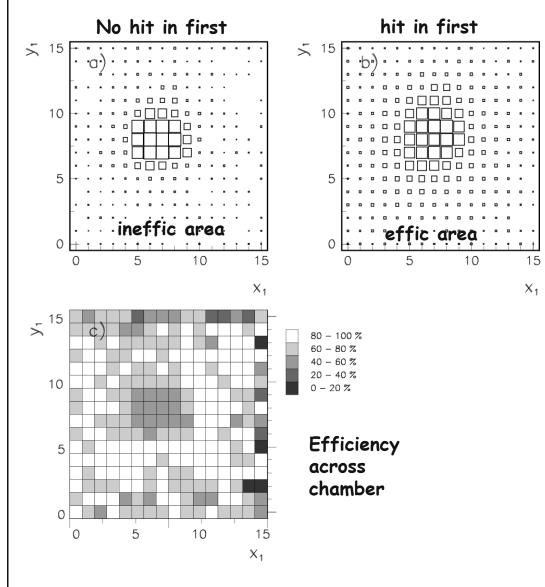


H. Weerts

Rate Capability

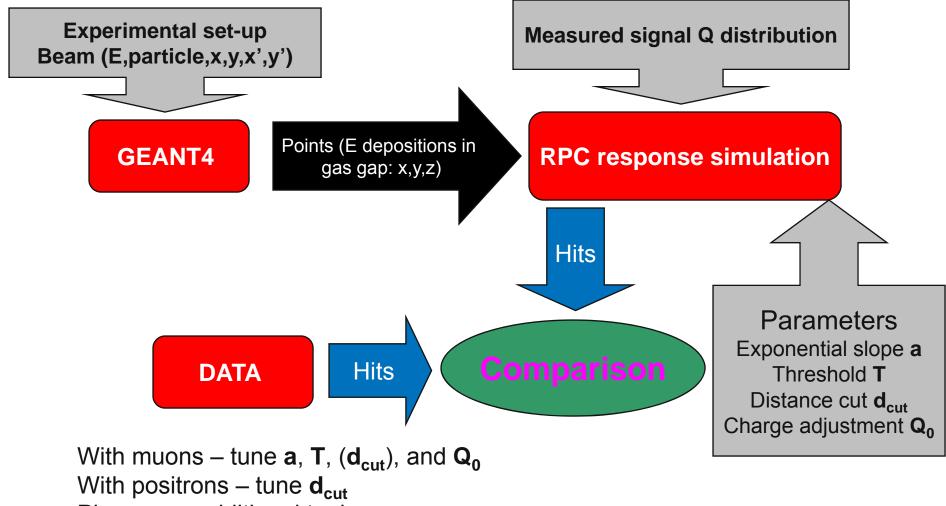


Two chambers: position of hit in 2nd chamber 1800Hz/cm²





Simulation Strategy and comparison to test beam data

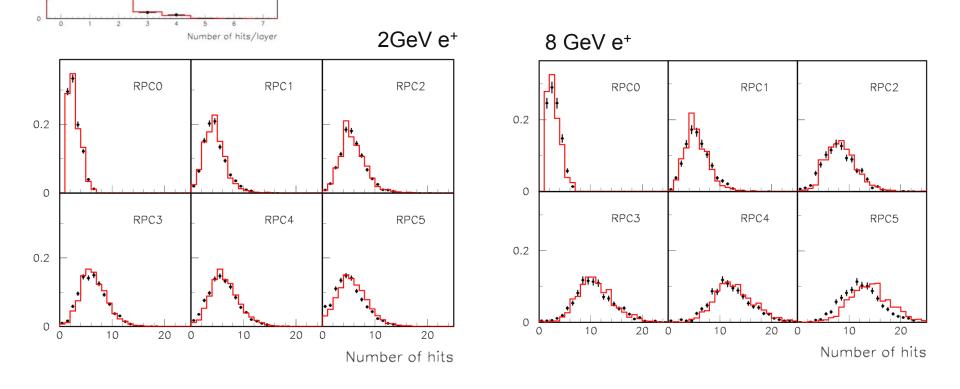


Pions – no additional tuning





Positron Showers





H. Weerts

0.2

0.1

0.4

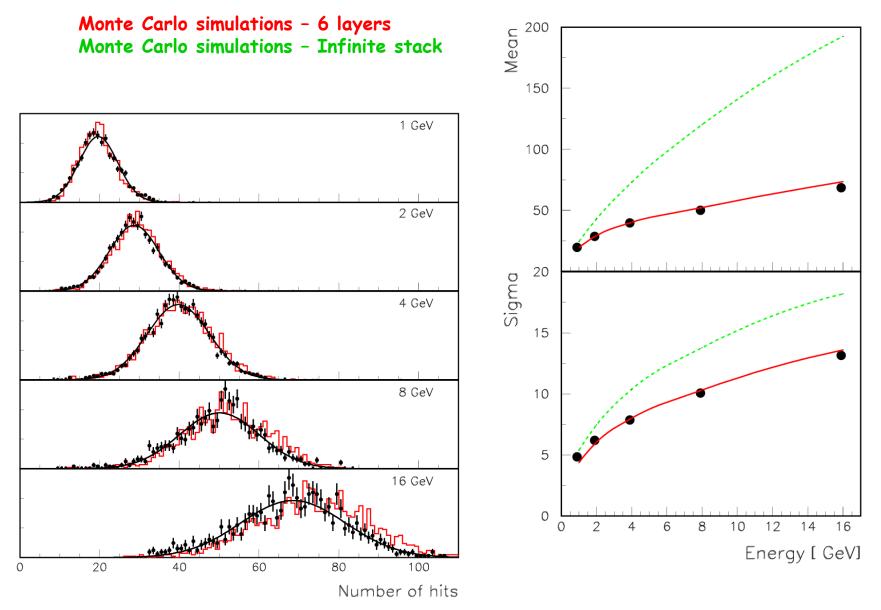
0.2

Broadband $\boldsymbol{\mu}$

16 18 Sum of hits

TILC09; Tsukuba, April 2009

Positron Showers





Response to Pions

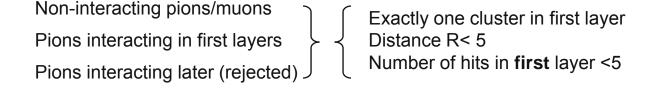
Simulation needs to be redone with tuned parameters...

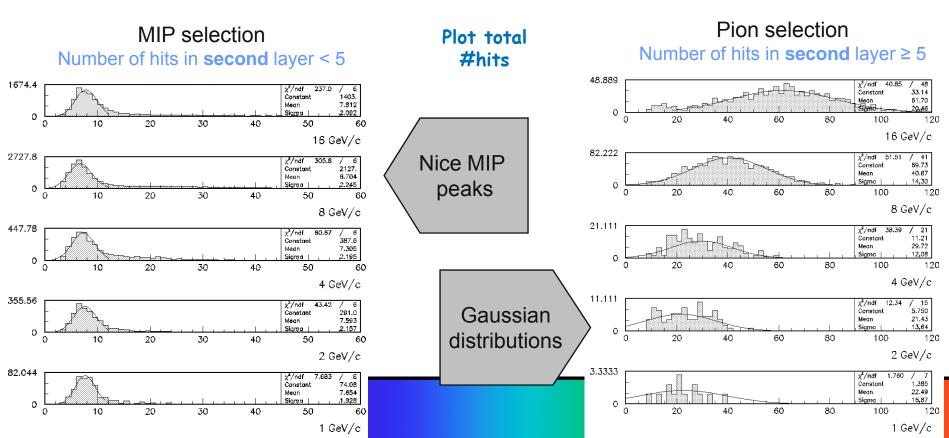
Data at

1, 2, 4, 8, 16 GeV (electrons rejected by Čerenkov)

Analysis separates

6 layer stack corresponding to 0.7 λ_{I}





Response to Pions

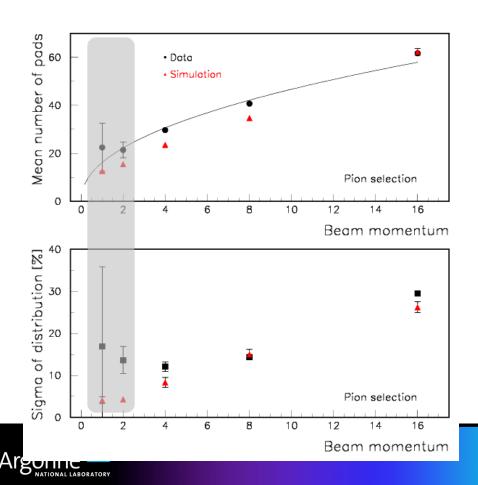
MIP selection

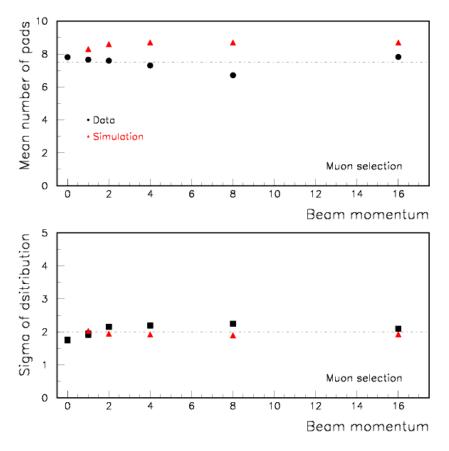
Mean and sigma ~independent of beam momentum Mean not very well reproduced by simulation

 \rightarrow Beam contains muons, simulation does not

(data are cleaner !!!)

Width of distributions adequately reproduced



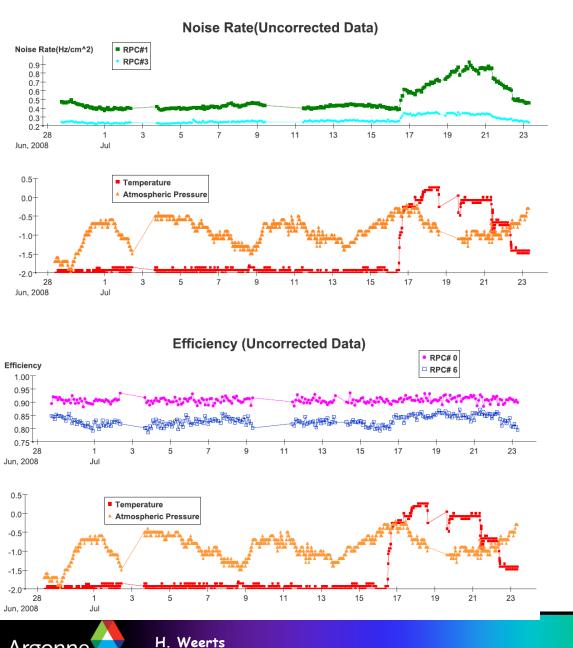


Pion selection

Measurements at 16, 8 and 4GeV/c Not sufficient statistics at 2, 1 GeV/c Non-linearity due to leakage Adequate agreement with simulation

Environmental

Argonne

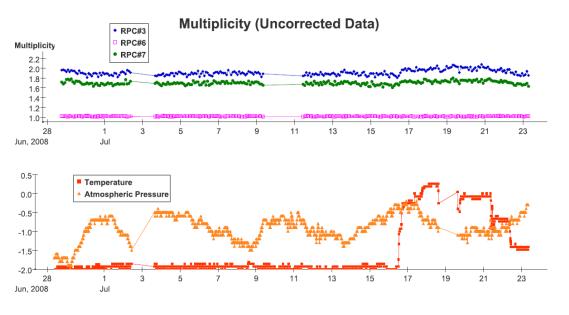


Data collected over period of 18 months

Weather station added about 9 months ago

Barometric pressure Ambient temperature Air humidity

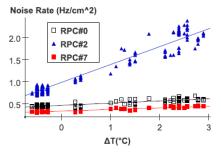


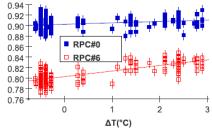


Noise Rate Vs. ΔT(-22.5°C)@100kpa

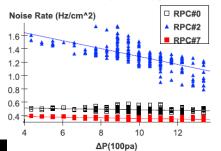
Efficiency Vs. $\Delta T(-22.5^{\circ}C)@100kpa$

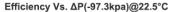
Efficiency

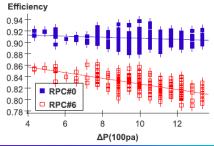




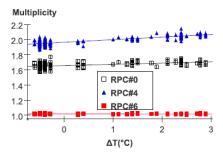
Noise Rate Vs. ΔP(-97.3kpa)@22.5°C



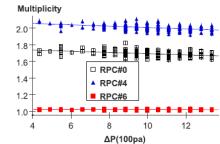








Multiplicity Vs. ΔP(-97.3kpa)@22.5°C



Argonne

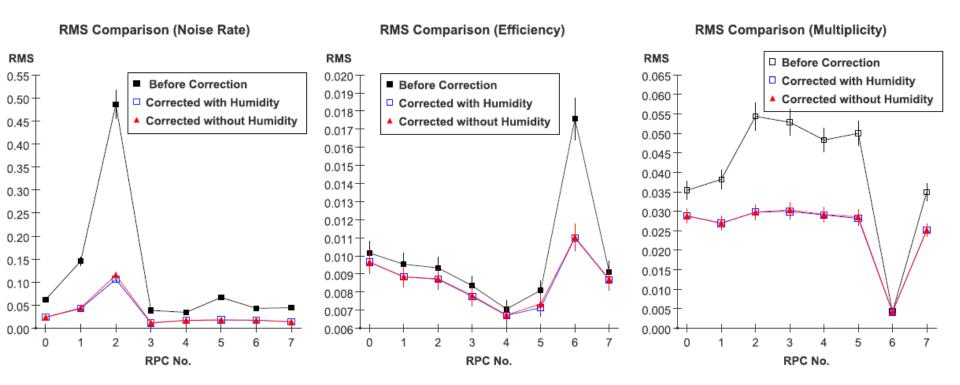
Environmental

Environmental

Assume linear dependence on p,T,H

$F_i(p,T,H) = F_{i,0} + b_{i,1} \Delta p + b_{i,2} \Delta T + b_{i,3} \Delta H \qquad \text{with } i = N,E,M$

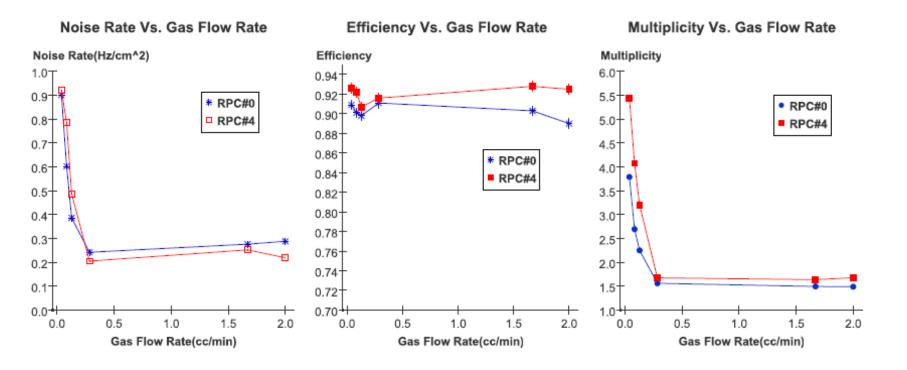
Apply correction \rightarrow check RMS of distributions





Environmental

Bonus: performance versus gas flow rate





Future

1 m³ - Physics Prototype

Description

40 layers each ~ 1 x 1 m² Each layer with 3 RPCs, each 32 x 96 cm² Readout of 1 x 1 cm² pads with one threshold (1-bit) ~400,000 readout channels Layers to be inserted into the existing AHCAL CALICE structure

Purpose

Validate DHCAL concept Gain experience running large RPC system Measure <u>contained</u> hadronic showers in great detail Validate hadronic shower models

Status

Started construction in fall 2008



Future

Front-end Electronics

DCAL III chip

Produced in 2008 Received 11 wafers with 966 chips each \rightarrow 10626 chips

 \rightarrow Packaged chips by early March--done

Testing to be done

'by hand' at Argonne for first chips (in progress) by robot at FNAL (being programmed)

Pad- and Front-end board

On critical path

32 x 48 cm² \rightarrow 4 x 6 chips Being designed (first prototype in hand)

Data concentrator

Design and firmware completed To be implemented onto front-end board



Future

DHCAL Construction Overview

| Item | Status | Outstanding problems/tasks | Critical path |
|---------------------|--------------------------------|--|-------------------|
| RPC construction | Several prototypes exist | Test of thin-glass 2-glass chambers Test of full-scale 1-glass chambers (requires final front-end board) Develop production procedure | No |
| DCAL chips | In hand | Robot testing | Until ~May |
| Front-end boards | Being prototyped | Final design/prototype (ASIC in hand) Testing procedure being developed | ~May - October |
| Back-end | Being produced | Small modifications to existing design | No |
| Gas system | Being assembled | None | No |
| HV system | Completed | None | No |
| DAQ software | Being modified | None | No |
| OFFLINE software | Being developed | None | No |



Conclusions

Learned a lot from vertical slice test and extended operation of chamber stack

Excellent foundation for next step

Building planes/electronics/DAQ for 1m³ test calorimeter to go into CALICE stack

