



Existing SiD Tracking R&D Efforts

◆ Sensors

- Double metal sensor design (SLAC + others)
- Charge division sensor (Santa Cruz + Brown/SLAC)
- Thin sensors (Purdue)
- Pixel sensors / readout (Fermilab + Oregon + UK + DepFet + ???)

◆ Readout

- KPix (SLAC + Oregon + Davis)
- LSFTEII (Santa Cruz)
- Non-US chip development efforts (J.F. Genat)
- Pigtail cable (New Mexico)

◆ Mechanics (Fermilab + SLAC)

◆ Alignment (Michigan)

◆ Simulation

- Digitization (SLAC + Oregon + Kansas State)
- Track finding (SLAC + Kansas State)
- Track fitting (Fermilab + Oregon)