## Status of GEM panel

visiting schedule

1st module: more check

2nd module

3rd,4th

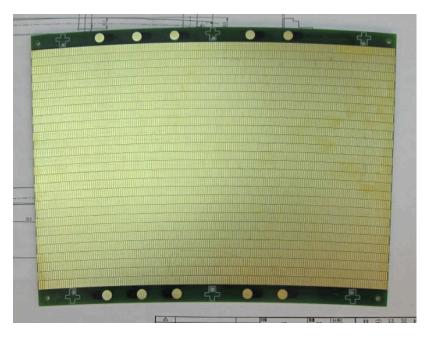
## visiting schedule

	arr.DESY leave
H.Kuroiwa	2/1 ~ 3/8
R. Yonamine	2/1 ~ 3/8
H.Yamaguchi	2/1 ~ 2/28
A.Sugiyama	2/1 ~ 2/14
T.Watanabe	2/9 ~ 2/18
Y.Kato	2/17 ~ 3/1
K.Ikematsu	2/27 ~ 3/9
K.Fujii	????

We have end term exam. and entrance exam. on Feb as well as qualification of undergrad. and grad. This is the best we can do.

# Under-grad. students had measured X-talk on PCB and gain check of module 1/2

#### PCB x-talk

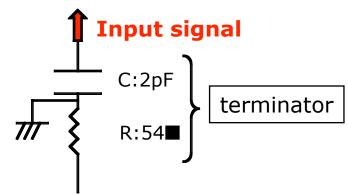


Target channel is connected

to pre-post-amp -> Oscilo

Scan X-talk moving input channel

around the target channel



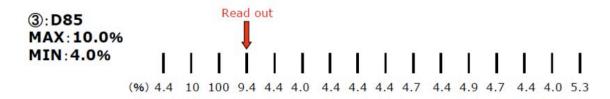


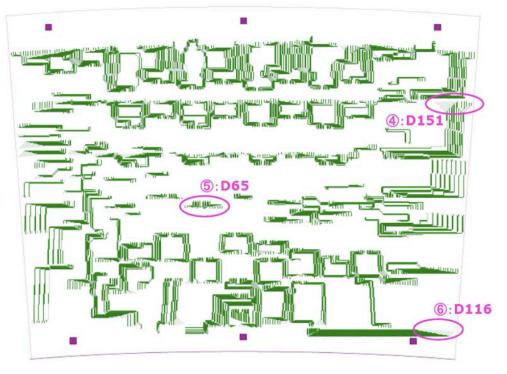


~6% at neighbor channel @ good/nominal routing

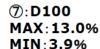


10% at neighbor @ long routing









Read out





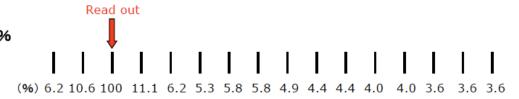
~6% at neighbor channel @ good/nominal routing



10% at neighbor @
long routing
(worst 13%)



6:D116 MAX:11.1% MIN:3.6%



There must be X-talk from connector/readout cable.. other than from PCB We know readout flex-cable has X-talk (but I forgot how much )

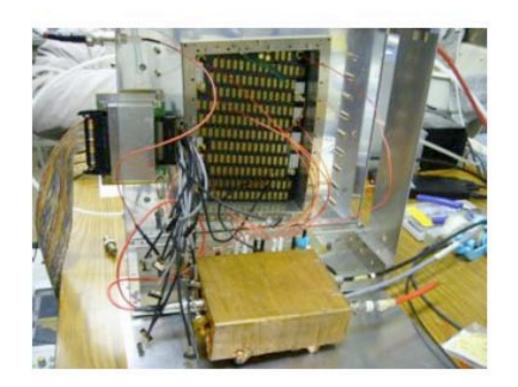
good channels have ~6% X-talk which must be coming from other than PCB worst routing has 13% -> ~7% x-talk from PCB ==> reasonable from pre-proto study

There must be no serious X-talk problem

## gain check of module 1

## method of measurement

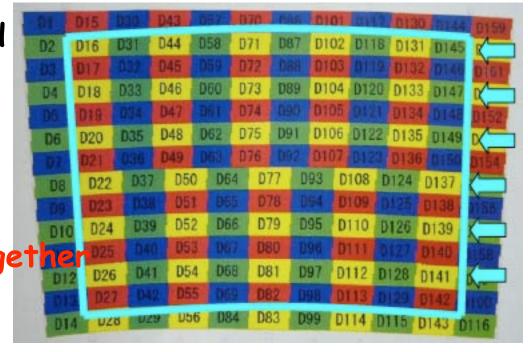
- Test
  - Oscilloscope
  - PMCA···OrtecAMP(gain5×0.5)
- □ VGEM=400V
- ED=100V/cm
- □ 線源Fe<sup>55</sup>

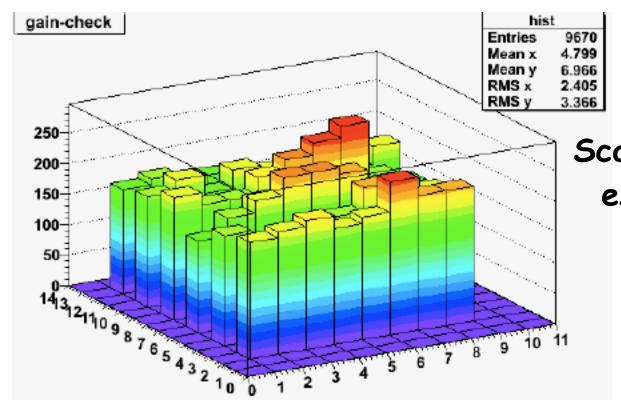


Fe55 PH(main peak) is monitored through (belle) pre/post amp by oscilloscope.

so accuracy is very poor!!

16ch+16ch are all connected together

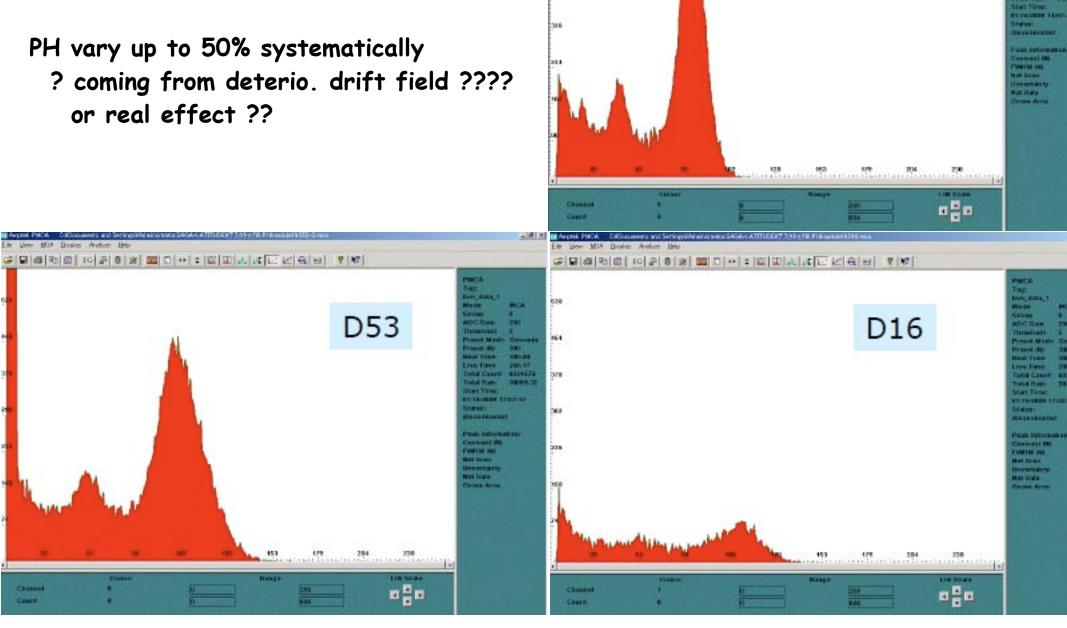




Scan every other connector except outer edge

Fe55 PH(main peak) is monitored through Ortec pre/post + PMCA

some are good, some are not so good



# [ ] 4 [ 10 ] 10 [ 10 ]

D38

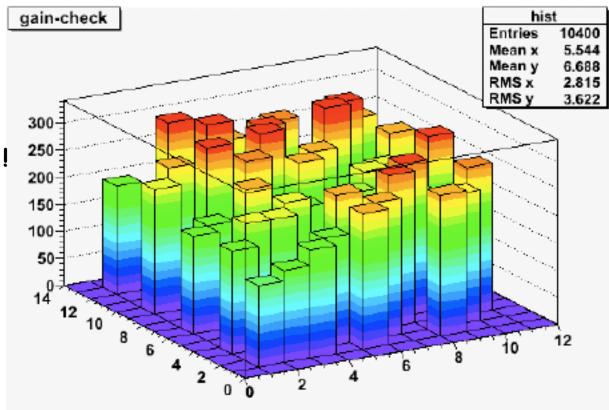
module 2 has also large variation

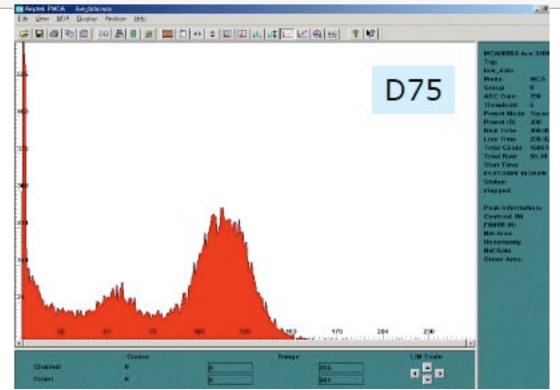
1 GEM sheet is dead after discharge!

replaced GEM is not well stretched.

we will fix this later

we see signal from module 1 and 2
but .... further study will be done
with ALTRO+DAQ
or in LP1 test





We received 3 more back frames from Dan. now they are mounted to PCB at the company

after we receive 2 more PCB, we will assemble GEM on them and check signal

and ship to DESY

Gate: we are preparing but we may start without Gate