Short summary of the R&D plans for CERN, which are (or could be) related to the TPC studies

These plans have been submitted to the CERN management recently. It is not yet known at which level they will be funded. In case the funding will be obtained, the actual work will start in 2010.

The plan comprises the following elements:

- 1. TPC simulation studies
- 2. Participation in the Timepix2 development
- 3. Continuation of TPC compact pad readout developments (second-generation chip developments based on the experience of the test beams, as well as a study of system aspects)
- 4. General CLIC-related readout studies (taking into account the CLIC bunch timing), in particular powering issues and power pulsing (50 HZ, as yet undefined whether applied to TPC electronics or electronics of another sub-detector)
- 5. Participation in TPC test beam efforts

CERN would like to carry out the above activities in the LC-TPC framework and we will be happy to do the work in conjunction with other LC-TPC collaborators.