

CALICE week at IPN Lyon

Thursday, 17 September 2009

Electronics and DAQ -part II - Amphithéâtre Dirac (10:45 - 12:35)

time	[id] title	presenter
10:45	[52] DHCAL DAQ integration in physics prototype	PAUL DAUNCEY (IMPERIAL COLLEGE)
10:55	[53] DAQ SW overview and simulation	DAVID DECOTIGNY (LLR)
11:15	[54] AHCAL DIF status and tests update	MATHIAS REINECKE (DESY HAMBURG)
11:35	[55] performance of spiroc chips in skiroc mode	MICHELE FAUCCI (LAL ORSAY)
11:50	[56] Status of DCC and DAQ testbench at LLR	FRANK GASTALDI (LLR PALAISEAU)
12:05	[58] DAQ2 status	MATTHEW WING (UCL LONDON)
12:20	[62] Micromegas m2 ASU and DIF	CYRIL DRANCOURT (LAPP ANNECY)