

Studies outline for week of August 24th (draft)

Monday

- SimconDSP system end-to-end checkout of functionality, DOOCS interface, etc
- Parallel work at ACC2/3, DSP system functionality test (BLC, "Slope").

Tuesday

- Continue SimconDSP system end-to-end checkout of functionality, DOOCS interface, etc

Wednesday

- Verify MPS interlock functionality; commission/test piezo control; DAC check-out

Thursday

- Turn on all RF systems, set up operation with long RF pulses.
- Cavity filling optimization by phase modulation

Friday

- System characterization with RF (measure noise levels, regulator gain optimization,...)
- Forward / reflected power signal calibration

Saturday [contingency]

Sunday [contingency]