IP BPM Electronics Beam Test

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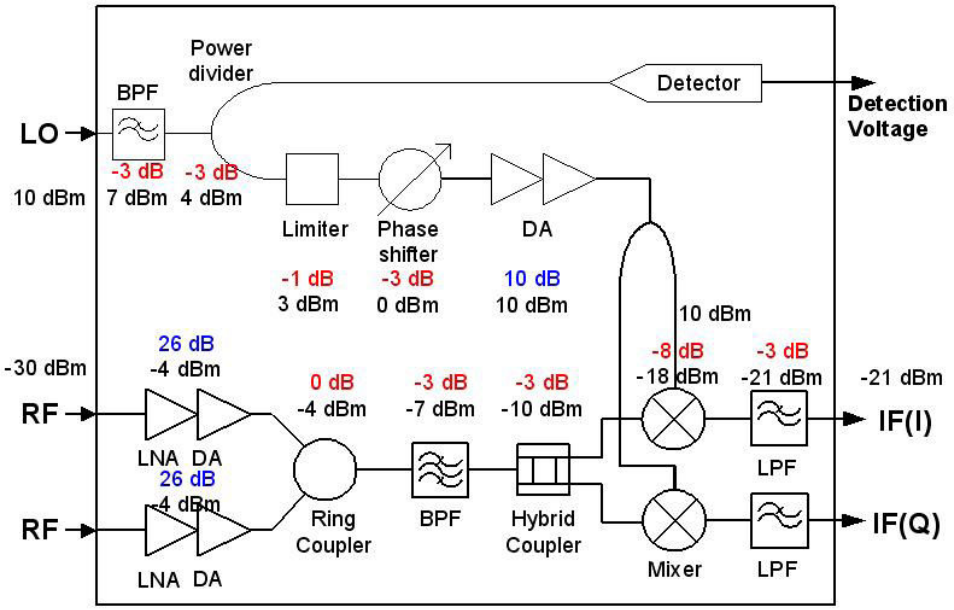
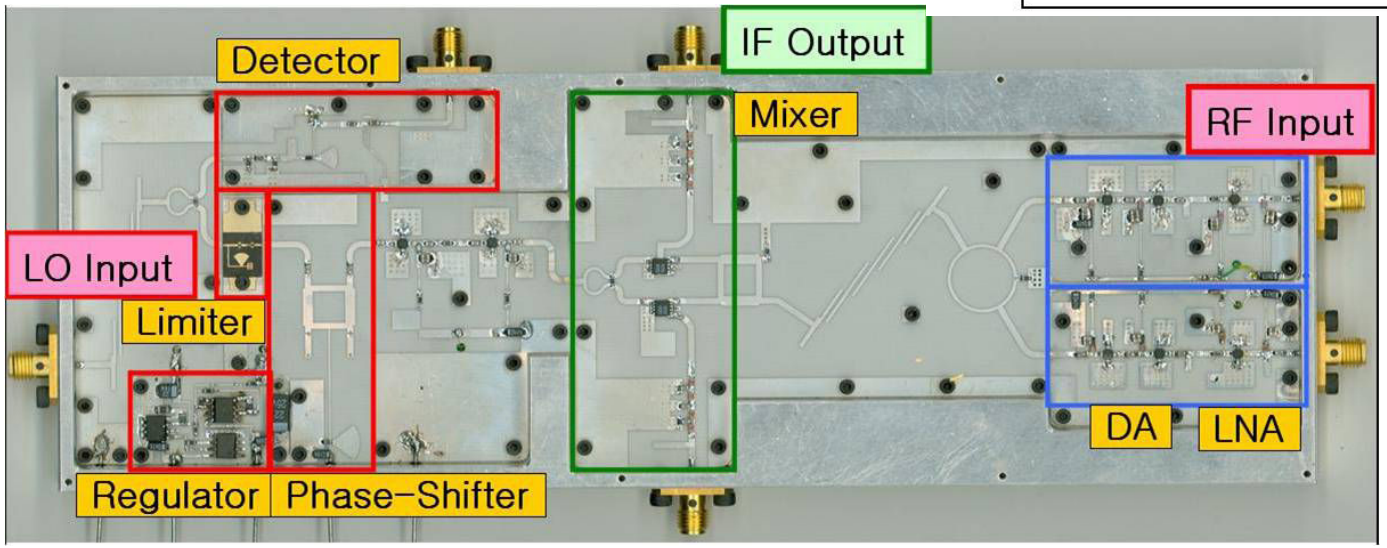
Y. Honda (KEK)
S. Shin (PAL)

Schematic view

Homodyne Processor
Two RF Input
6.426 GHz with 180 phase diff.
LO Input
6.426 GHz, using Ref. signal
capability of phase shift changing volta
Two IF Output
I & Q with 90 phase diff.





Linearity Measurement

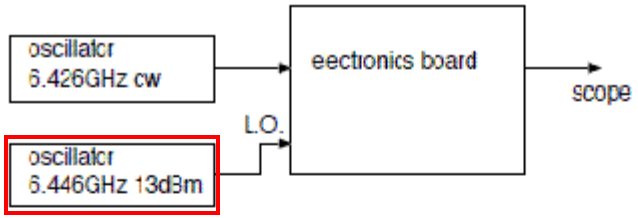


図 2: 入出力の関係を調べるセットアップ

KNU electronics
6.426GHz 4dBm

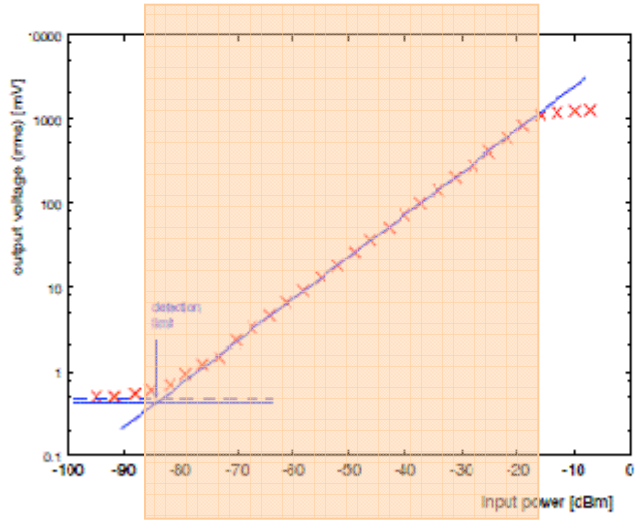
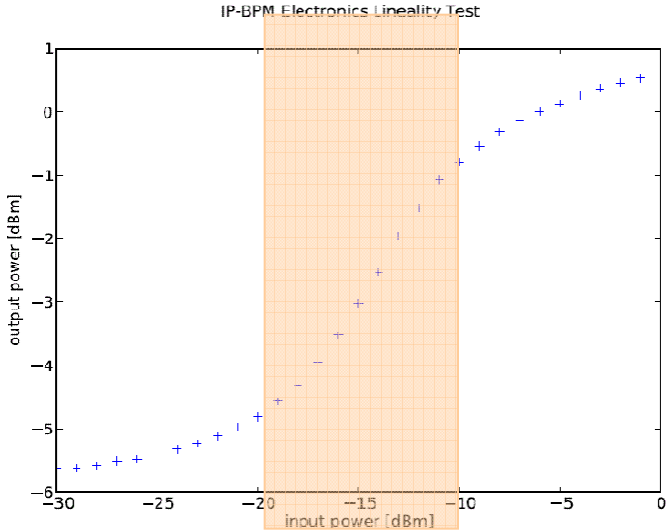
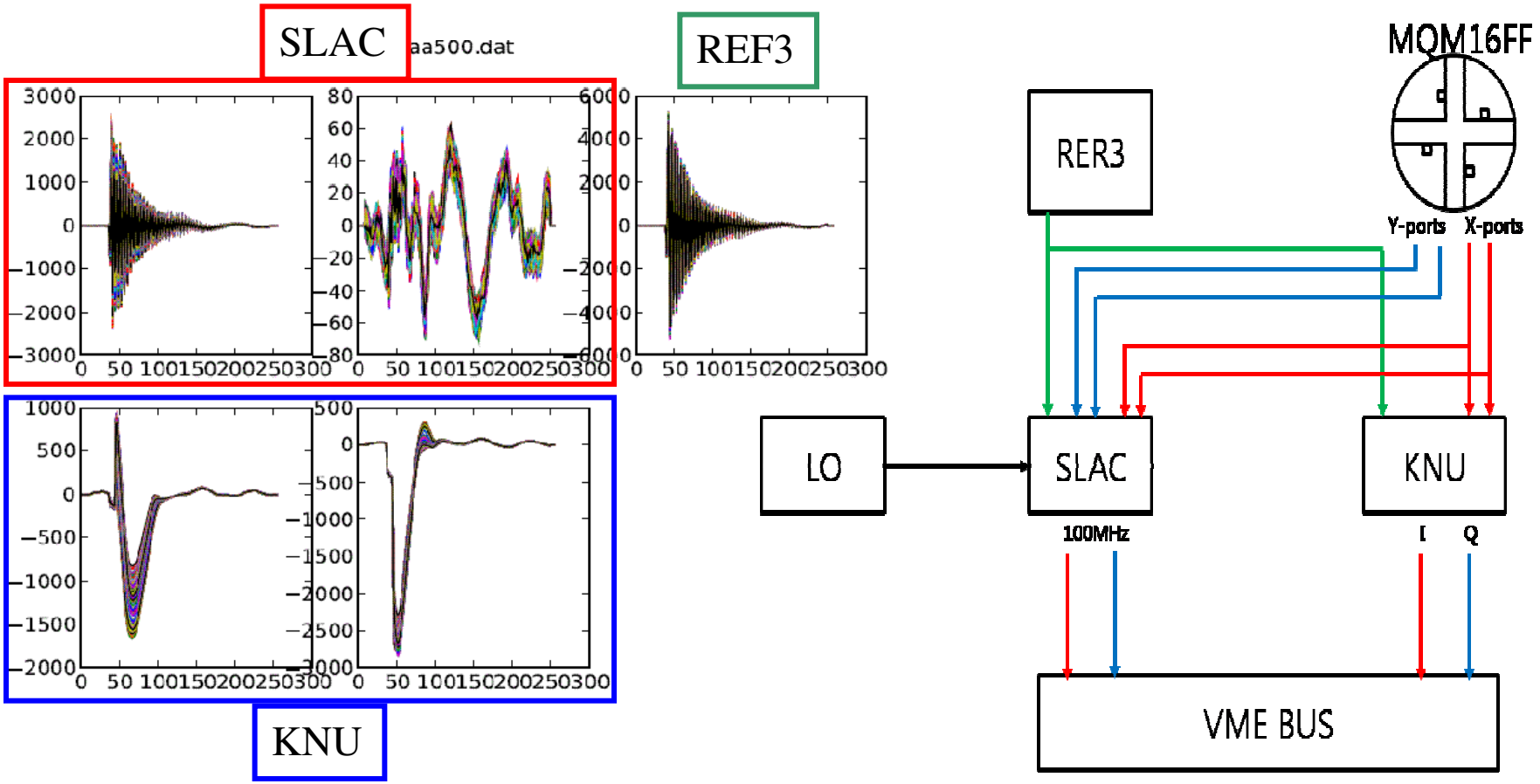


図 3: 入出力の関係

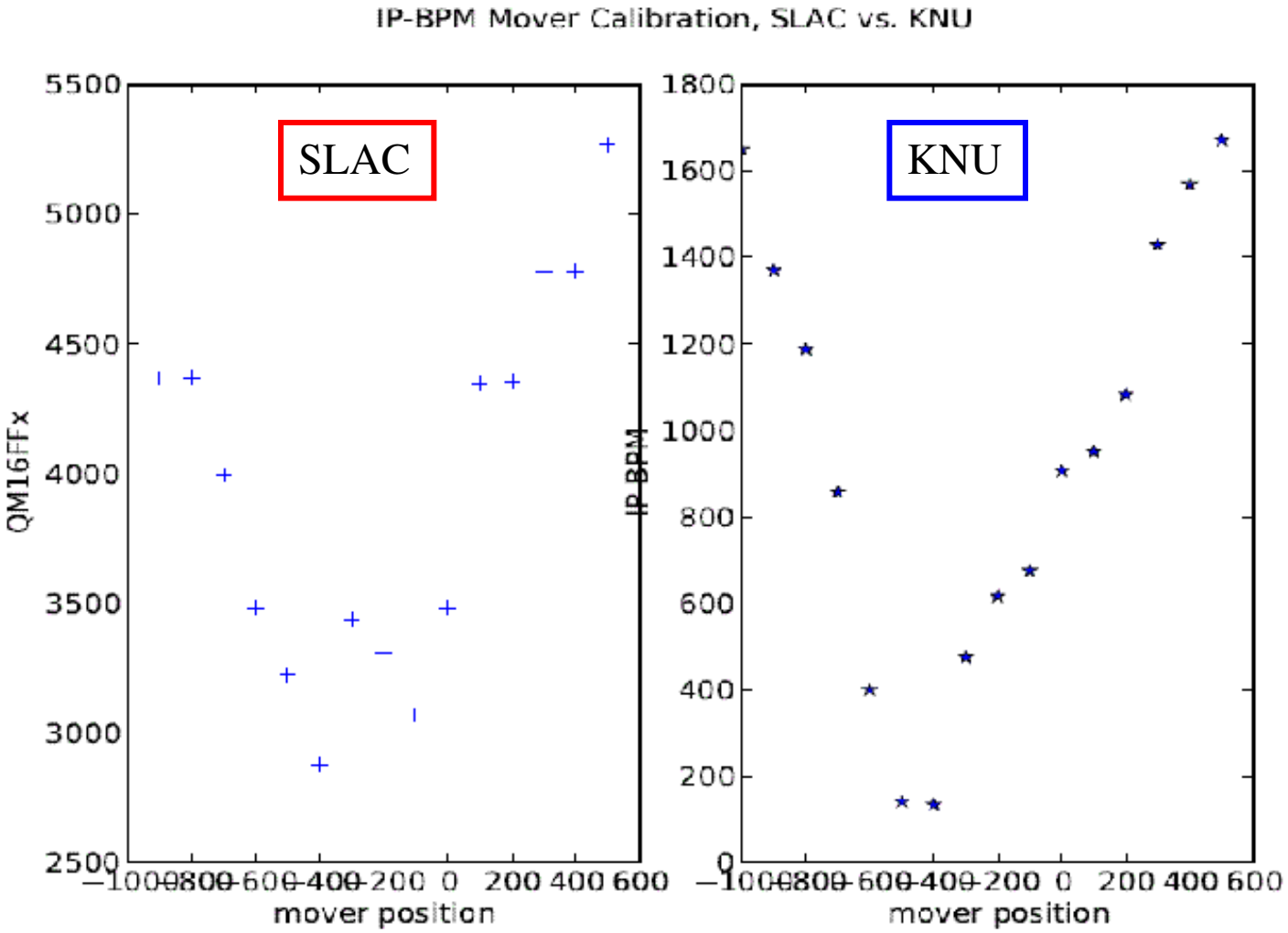


Ref : Test of SLAC electronics (Y.Honda) 2005/12/30
Internal Report

Block Diagram



Position Sensitivity



Plan

- Linearity measurement again
  - This weekend ☺
- Resolution Measurement

- Test w/ KNU IP-BPM
- Modification of IP-BPM electronics??