



Contribution ID: 392

Type: **not specified**

The Saltro chip design for TPC pad readout

Wednesday, 20 October 2010 09:00 (20 minutes)

Primary author: ASPELL, Paul (CERN-CERN-CERN)

Presenter: ASPELL, Paul (CERN-CERN-CERN)

Session Classification: ECFA Parallel