

# Analysis of PCB Exposure Tests

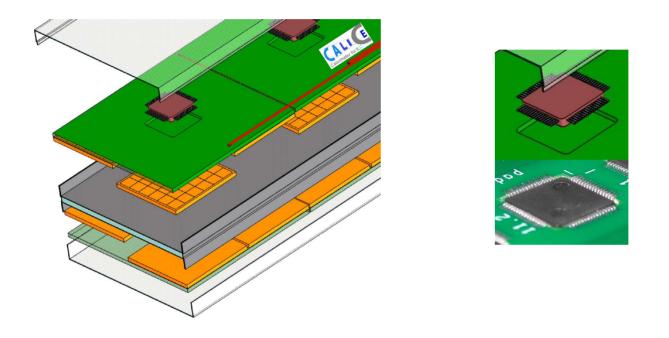


Roman Pöschl LAL Orsay

- Motivation
- Analysis and Results
- Summary, Conclusion and Outlook

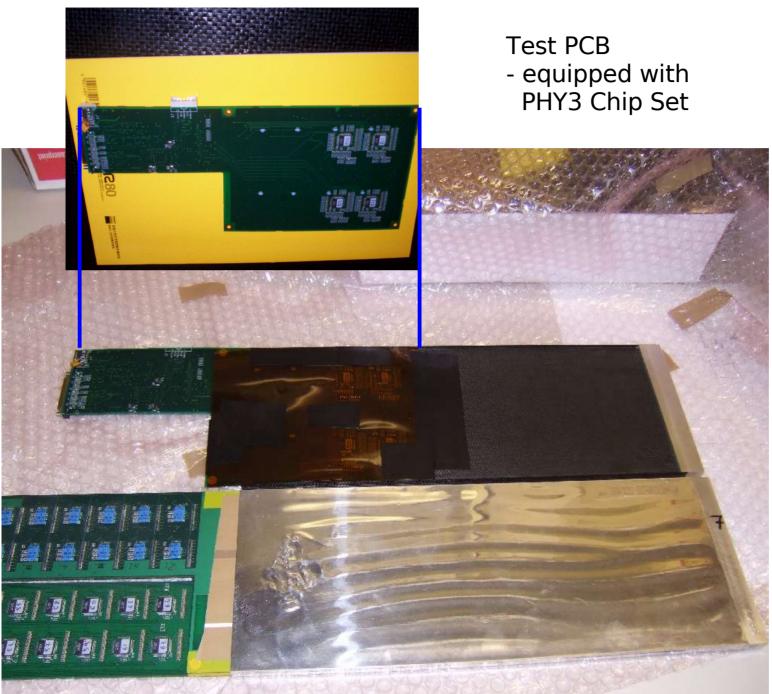
### Introduction

Calorimeter Electronics to be interleaved with layer structure



Do high energetic showers create signals directly in electronics ? If yes, Rate of faked signals ?

Special PCB in Ecal Prototype during CERN 07 testbeam – Experimental Setup I



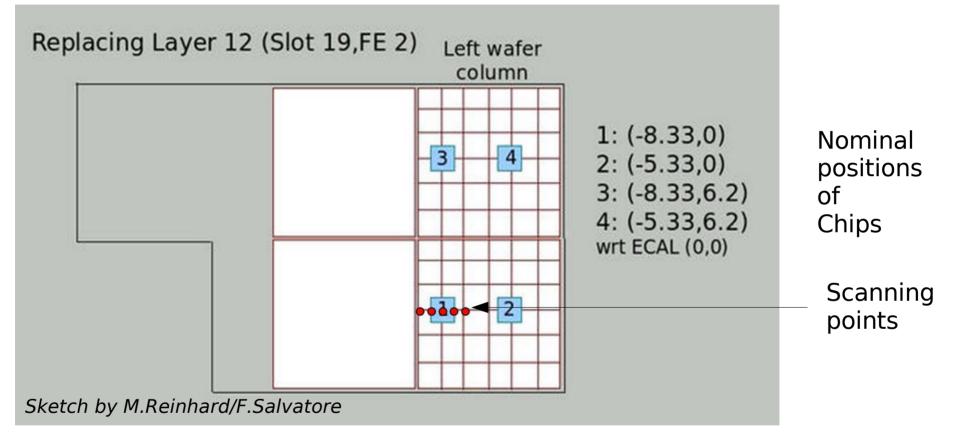
#### **Prepared Slab**

W dummy
capton and paper for electrical shielding

**Usual Slab** 

Special PCB in Ecal Prototype during CERN 07 testbeam – Experimental Setup II

- PCB positioned at place of layer 12 in Ecal  $\sim$  shower maximum x,y position identical to layer 2
- Schematic view of test PCB 'Expect' signals from 72 pads, 4x18 = 2 Wafer



- 2.6 10<sup>6</sup> Events with 90 GeV Electrons (- 5.8 10<sup>5</sup> with 70 GeV Electrons) At least 70 K at each scanning point (Details see later) Runs 331462 – 331518 Today: Full Statistics
- First Step: Runs were subject to the same data processing chain as 'usual' runs Calice Analysis Meeting 3/5/2010

Disabling of zero suppression in reco output

# - Three Scenarios:

- 1) No pedestal correction
- 2) Full pedestal Corrections

# 3) Pedestal Corrections restricted to signals from Chips

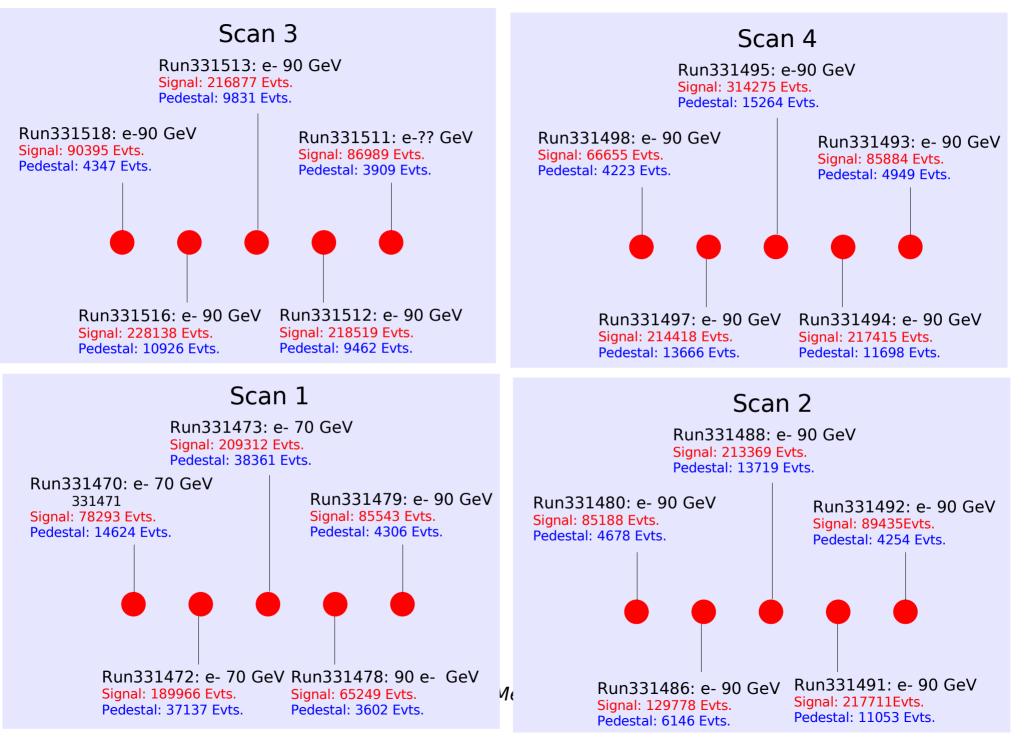
Remember that there are still 216 entries for the layer in the data files

# - General Methodology:

Subdivision of Runs into BeamTrigger and Pedestal Trigger Events (Oscillator Trigger) interleaved with beam events Corrections are applied (or not) to pedestal as well as to signal events

Note: The reconstruction s/w had to be tweaked a bit for that

#### **Statistics of Analysis**



On Run Selection and Observations

- Run Selected according to entries in the logbook No comments on bad quality by Shift Crew
- Switch of energy between Run 331473 and Run 331478
  - Change in Pedestal Rate
     20% of all events -> 5% of all events
     Still at least 3500 of (valuable) pedestal events

### - at least 70k Events at each point

- mostly 90 kEvents for off center runs
- > 200k at (nominal) Chip Center

- Aim: Upper Limits/Probabilities as a function of the Threshold
- Requires calculation of limits with underlying background

Probability Density Function (Frequentist Approach):

 $f'(n; \lambda_s + \lambda_B) = f(n; \lambda_s + \lambda_B) / \sum_{n_B = 0}^{k} f(n_B; \lambda_B)$  f, f are Poissonnian Densities

Presence of Background via numerator (Approach á la Zech NIM A277)

Using this pdf the Confidence Limits/Upper Limits can be calculated using regular statistics techniques

Here: S. Brandt, Datenanalyse, pp.183

Developed ("c++ fied") program to calculate upper limits in the presence of <u>known</u> background.

### Estimation of Background

# Several Approaches:

### - "Gaussian Background"

Assume gaussian distribution of noise spectra mean and sigma from measured noise, i.e. pedestal spectra

### - "Detailed Background"

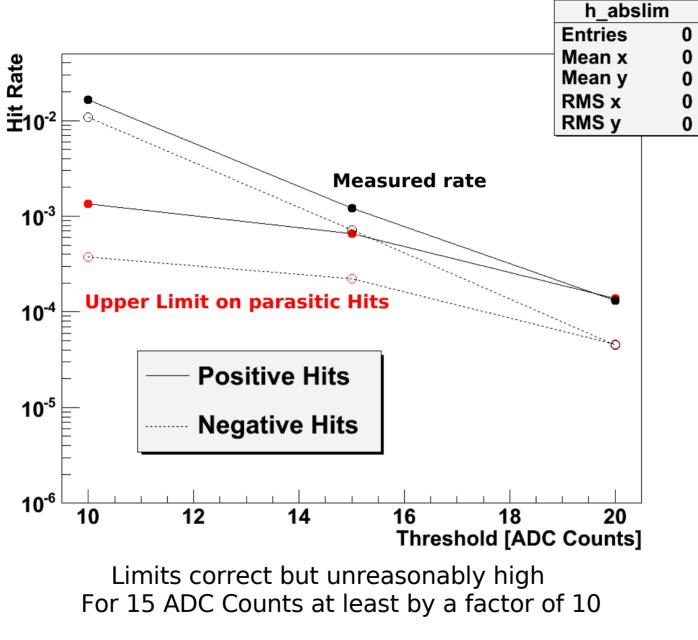
Create pdf from measured noise spectra and generate noise background from these pdfs

- "Cross Modelled Background"

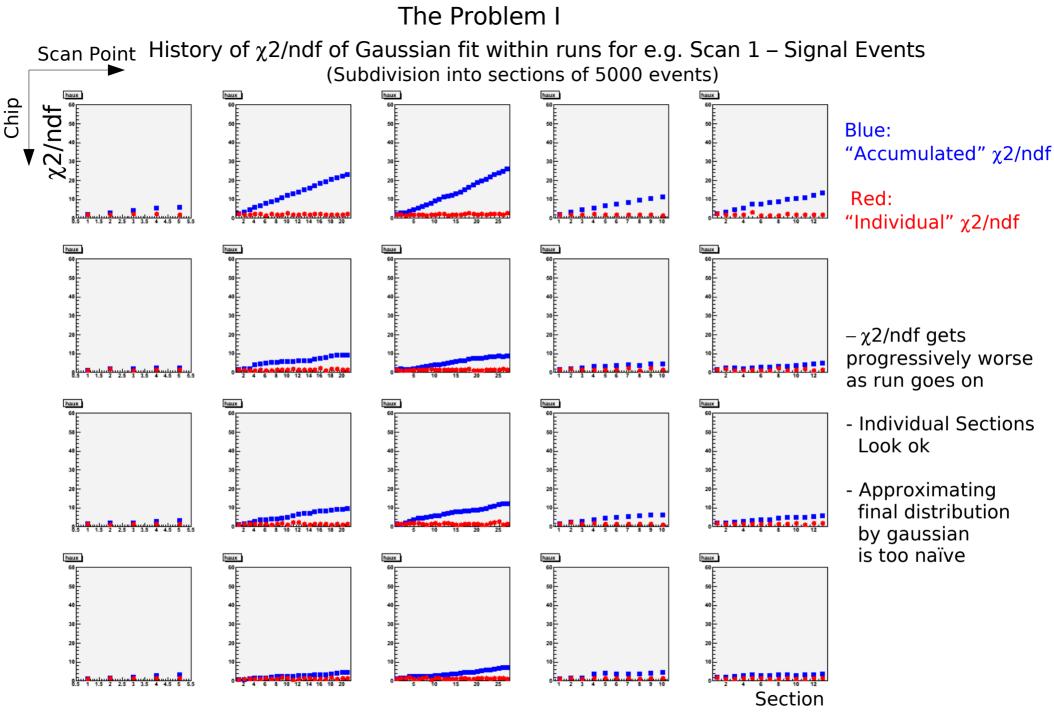
Create pdf of a Chip x from measured Signal spectra when scanned over a Chip opposite to Chip x E.g. spectra for Chip 1 from Scan over Chip 4

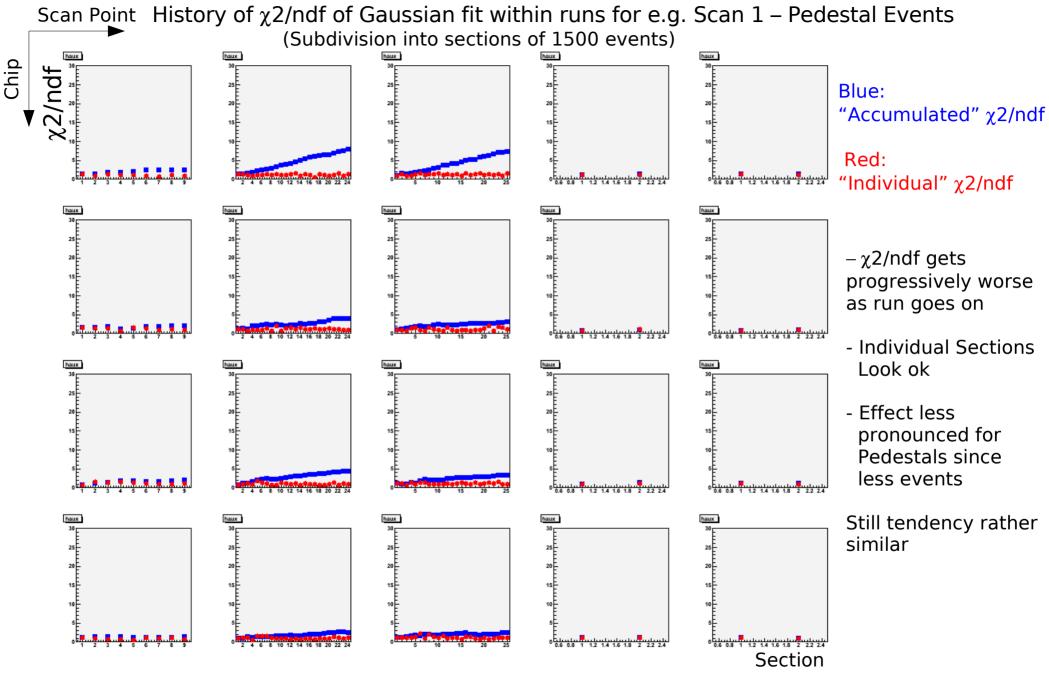
### Starting point today

Absolute Upper Limits as shown on Meeting 8/2/10



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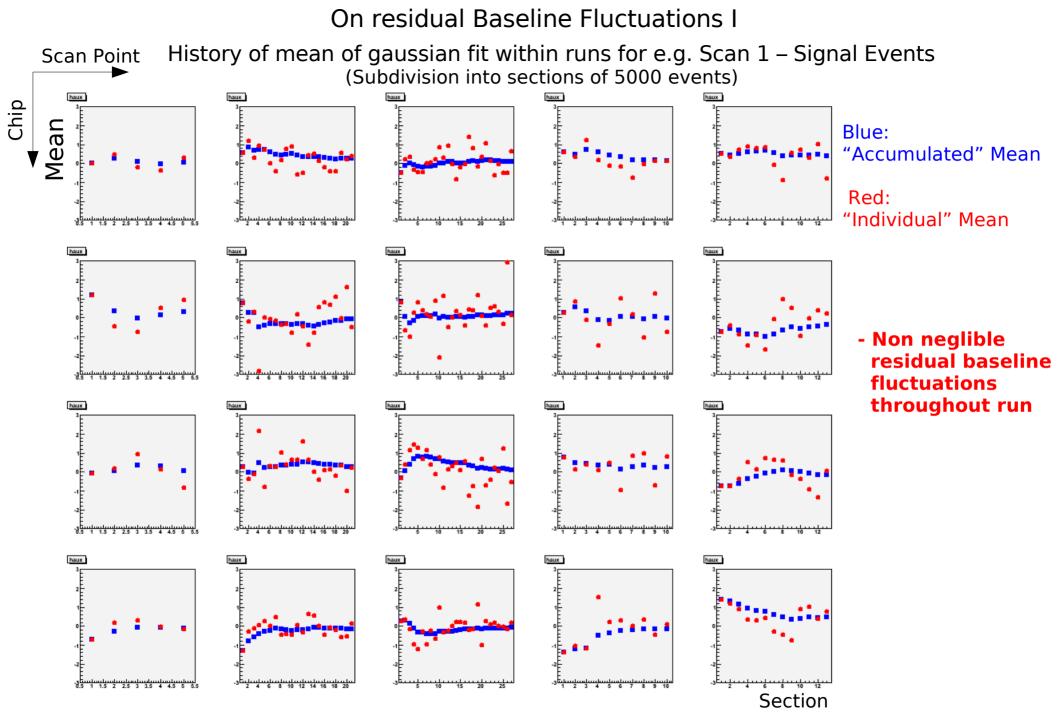


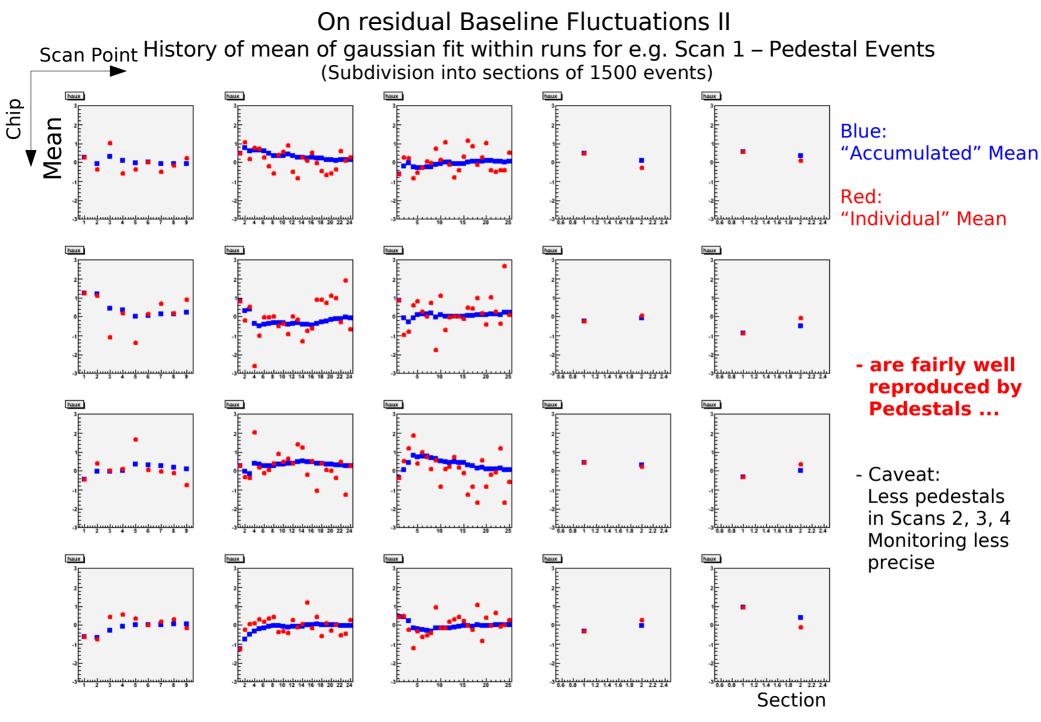
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Two potential reasons for distorted  $\chi^2/ndf$  values

# 1) Residual fluctuations of baseline

2) Coherent Noise (Hint by CdIT)





### On Coherent Noise

(I have investigated) 2 ways to determine coherent noise

1) <u>Simple method</u> based on distribution of direct and alternating sums of noise

$$X_{dir} = \sum_{i=1}^{N} x_i \qquad X_{alt} = \sum_{i=1}^{N} (-1)^i x_i \qquad N = Number of Channels$$

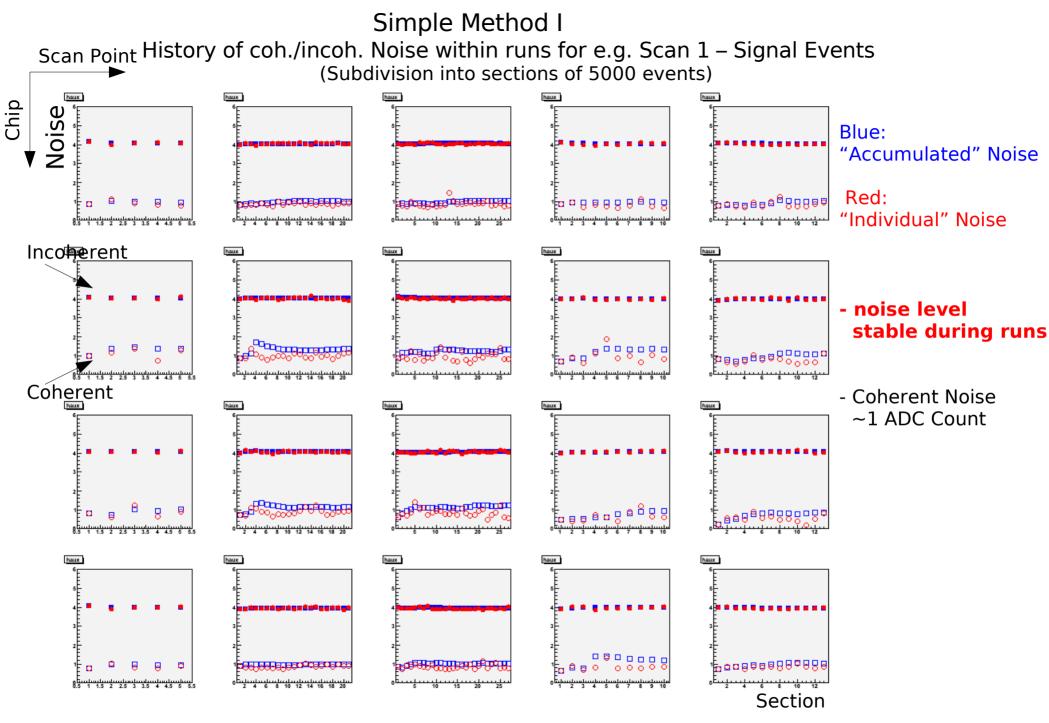
**Incoherent Noise:** 

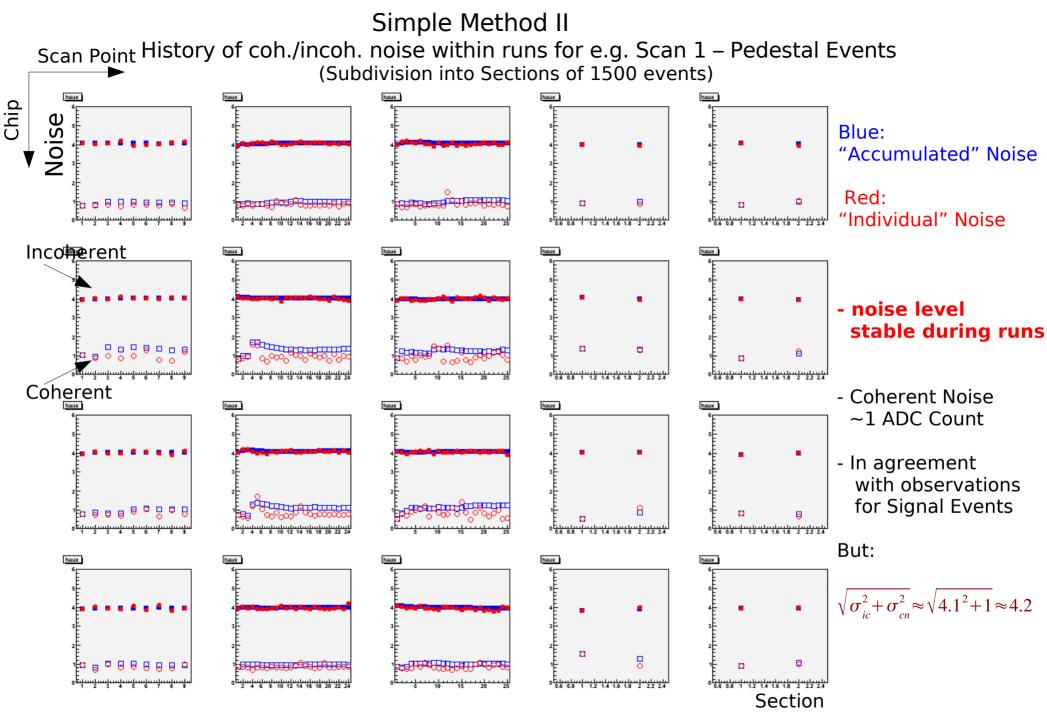
Coherent Noise:

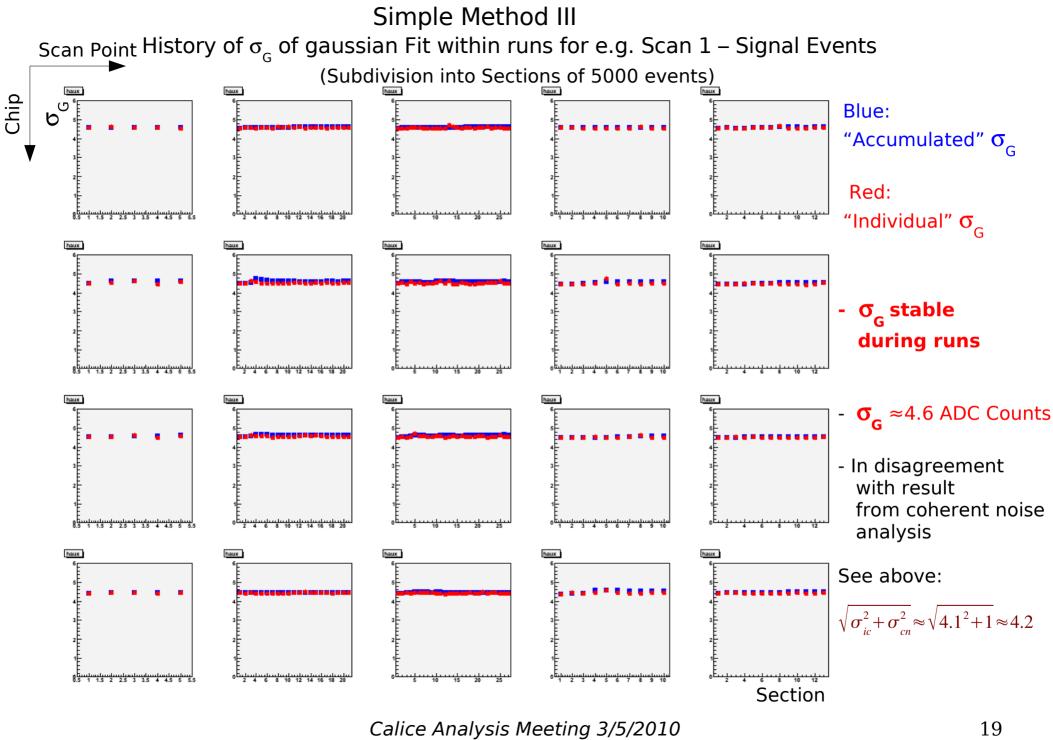
$$X_{ic} = s(X_{alt}) / \sqrt{N}$$
  $X_{cn}^2 = (s^2(X_{dir}) - s^2(X_{alt})) / N^2$ 

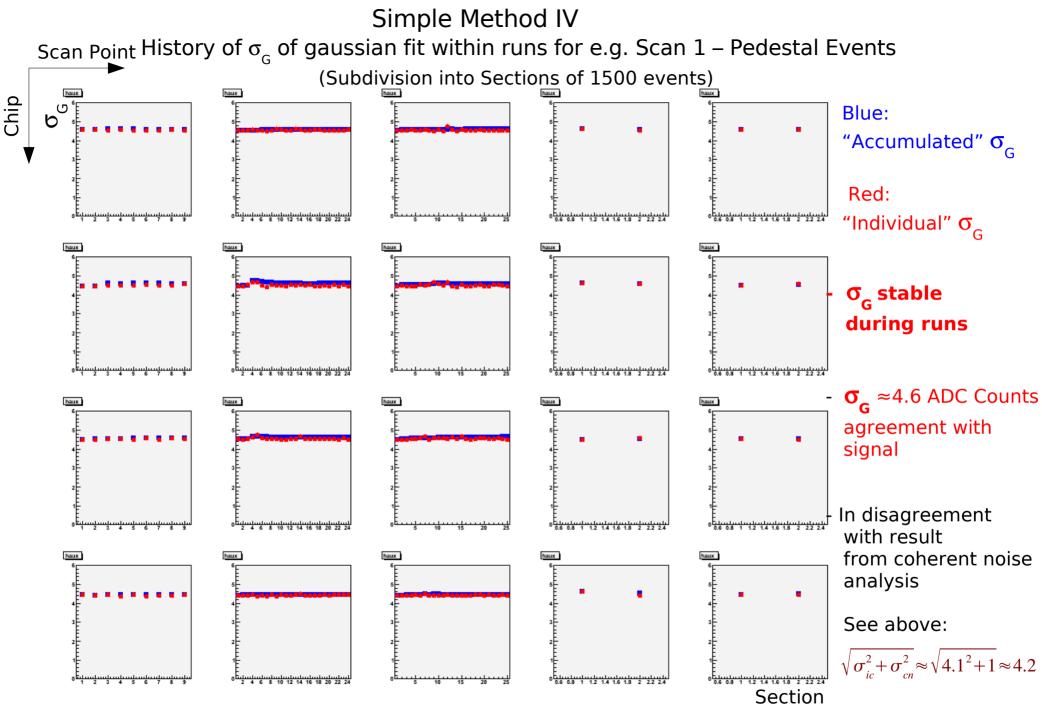
### 2) Principal Component Analysis

Standard technique to find patterns in data of high dimensions Applications in fields such as face recognition and image compression Follow a 'Cooking Recipe' published in ATL-LARG-99-006 (Details see below)









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Simple Method - Conclusions

- Noise analysis exhibits incoherent and coherent noise level consistent for signal and pedestal events

- But:

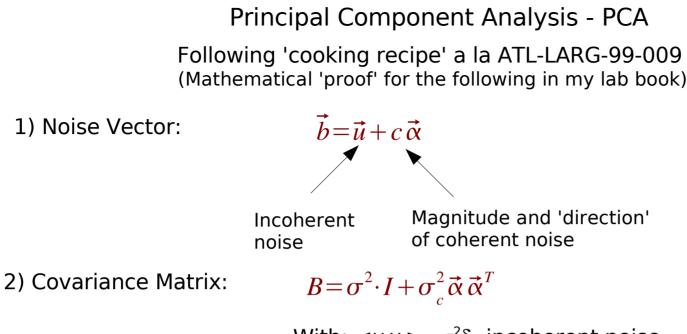
Sum of retrieved coherent and incoherent noise component cannot (!!!) reproduce measured noise level

=> Simple method does not lead to reliable results (General warning for analyses) CALICE has shown results based on simple method at EPS07

Non-Reliability of simple method has been observed/reported in the literature

See e.g.: ATL-LARG-99-006 ATL-TILECAL-PUB-2008-011

=> Need to examine more sophisticated method



With:  $\langle u_i u_j \rangle = \sigma^2 \delta_{ij}$  incoherent noise  $\sigma_c^2 =$  Variance of c

3) Eigenvalues and Eigenvectors of covariance matrix:

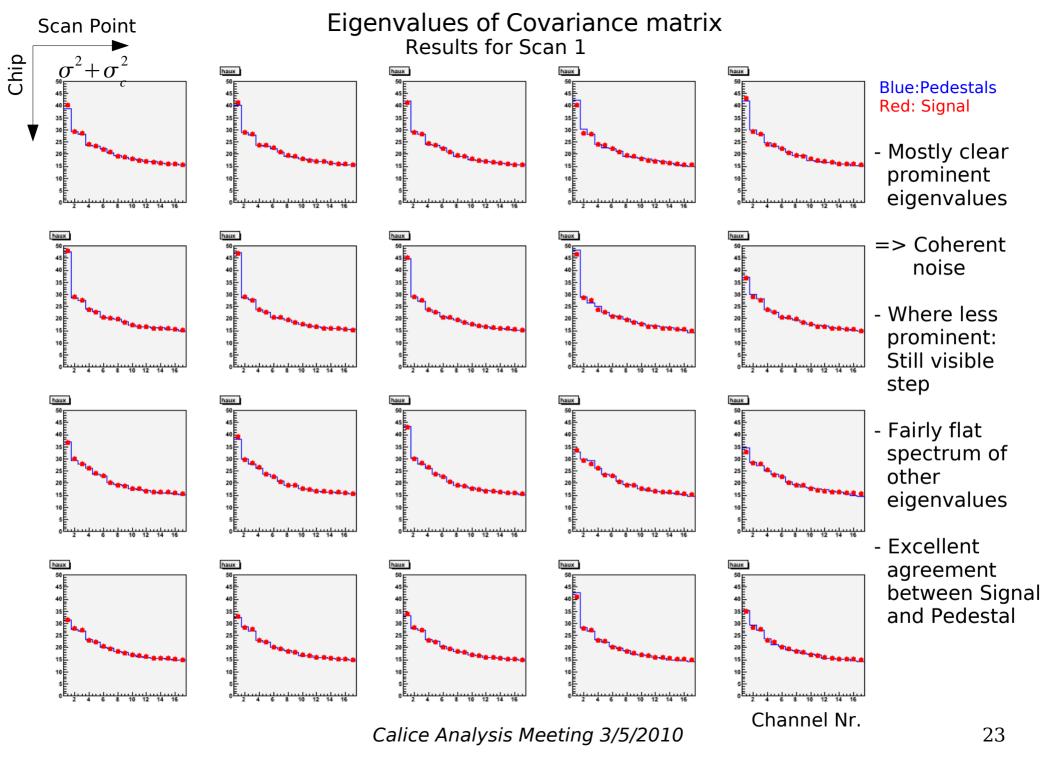
 $\vec{\alpha}$  is eigenvector to (largest) eigenvalue  $\sigma^2 + \sigma_c^2$ 

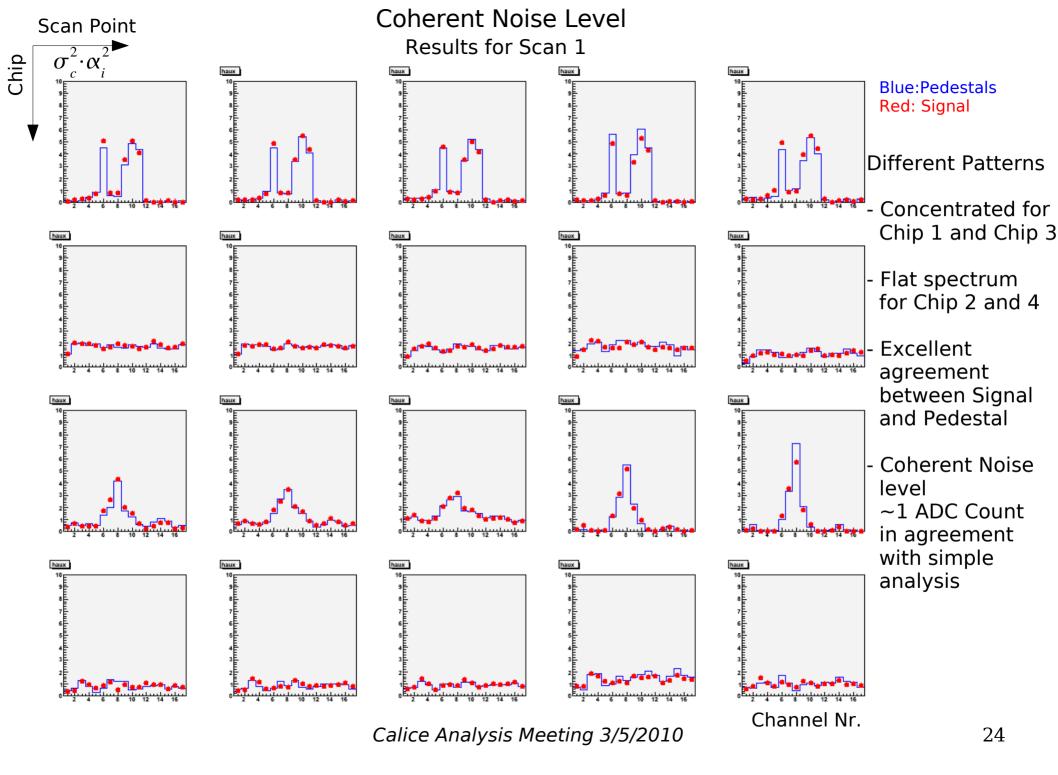
Any other eigenvector (orthogonal) to  $\vec{\alpha}$  is eigenvector to eigenvalue  $\sigma^2$ 

=> Expect flat spectrum of matrix B except for one (or few) eigenvalue associated with  $\alpha$ Coherent channel noise via  $\sigma_{c} \cdot \vec{\alpha}$ 

4) Incoherent Channel noise via:  $B' = B - \sigma_c^2 \alpha \alpha^T$ 

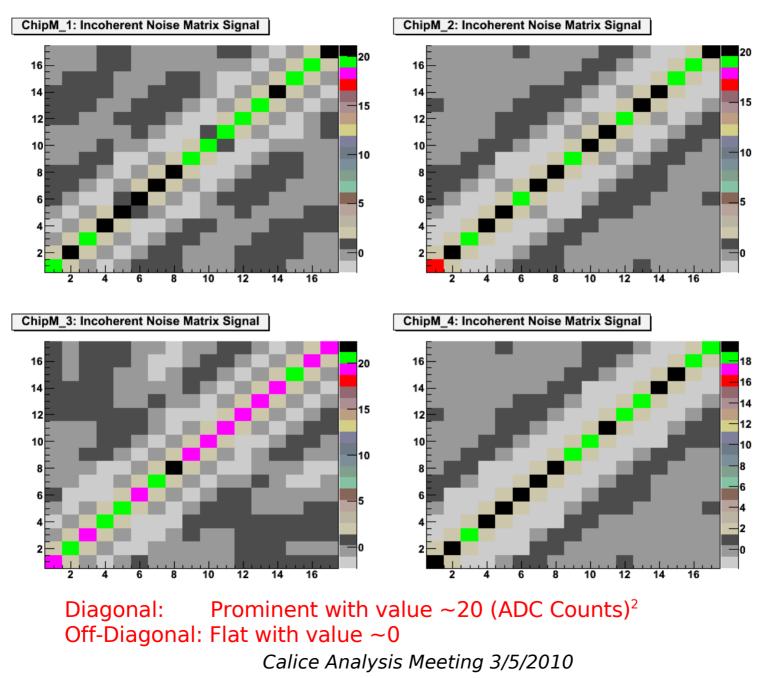
=> Matrix with incoherent channel noise on diagonal and off-diagonal elements flat and zero

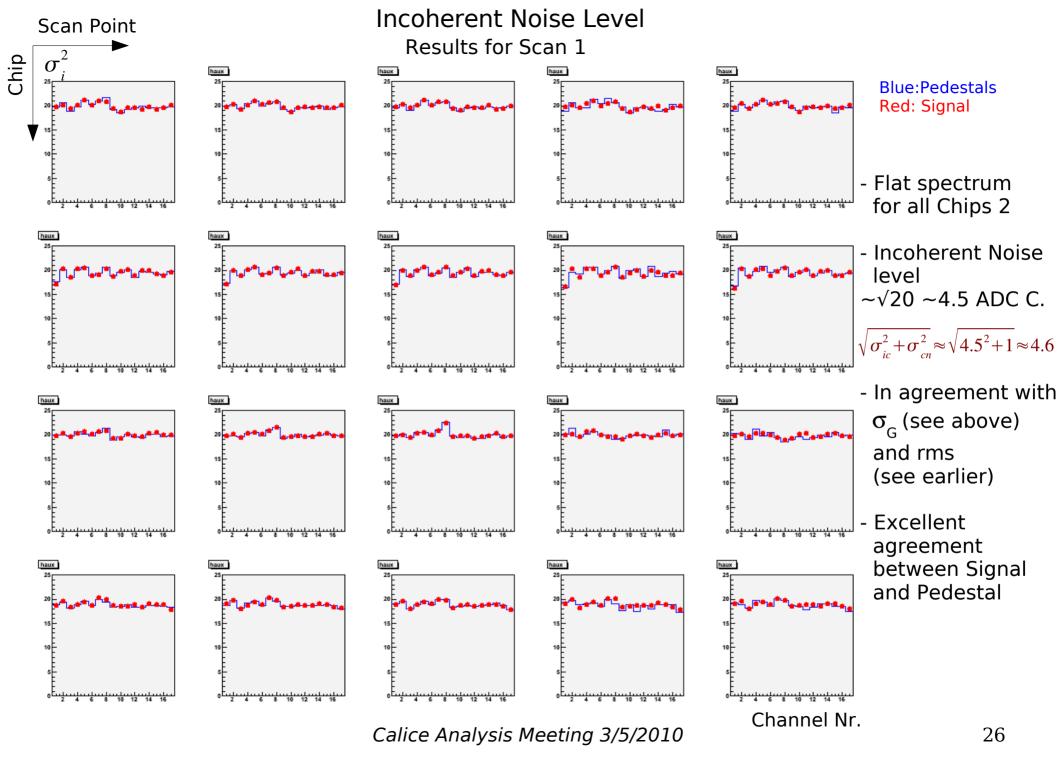




### Matrix B'

### Example for central impact Scan 1 – Signal Events





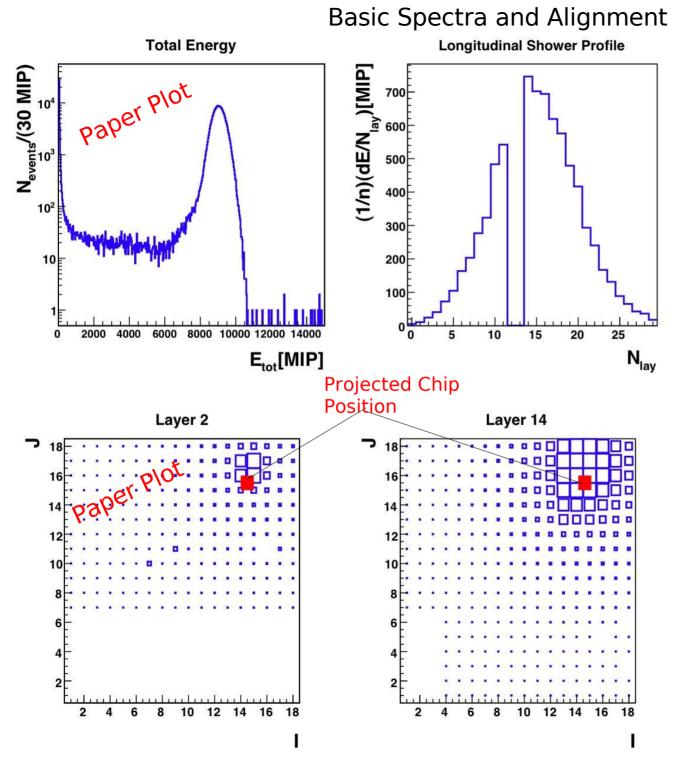
### Summary and Conclusion

- No new limits today :-(

### But ...

- Detailed monitoring of mean of noise distributions and noise analysis based on PCA gives confidence that pedestals can well be used to model the chip responses (In contrast to my earlier worries)
- I think I know how (Not shown/realised part 5 of cooking recipe) stay tuned
- Analysis still gives no evidence that beam in VFE distorts signals What is missing is the full quantitative result <=> limits (see above)
- As fruitful side goodie of analysis:
  - Algorithms at hand for "professional" noise analysis of at least all analogue calorimeters of calice!!!!
  - Can be transformed into general "Noise analysis suite" (after cleaning and structuring of my code)
  - Be very careful with simple analysis to obtain coherent noise (Not reliable)

# **Backup Slides**



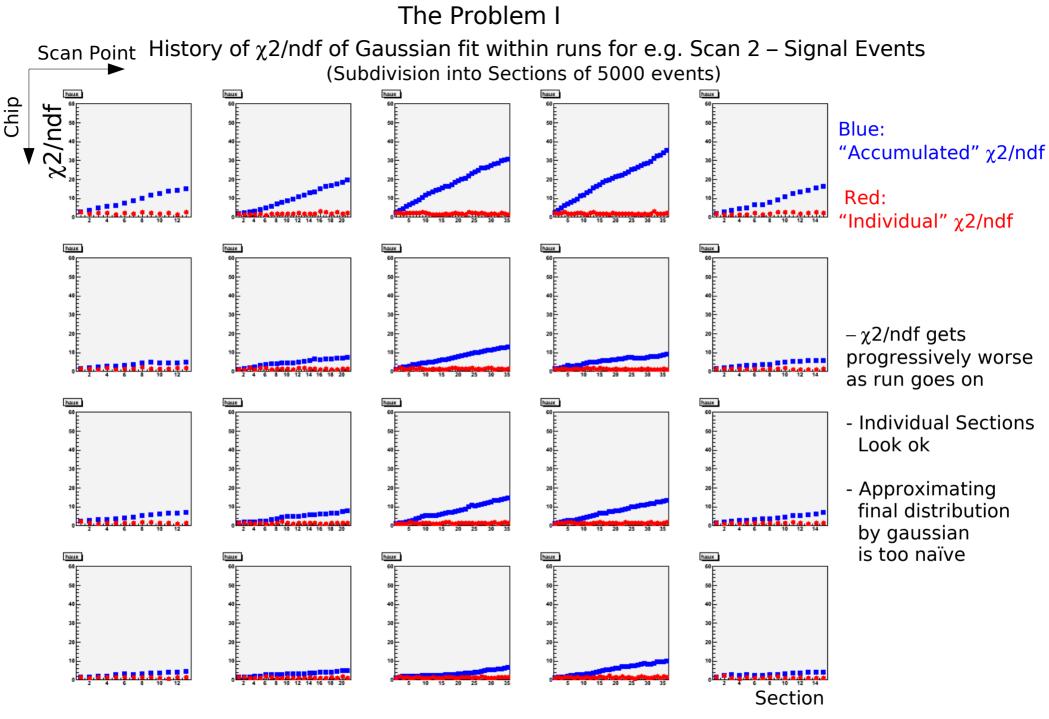
#### 90 GeV run (331495)

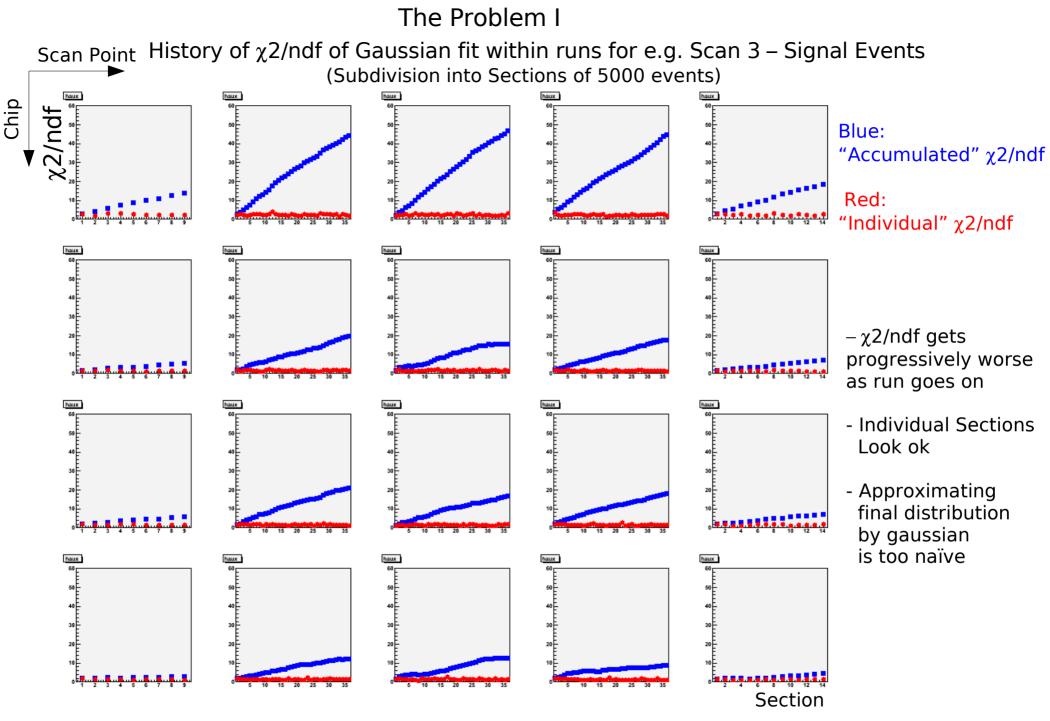
- Clear Energy Peak
- Special Board place at
  - ~ shower maximum

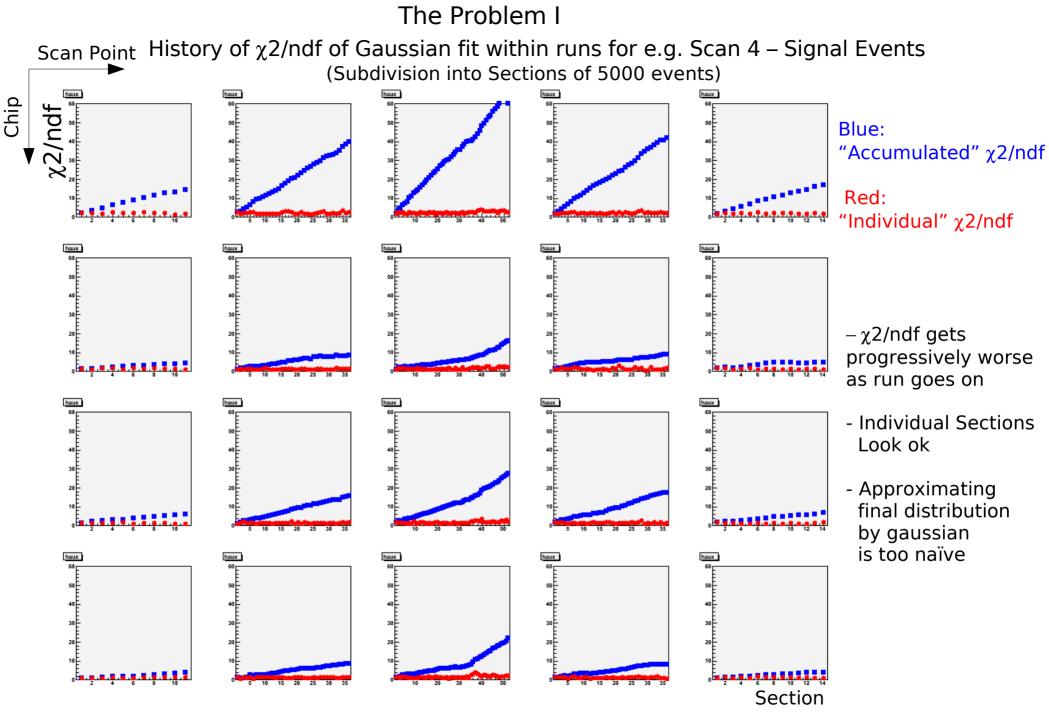
### Hit Maps

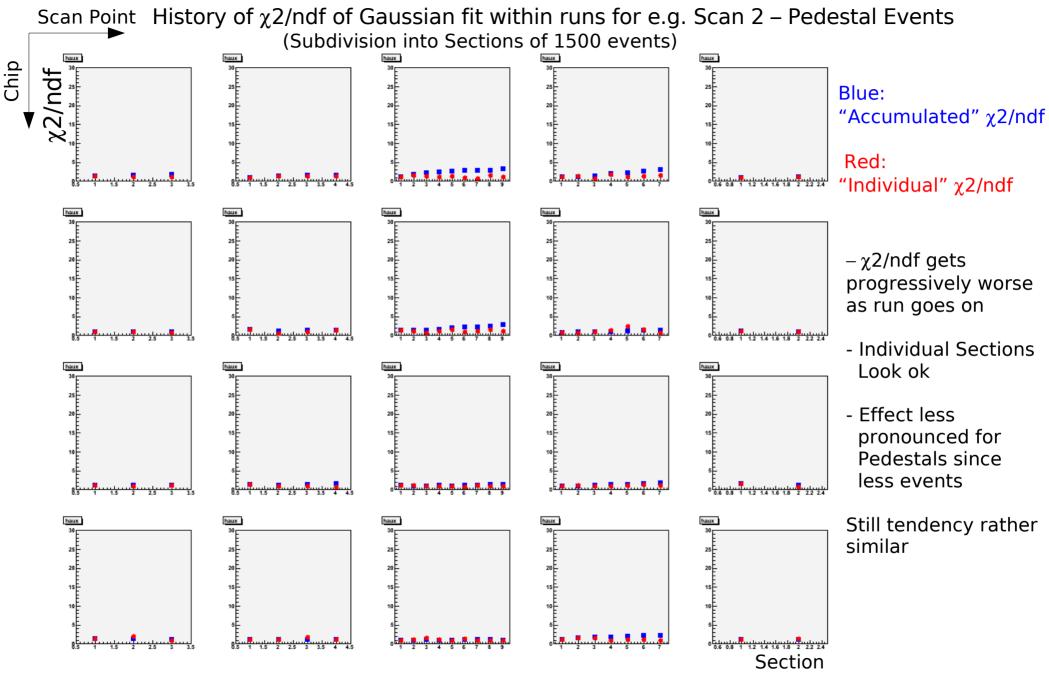
- Layer 2
  - Same xy-Position as Special Board
- Layer 14 First instrumented Layer after Special board

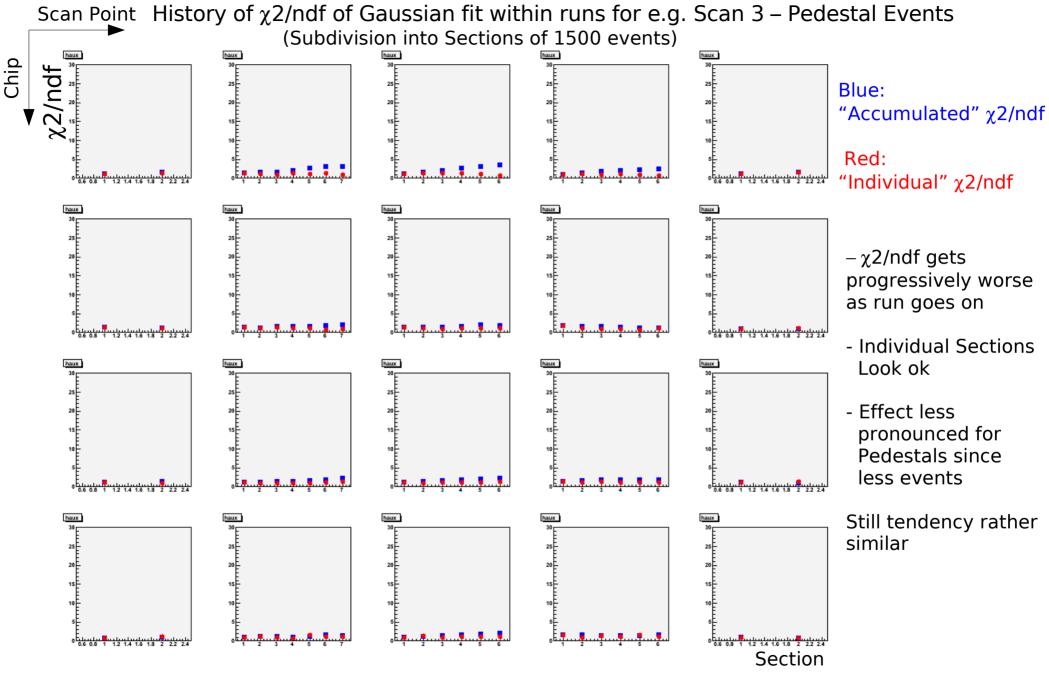
Chip(s) well within lateral shower extension



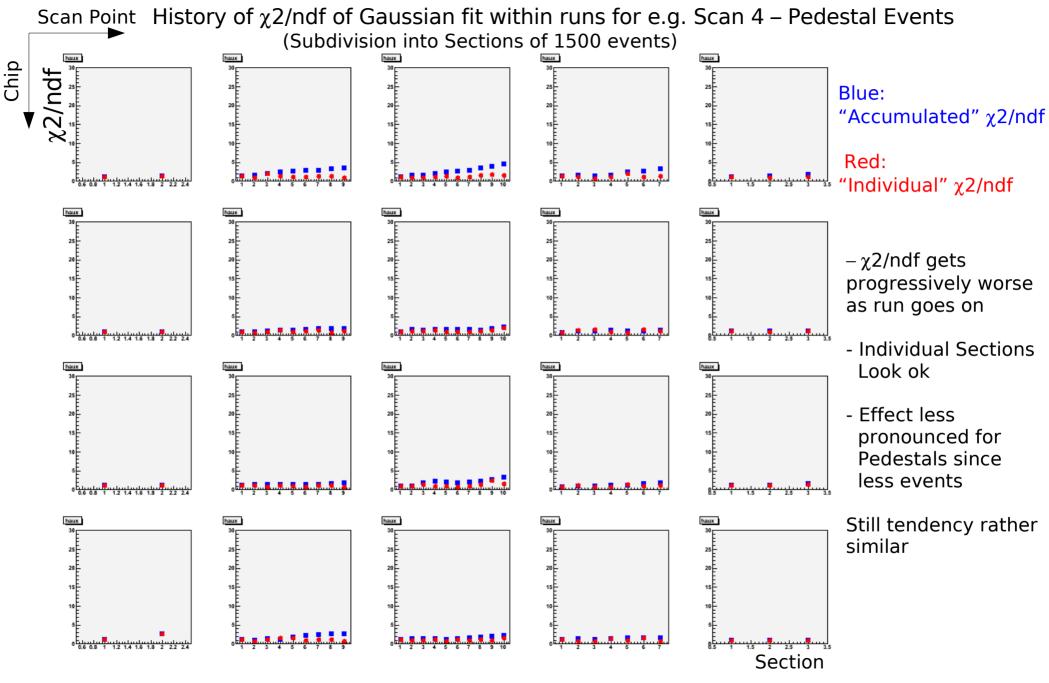


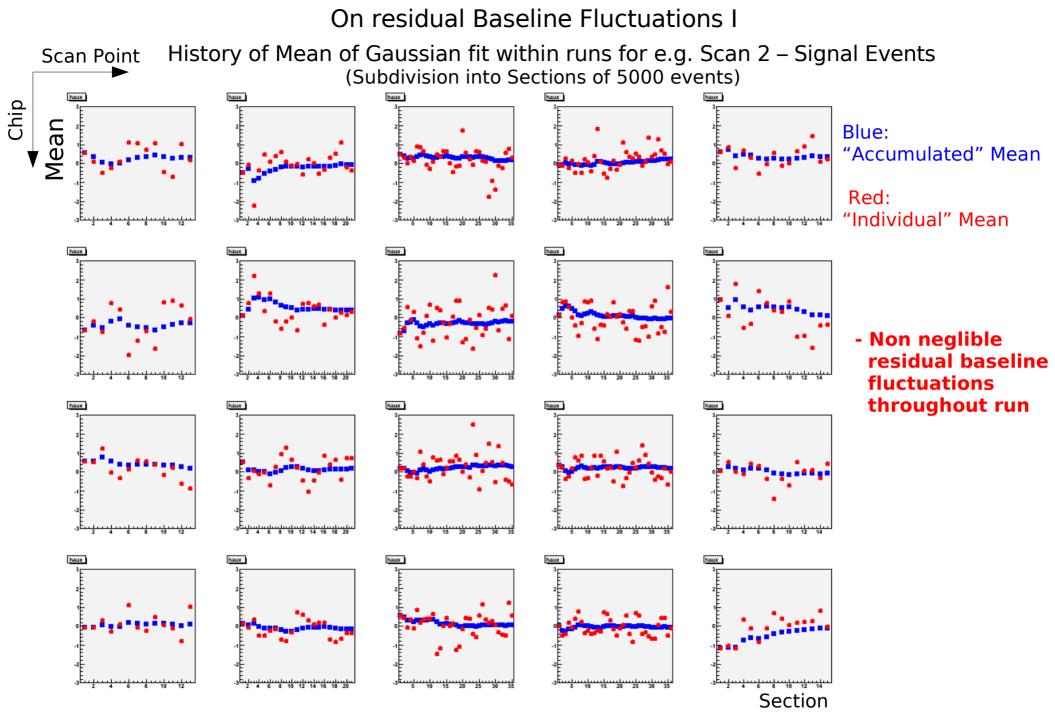


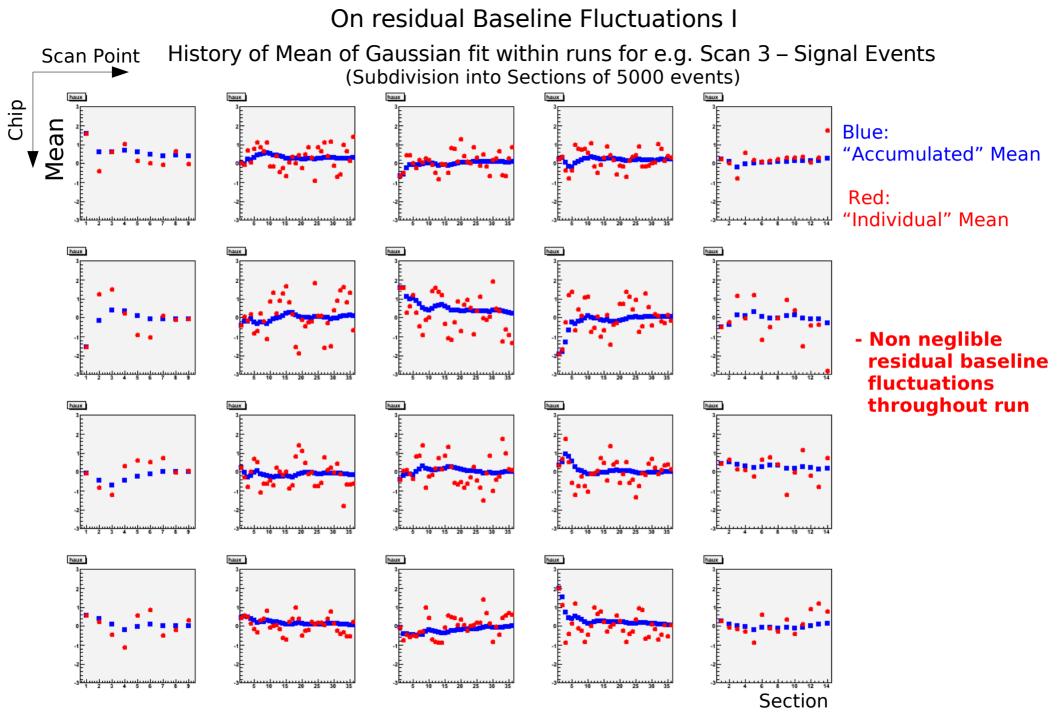


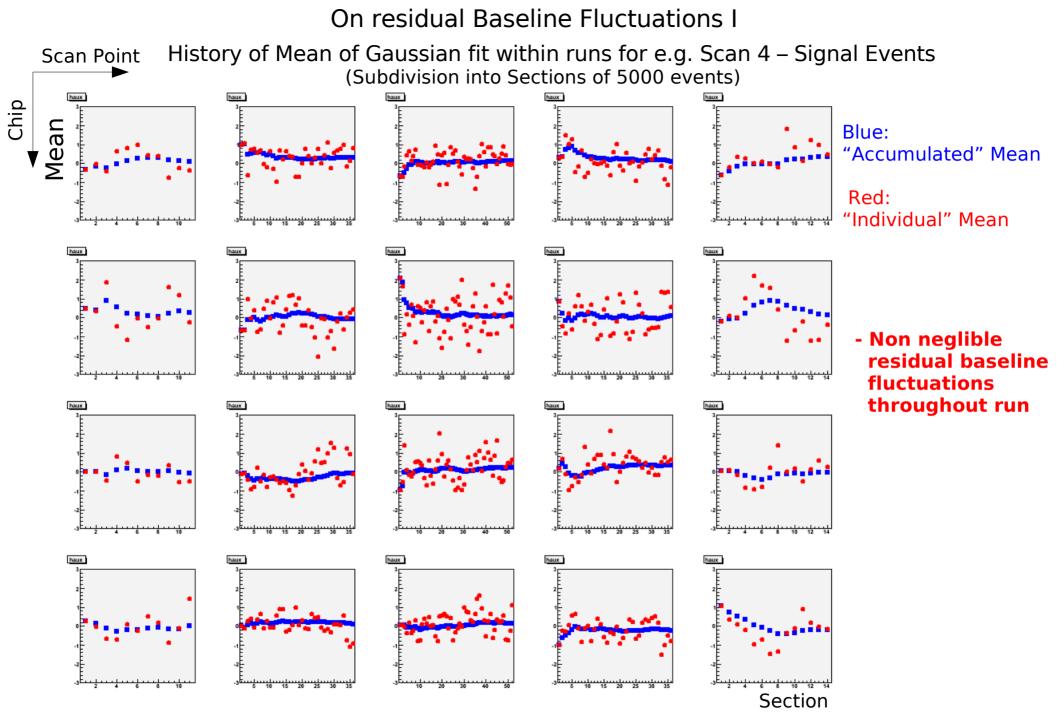


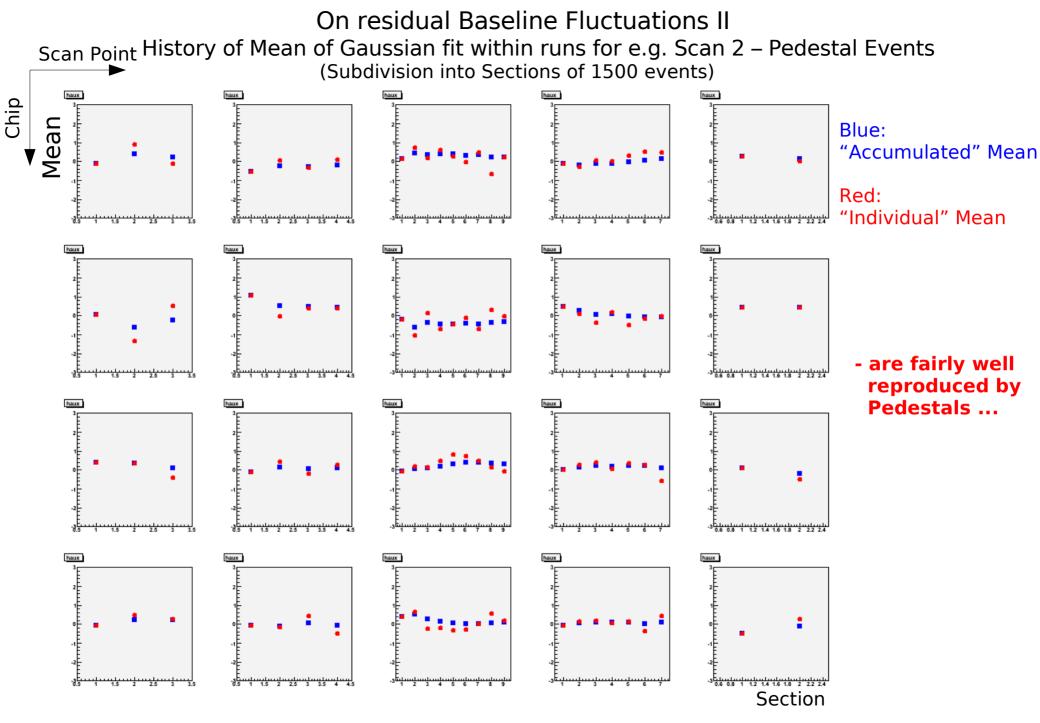
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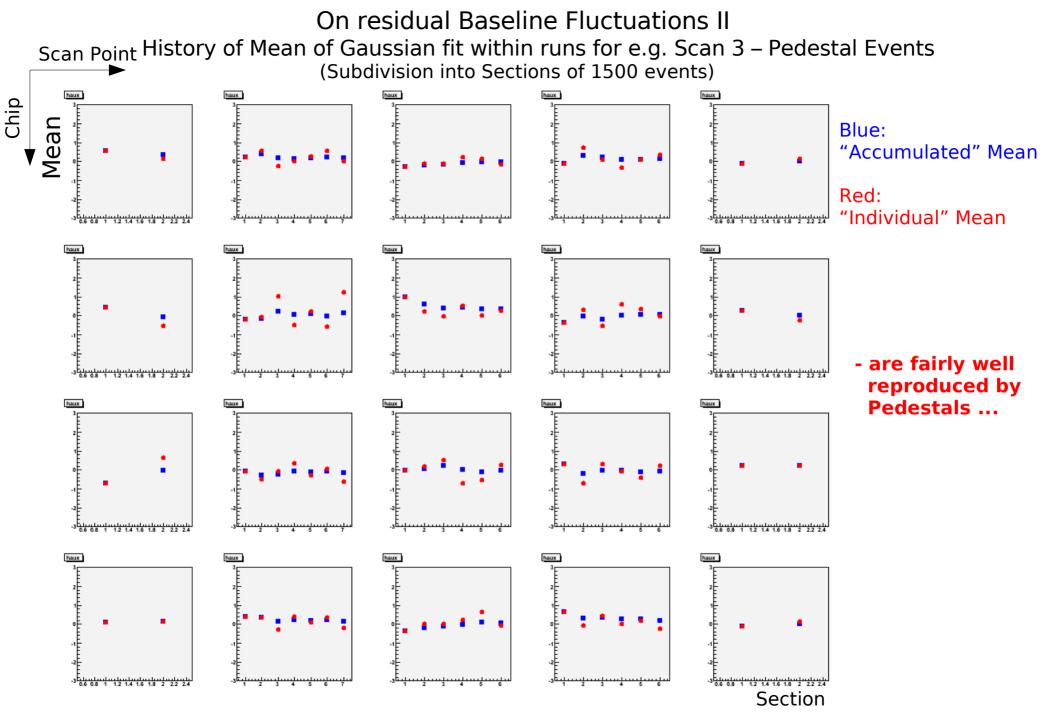




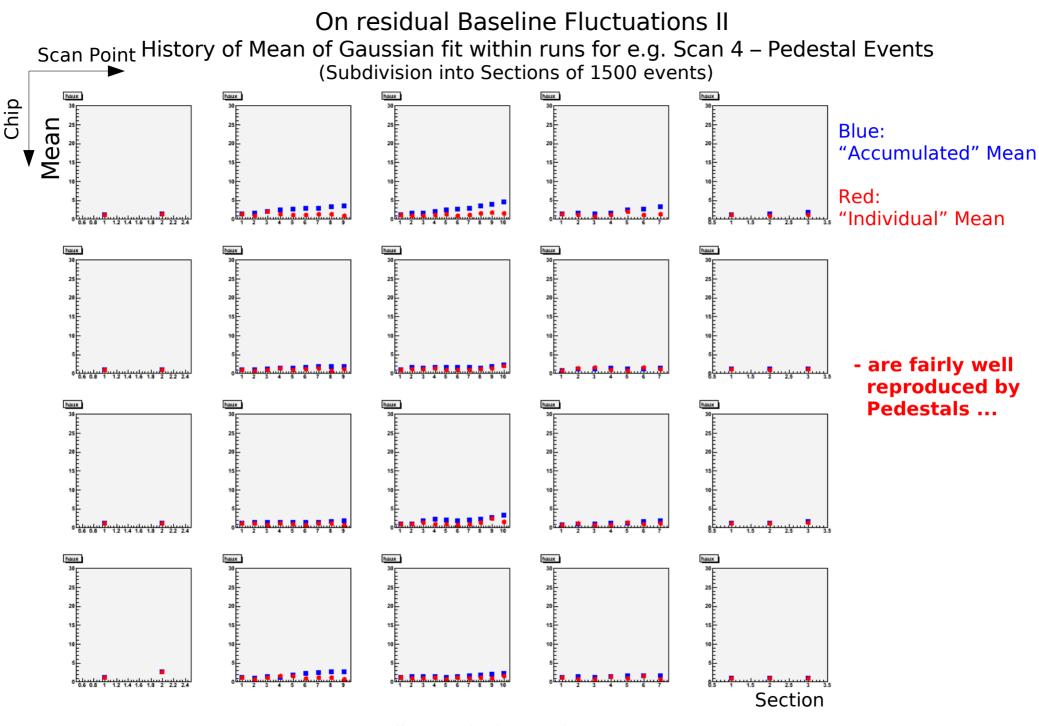


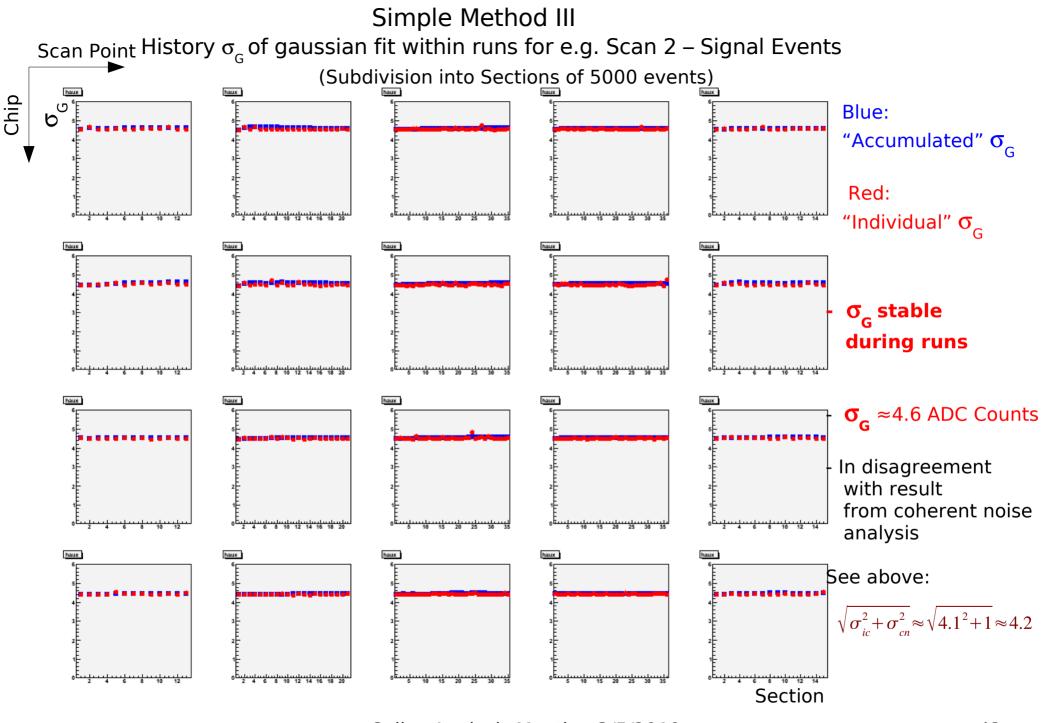


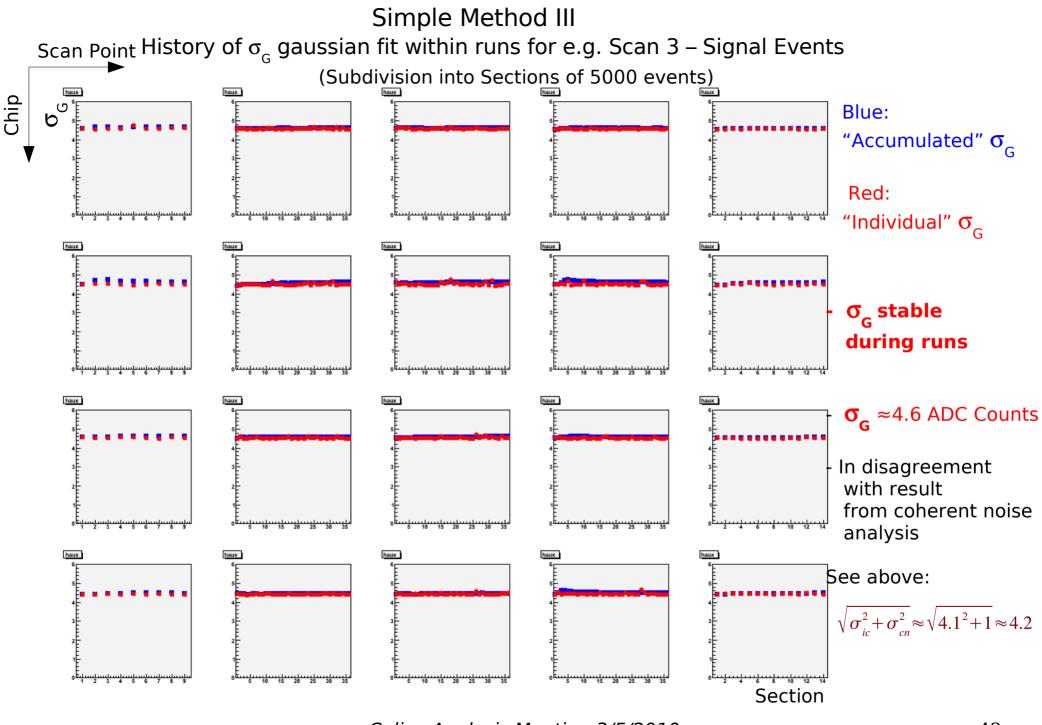
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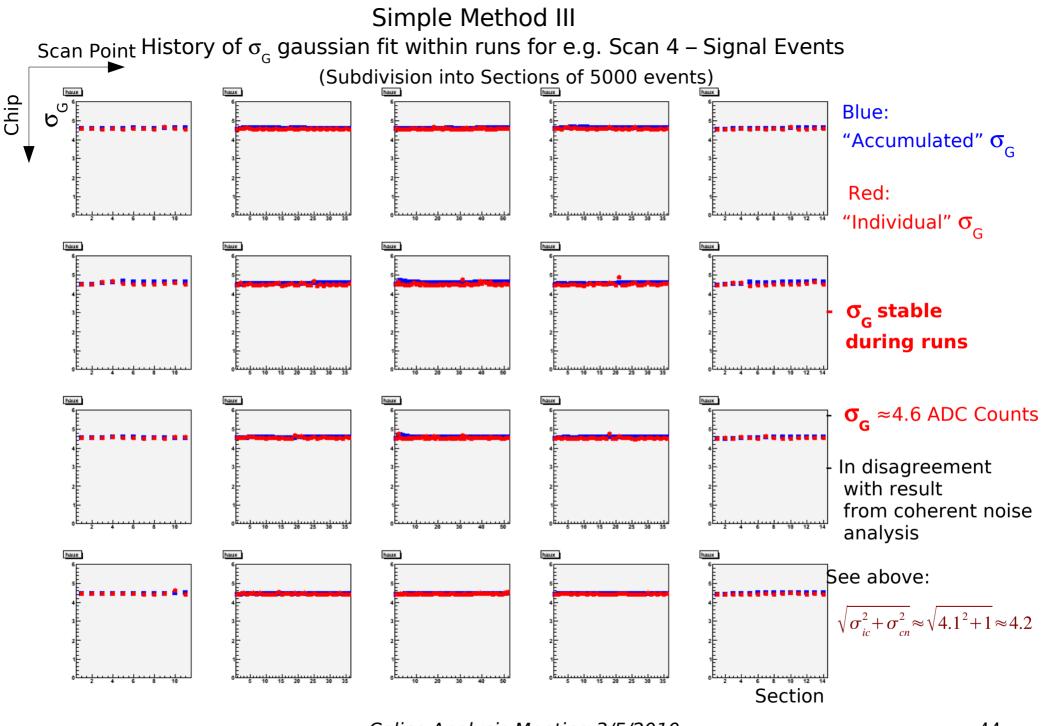
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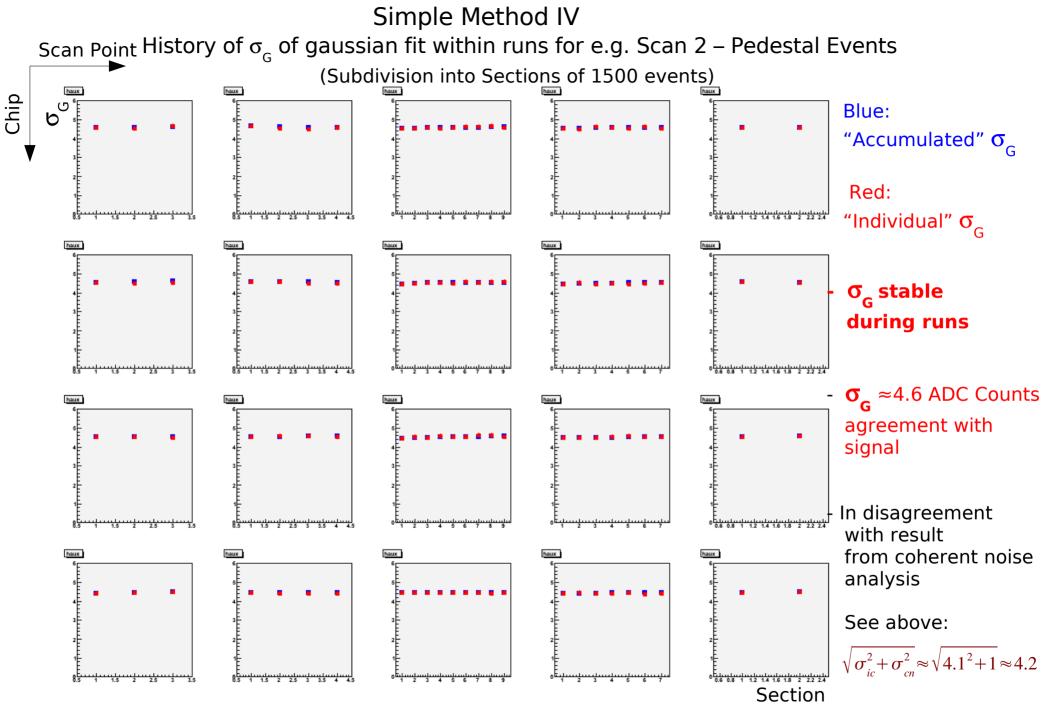


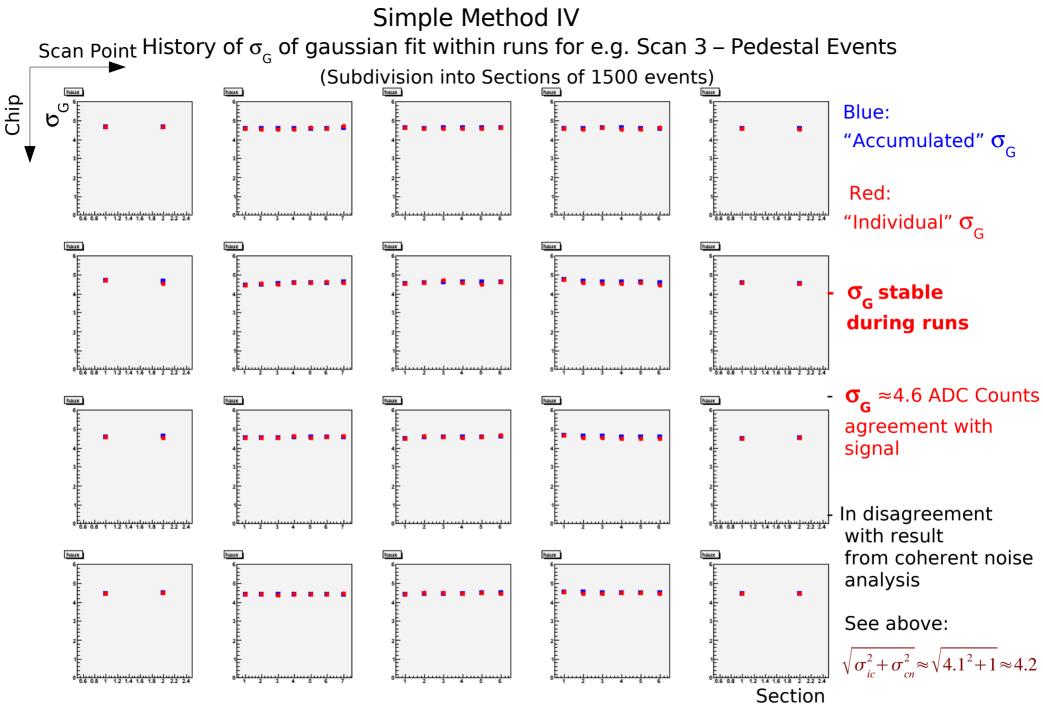


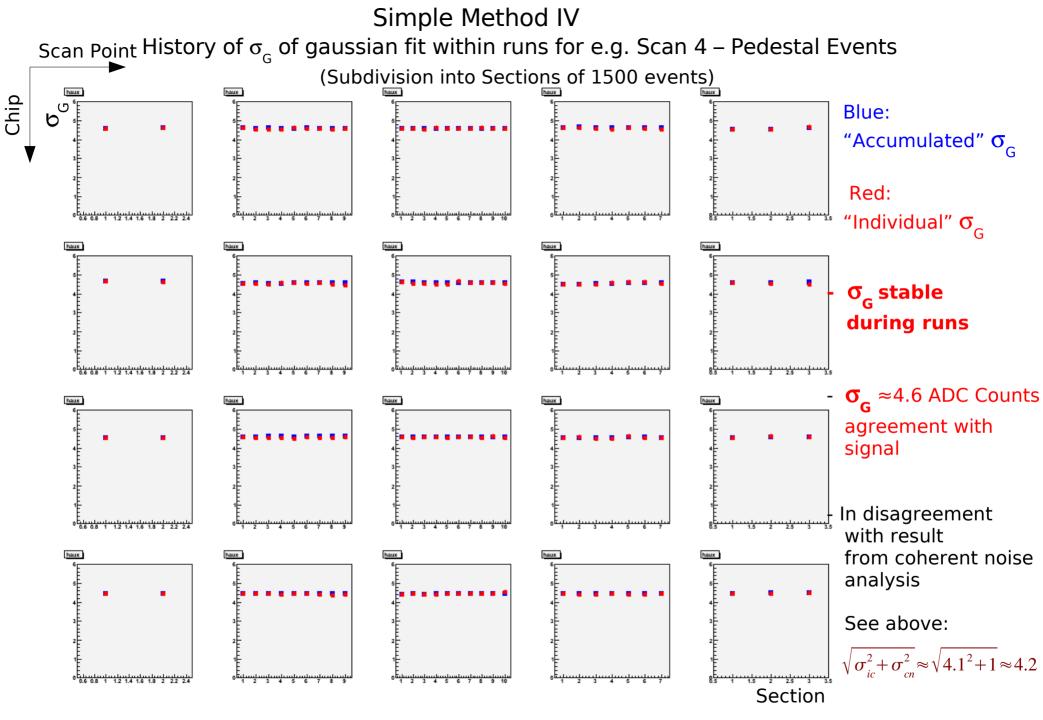
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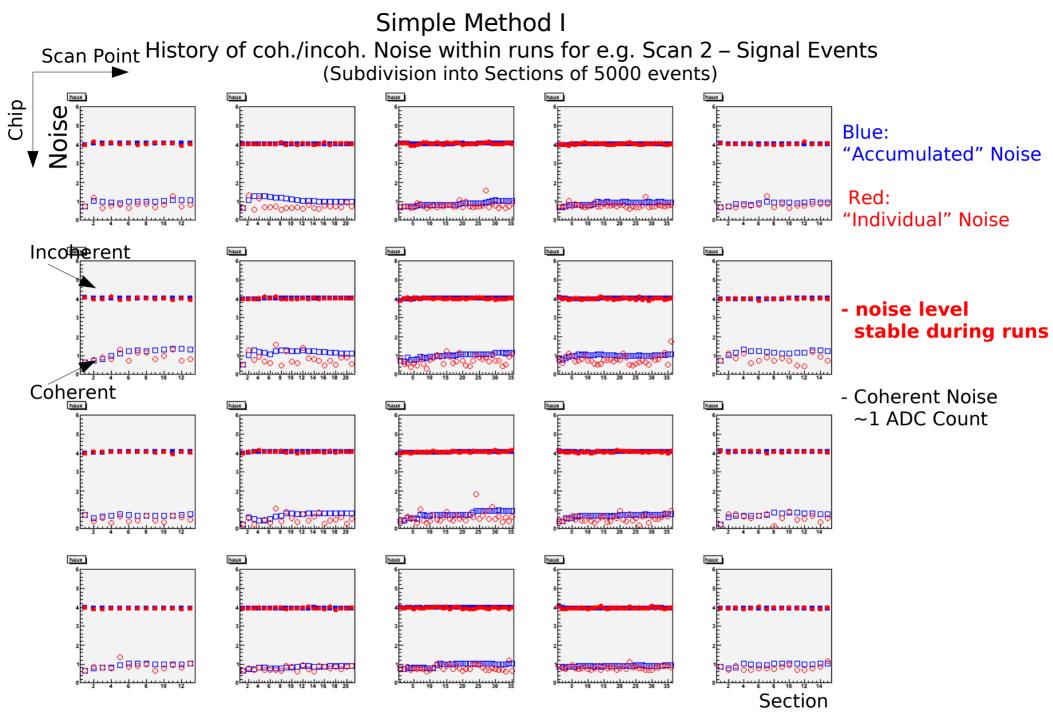
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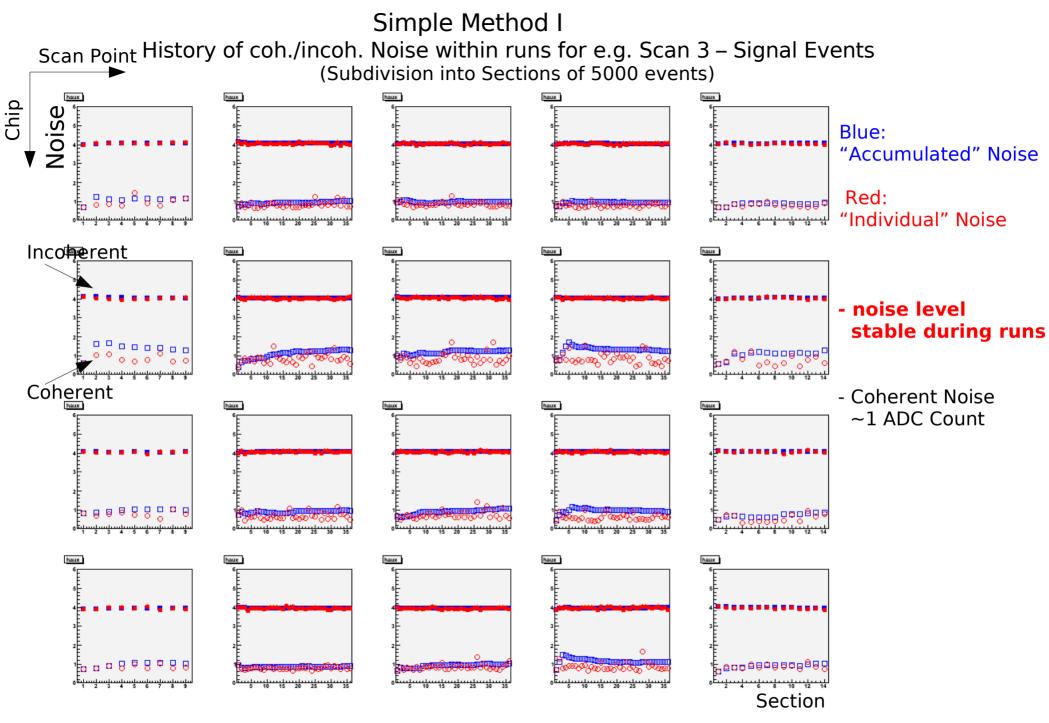


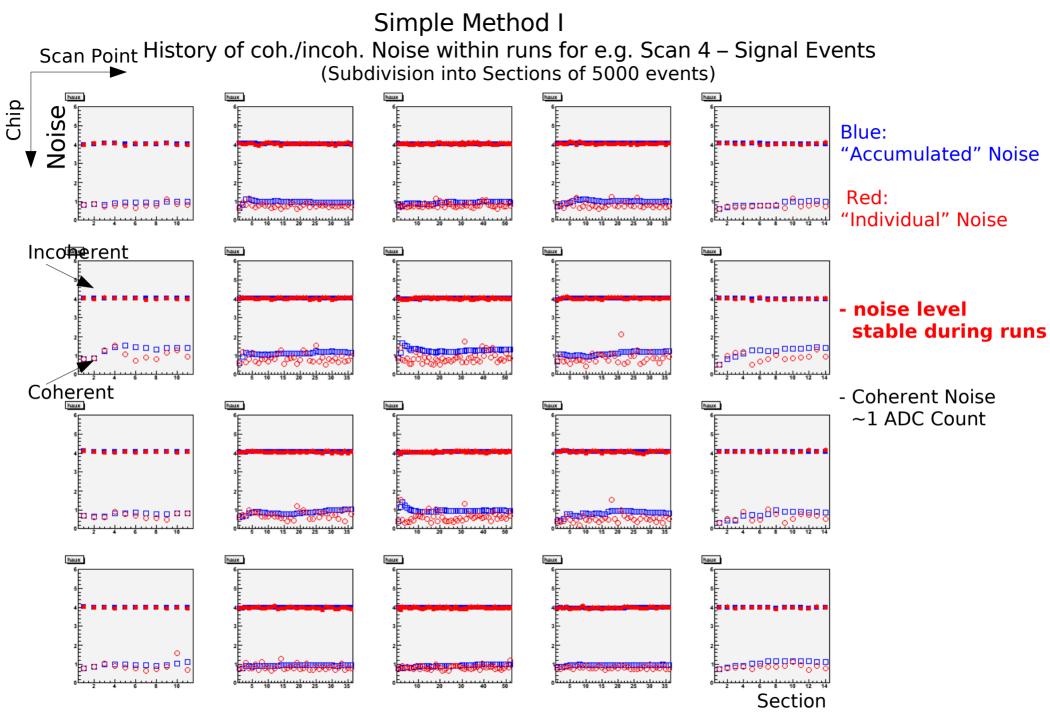


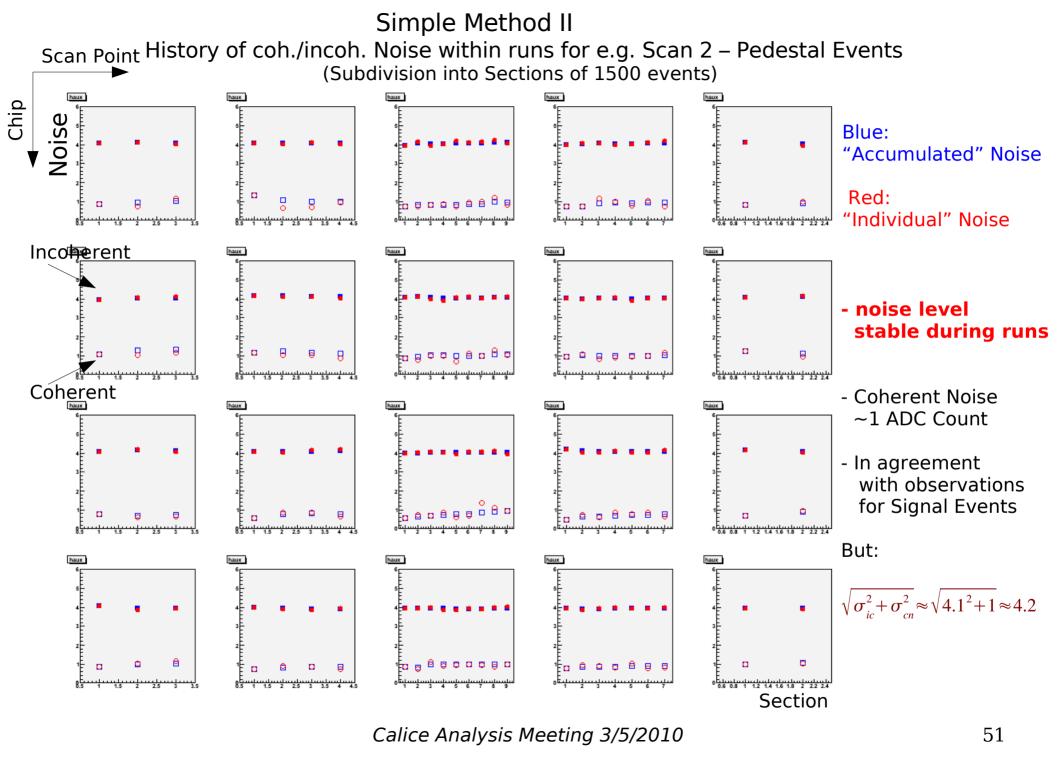


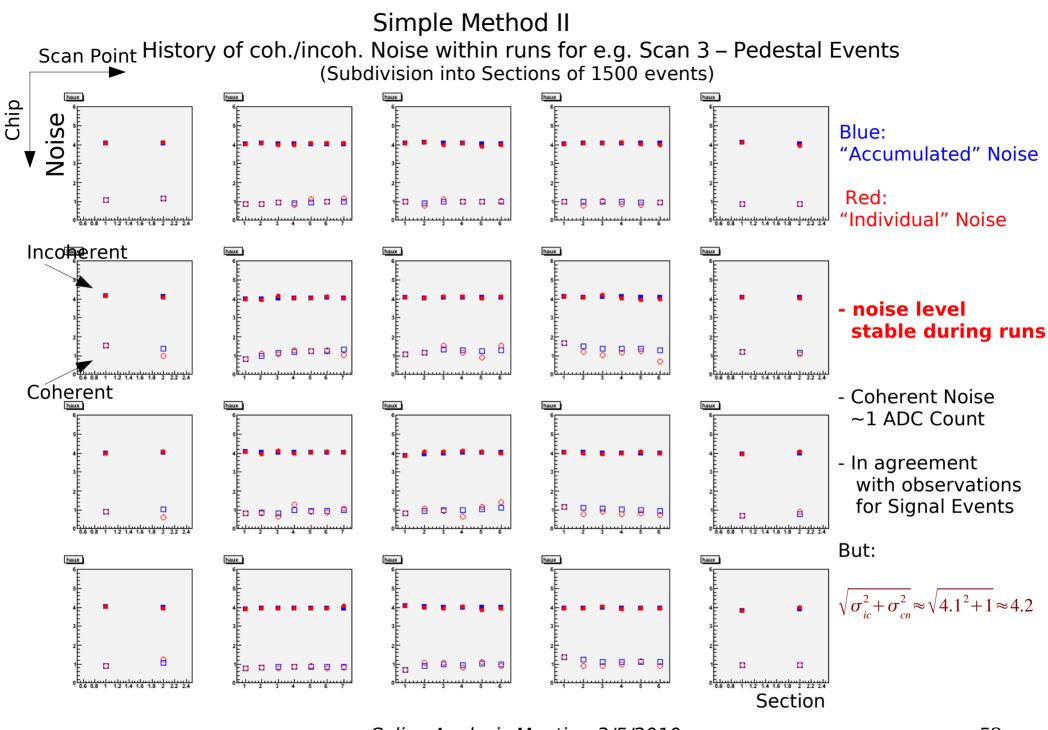
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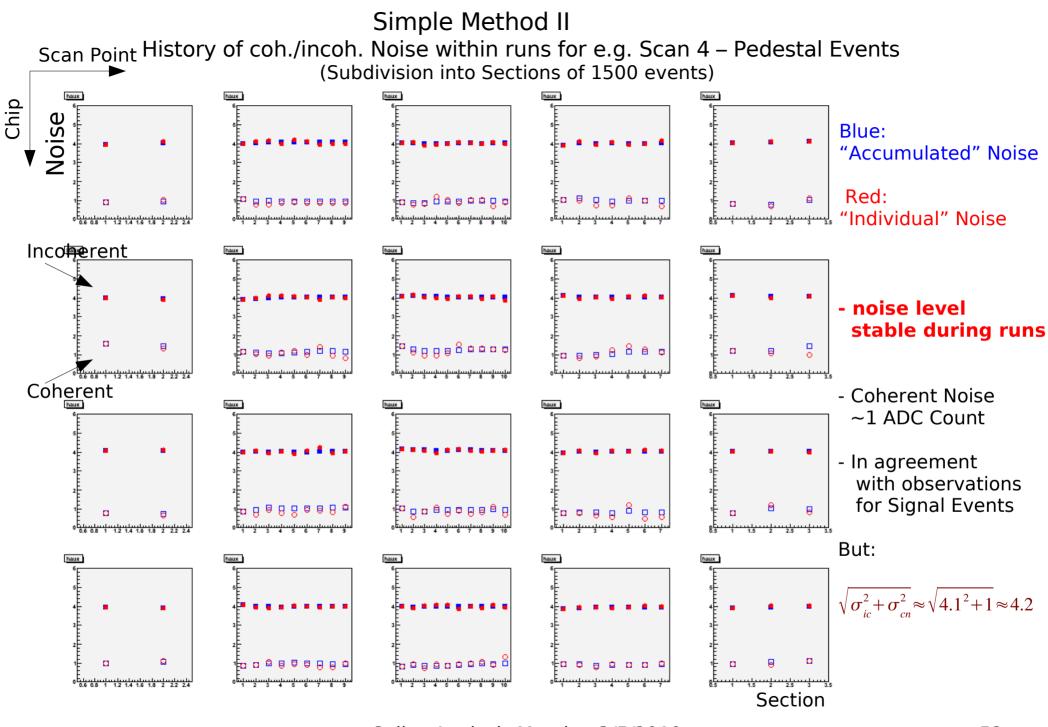


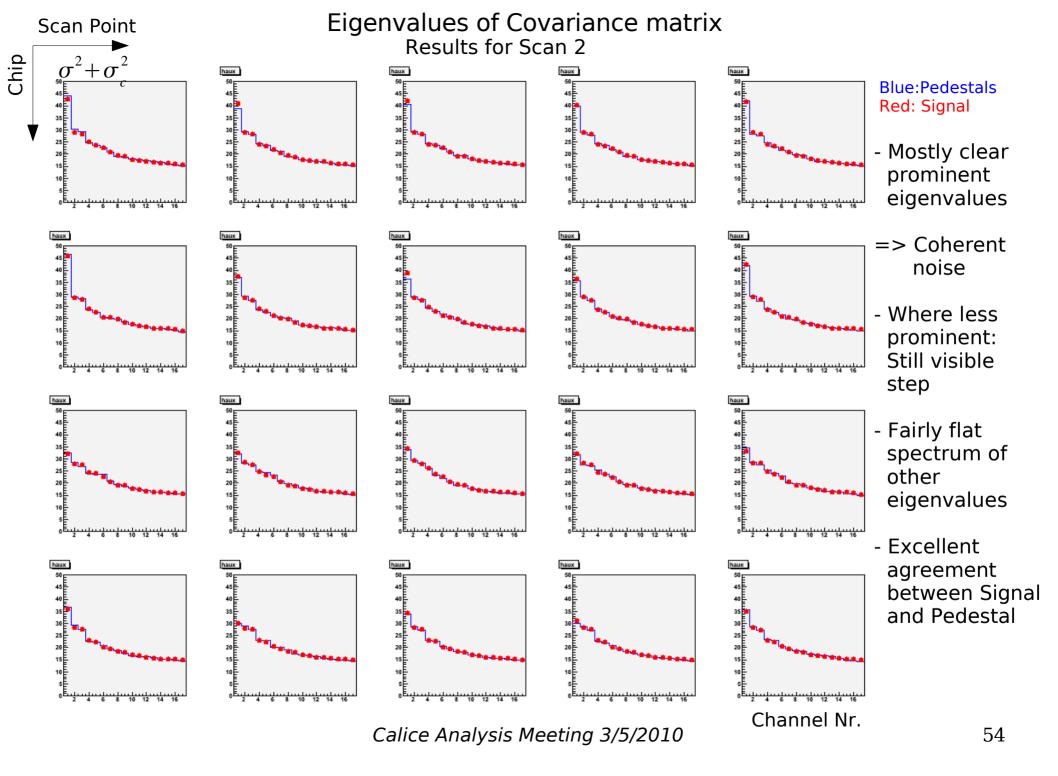


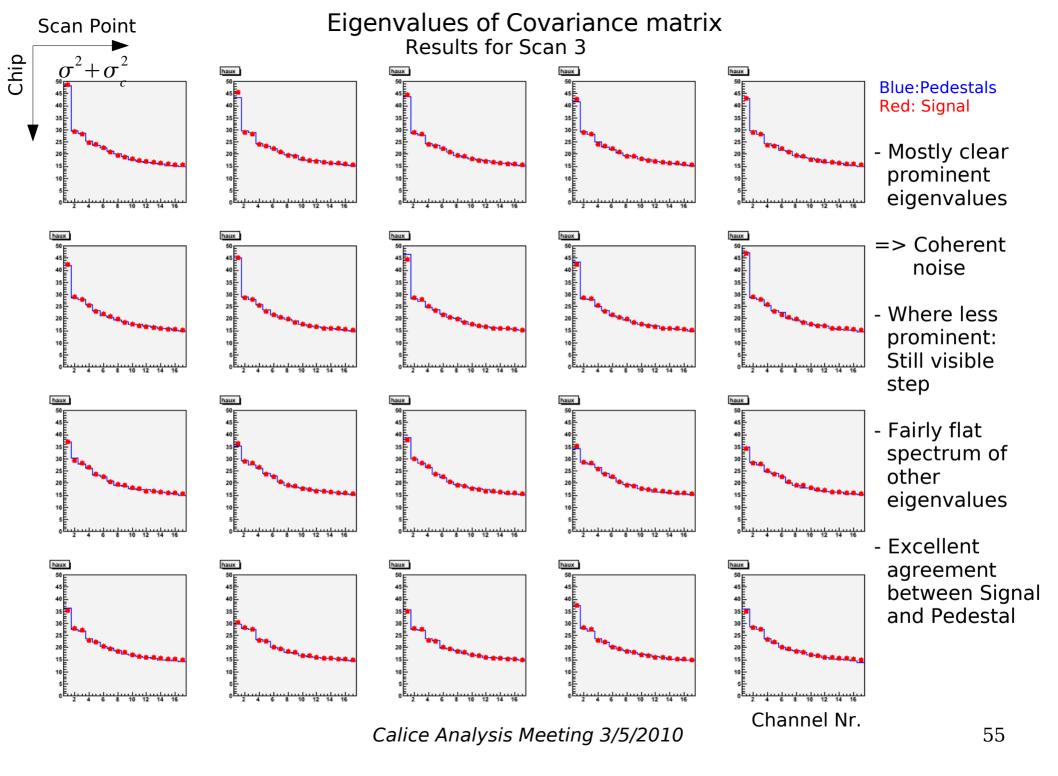


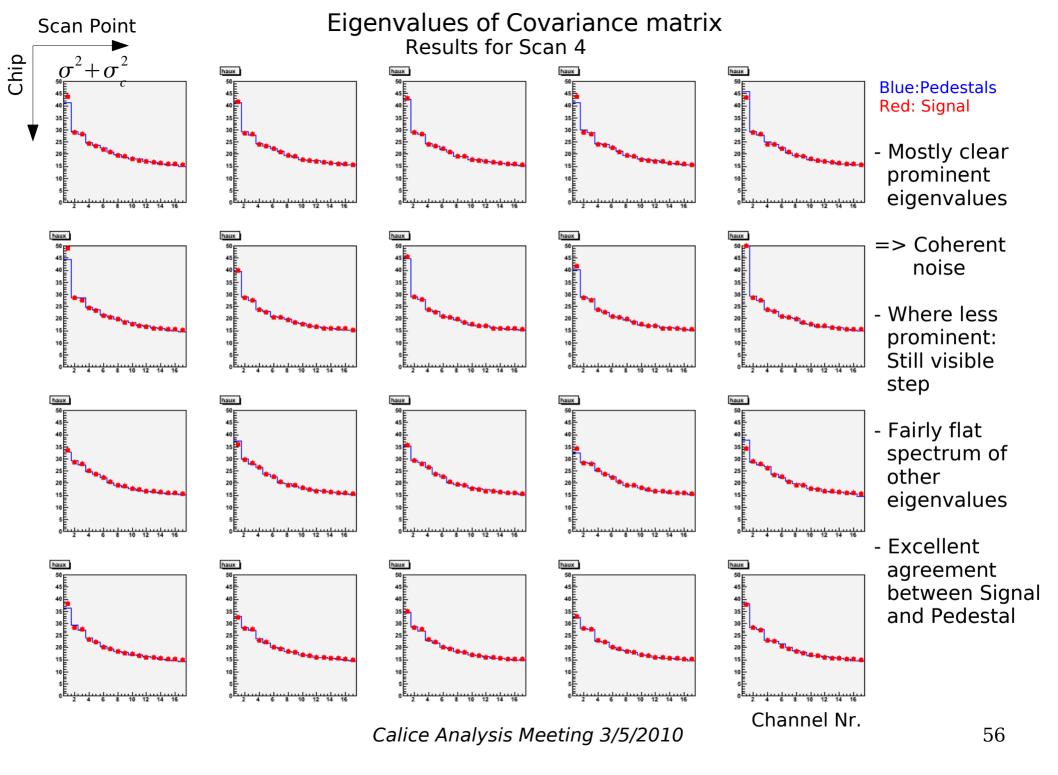


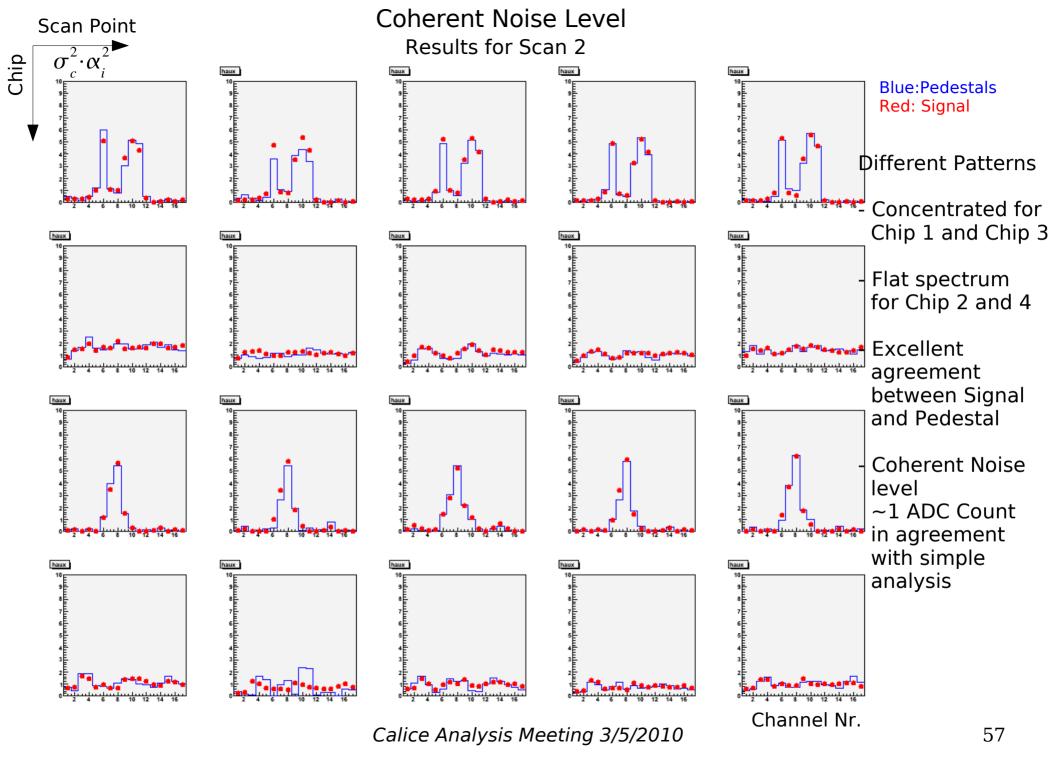


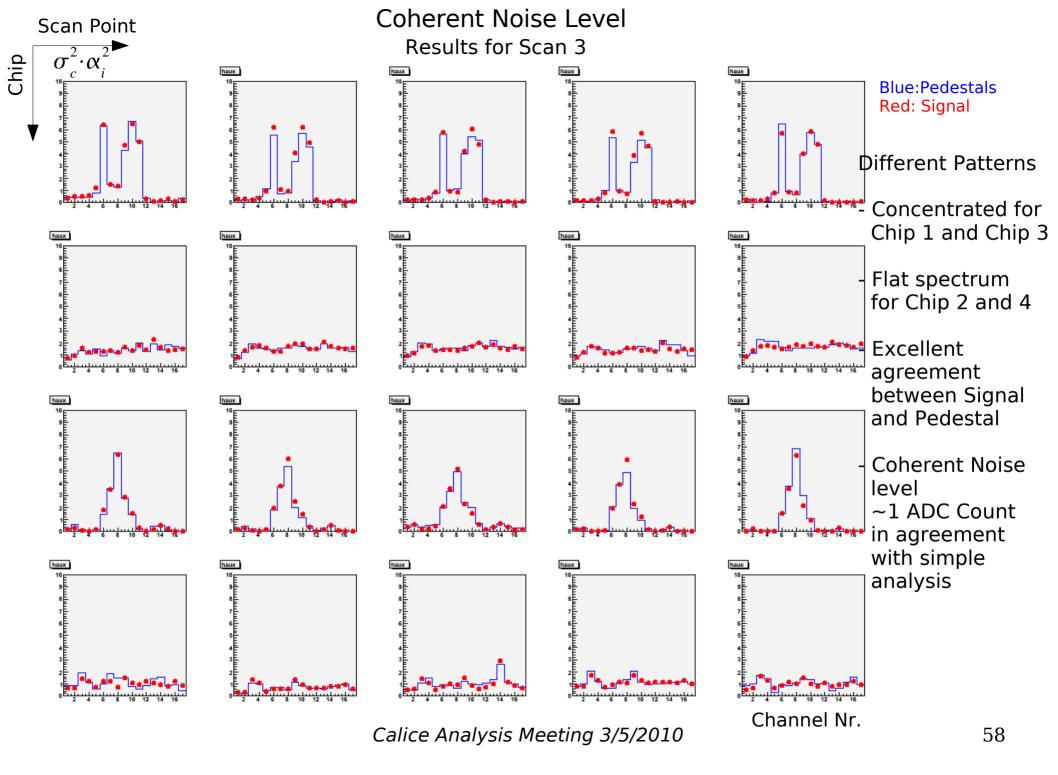


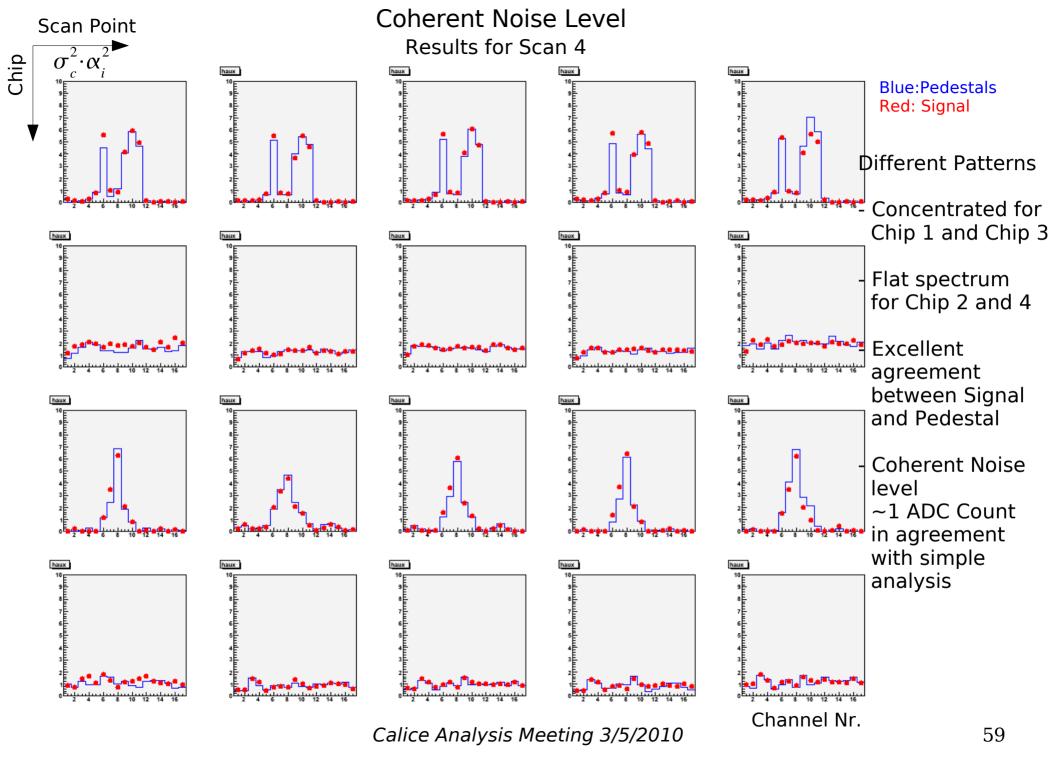


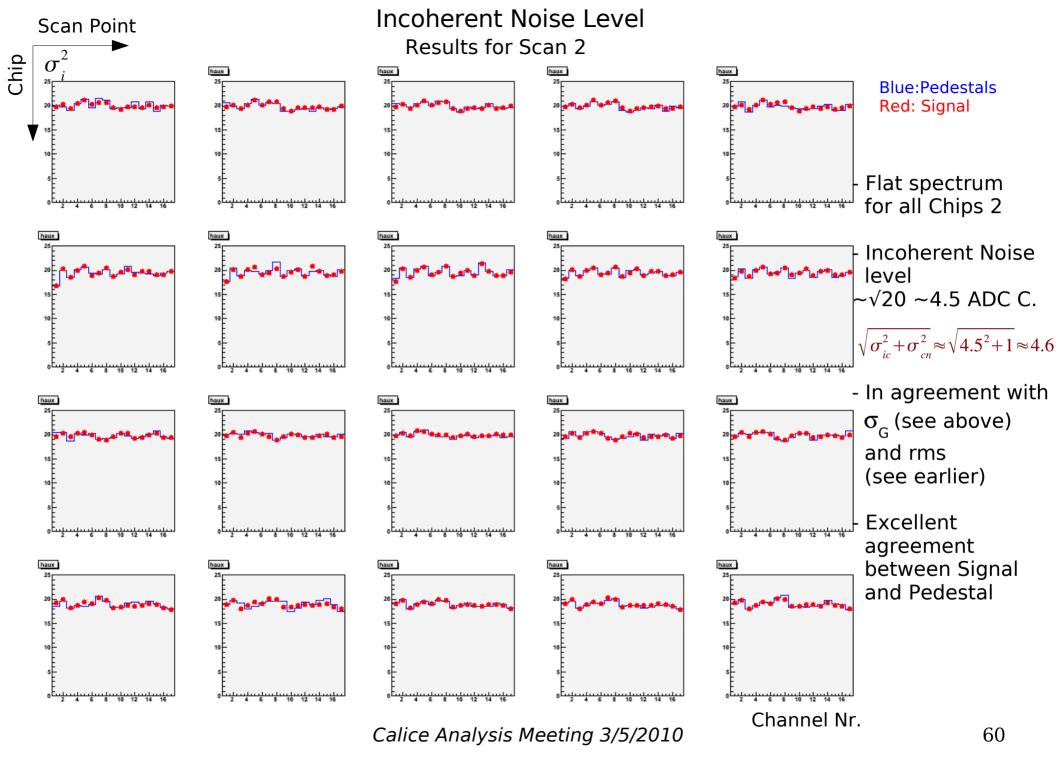


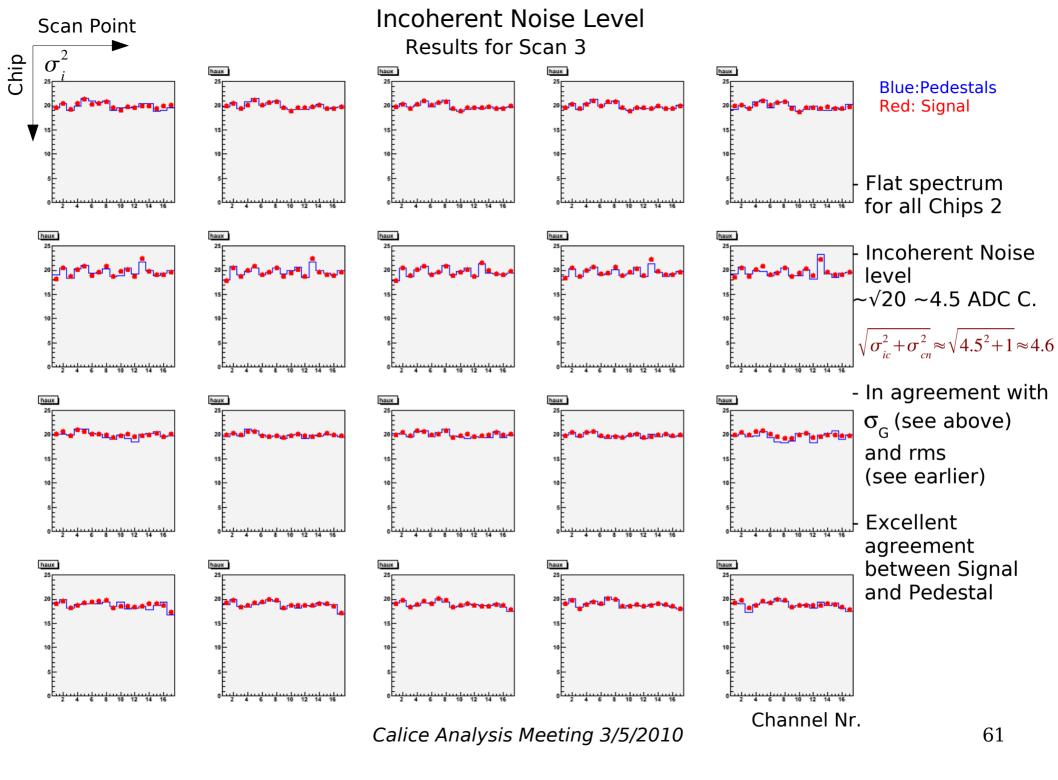


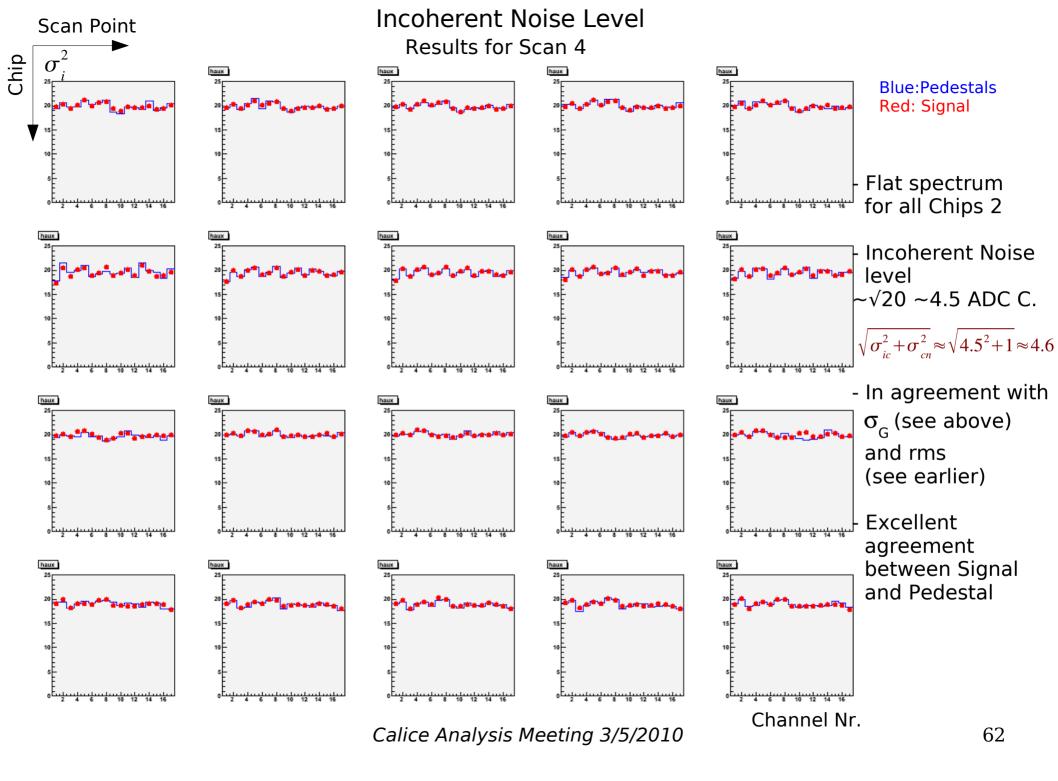












## All matrices *B*' on request ;-) (39 more plots)