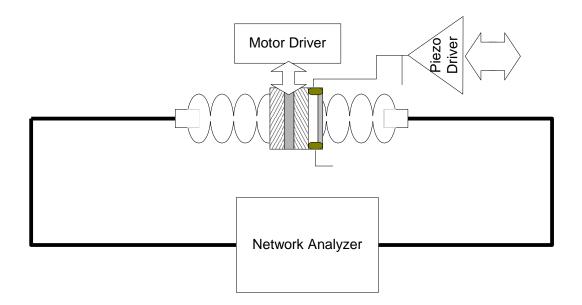
Piezo tuner test at S1 Global

Proposal for test setup A. Bosotti - R. Paparella Lab LASA - INFN Milano

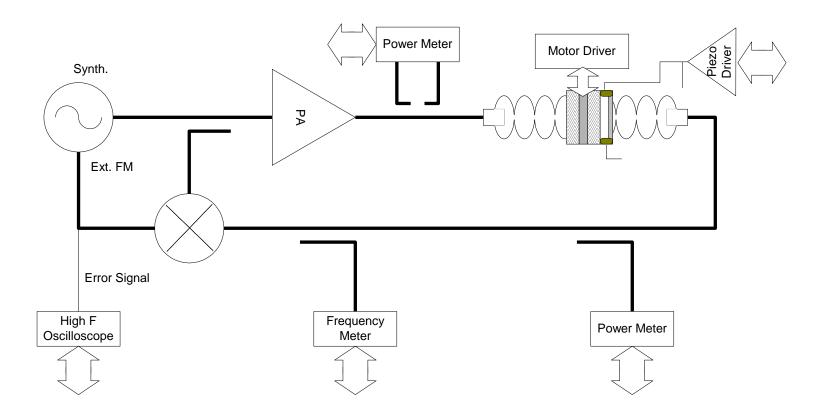
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Set up # 1: Network Analyzer



Very useful for resonance detection and to track resonance displacement if frequency shift is bigger than instrument frequency sensitivity. Tricky for frequency shift of the order of few kHz. The coupling and pick up ports here are not specified in the scheme and are characteristic of the test facility.

Set up # 2: PLL

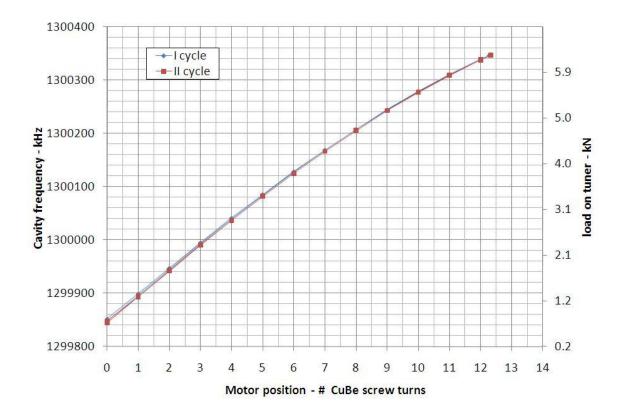


With PLL very small frequency shift can be resolved, down to the order of the Hz:

- Reading the frequency meter.
- Acquiring the error signal, proportional to frequency shift through the FM index.

Full tuning range

This "coarse" tuning range is measured spanning the thorough safe working region of the device, and can be represented as frequency shift vs screw turns or motor steps. The test is repeated 2-3 times to check reproducibility.

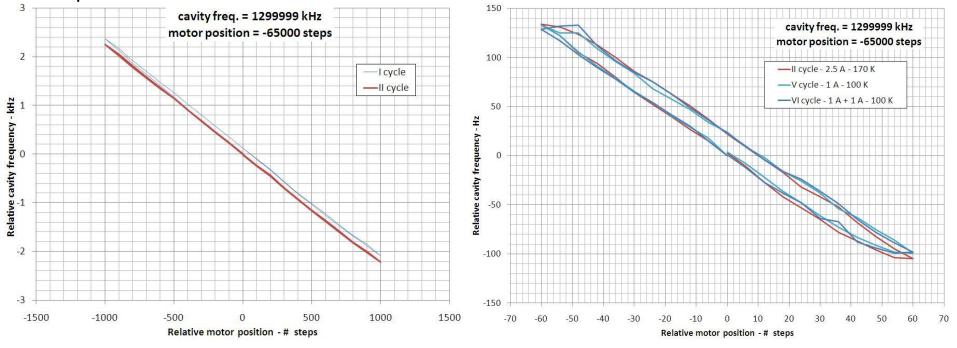


Tuning sensitivity and hysteresis are evaluated. Is useful to repeat the test changing the stepping motor parameter.

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Shorter range test

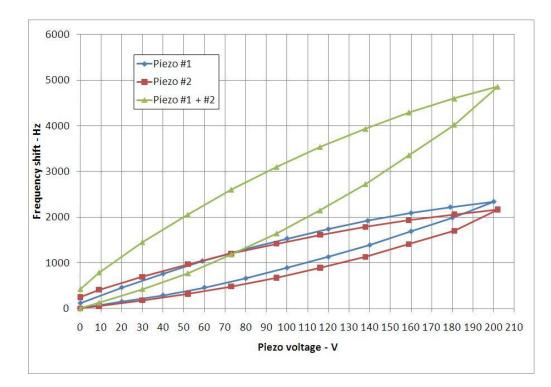
The purpose of these tests is to investigate the tuning position reproducibility and to quantify the backlash of the tuning system. One can start with \pm 1000 steps spanning around a fixed tuning frequency, the nominal 1.3 GHz, and after repeated for \pm 60 steps around 1.3 GHz.



The small frequency shift can be easily tracked through the PLL error signal.

PIEZO DC MEASUREMENTS

Each piezo is fed by the piezo-amplifier using DC signal up to its maximum nominal feed voltage. Eventually both piezo are fed together. The test is then repeated for different tuning conditions, i.e. for different piezo loads.

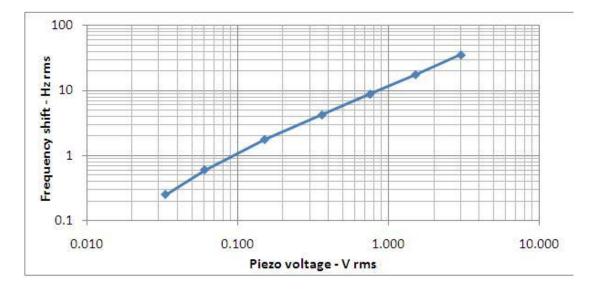


In this case 5 kHz of frequency shift has been achieved for 200 V of input voltage to both piezos.

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Piezo resolution measurements

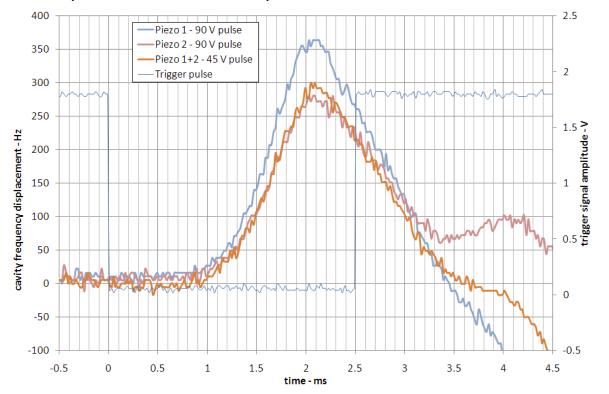
This is a way to directly measure the efficiency of the coupling of piezo to cavity at a sub-micrometer level. Results show an extremely smooth coupling and no sticking or threshold effect in piezo tuning. A lock in amplifier can be used for this kind of measurement. The piezo feeding voltage is generated by the lock in amplifier, which reads the PLL error signal in input at the EXT FM port of the synthesizer.



In this the cavity has been phase-locked at 1.3 GHz with 500 Hz/V frequency modulation index and the piezo have been driven in parallel with a sine wave of 5 Hz frequency and varying small amplitude. The lowest point in the graph is about 0.5 mV rms of error signal that is 0.25 Hz rms of frequency shift detected.

Piezo pulsed test

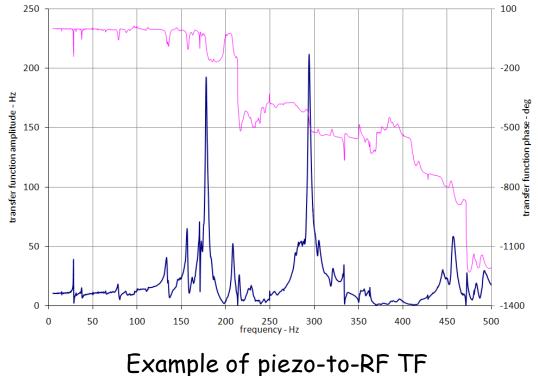
The test is performed applying a half sine wave of proper frequency (i.e.: 200 Hz) to a single piezo alternately and eventually to both piezos in parallel of different amplitudes up to the maximum feed voltage and driver capability, and recording the PLL error signal in input to the EXT FM port.



From the analysis of the peaks of these signals in the time frame of the E_{acc} flat top time interval (1 ms for TESLA), one can extrapolate the dynamic tuning capability.

Transfer functions

Very accurate "piezo-to-RF" and "piezo to piezo" transfer functions can be acquired during longtime (overnight) measurements with high frequency sensitivity (i.e: 0.02 Hz), with the cavity resonance locked by the PLL. For example, in the case of the "piezo to RF" TF, a Lock-In Amplifier can used to excite the chosen piezo actuator and to read the phase detector output. The Lock in driving signal is in sweeping mode, while the FM gain of the synthesizer used as a VCO is used to convert the PLL error signal to frequency shift.



Summary of proposed measurements

- DC piezo test: Voltage of 50 and 100 V applied to both and separately to each piezo and record of the frequency shift. Mandatory to see if piezos work correctly.
- Full range tuner test: full range forth and back, repeated 2-3 times. Evaluation of the hysteresis.
- Short range tuning test, i.e.: ± 1000 and ± 60 steps forth and back across selected positions. Backlash evaluation and reproducibility tests (PLL needed for the last).

For the following points can be useful to lock the cavity in a PLL. Can KEK arrange a (even simple) PLL for us?

- Piezo DC measurements spanning feed voltage forth and back and evaluation of piezo fine tuning range. To be done at different tuning positions to evaluate piezo strokes as a function of the cavity load.
- Piezo resolution measurements: phase locked at a given frequency (say 5 Hz) with small feed voltage amplitude.
- Transfer Functions piezo to piezo and piezo to RF. We have to clarify which instruments we will use with KEK people.
- Pulsed RF excitation, Lorentz Force detuning tests. If this is not possible we can test
- Phase Oscillations with pulsed piezo signal, with cavity locked in PLL.
- Microphonics noise test.