



# CALICE ECAL/AHCAL - EUDET electronics and DAQ - AIDA

## Tuesday, 6 July 2010

### Electronics and DAQ (part III) - Seminar 1, building 1 (11:00 - 12:30)

time	[id] title	presenter
11:00	[60] developments and planning towards a 1m3 RPC technological DHCAL prototype	MATHEZ, hervé
11:20	[61] Micromegas developments at LAPP	VOUTERS, Guillaume