

CALICE Week at Casablanca

Thursday 23 September 2010

Electronics and DAQ - I (11:00 - 12:30)

time	[id] title	presenter
11:00	[41] design process of KLAUS chip V2.0	SHEN, Wei
11:15	[42] production run update	SEGUIN-MOREAU, Nathalie
11:30	[43] AHCAL electronics tests results	Mr REINECKE, Mathias
11:45	[44] status of converters and multiplexers designs	DZAHINI, daniel
12:00	[45] SDHCAL ASU board and interconnects and robot results	CAPONETTO, Luigi