

Status of the FPCCD software

Physics and Software meeting

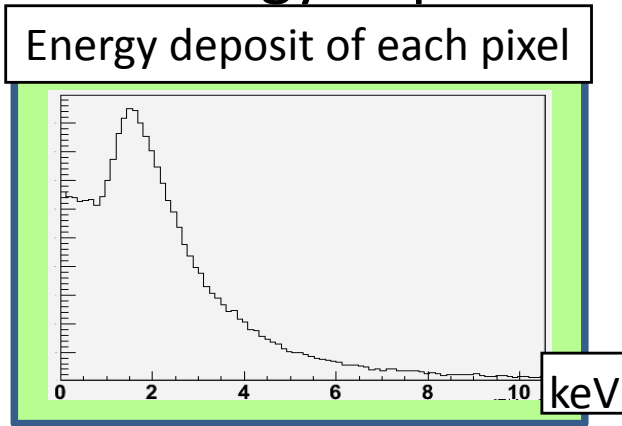
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D.Kamai (Tohoku University)

Plan

■ FPCCD Digitizer

- Energy deposit of each pixel is digitized into 7bits.



25 electrons / 1 step

→ max 3200 electrons (~ 11.5 keV)

■ Background rejection

- Rejection is done independently for each ladder.
- develop an algorithm to reject background by using the information of multiple ladder.

■ Check the performance of FPCCD vertex detector

- Tracking include TPC will be done.
- Performance for e^- , π^- signal will be checked.