

# Status of the FPCCD software

Physics and Software meeting

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D.Kamai (Tohoku University)

# Full tracking

- All processor needed to the study of FPCCD VXD performance were tested.
  - Ilcsoft/v01-09-02 was used.

Name	Type
MyAIDAProcessor	AIDAProcessor
MyMaterialDB	MaterialDB
MyTPCDigiProcessor	TPCDigiProcessor
MyFPCCDDigitizer	FPCCDDigitizer
MyOverlayBXVTXPixelHits	OverlayBXVTXPixelHits
MyFPCCDClustering	FPCCDClustering
MyETDDigiProcessor	ETDDigiProcessor
MyFTDDigiProcessor	FTDDigiProcessor
MyLEPTrackingProcessor	LEPTrackingProcessor
MySiliconTracking	SiliconTracking
MyFullLDCTracking	FullLDCTracking
MyLCIOOutputProcessor	LCIOOutputProcessor

← Bug was found.

My processors

- In v01-09-02, TPCDigiProcessor had a bug.
  - @line:832 (“STHcol” → “STHcolLowPt”)
- Memory usage of Overlay processor is too large to merge background for 1 train.
  - Perhaps, Marlin needs to be run divided into small tasks.