

Status of the FPCCD software

Physics and Software meeting

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Current Status

- Development of new silicon tracking processor is started.

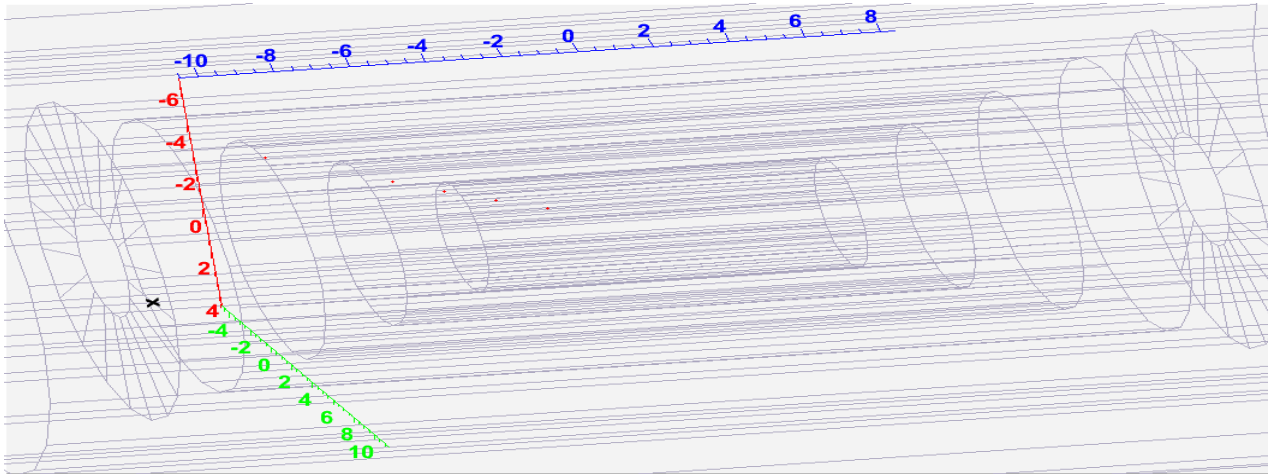
Development of Silicon tracking

- The Tracking method of ILD
 - Silicon Tracking (VXD, SIT, FTD)
 - TPC Tracking (TPC only)
 - Full LDC Tracking (connect Si track and TPC track)

- Goal : Replace existing Silicon Tracking
 - Implement the Kalman filter.
 - Extrapolate the track from outer layers.
(because background in outer layer is low)
 - What is expected.
 - Improvement of IP & mom resolution by Kalman filter.
 - CPU time saving.

Implementation of Kalman filter

- Implement FPCCD into KalTest package (by Fujii-san).
 - At KalTest, VXD is just tube.



Event display
of KalTest

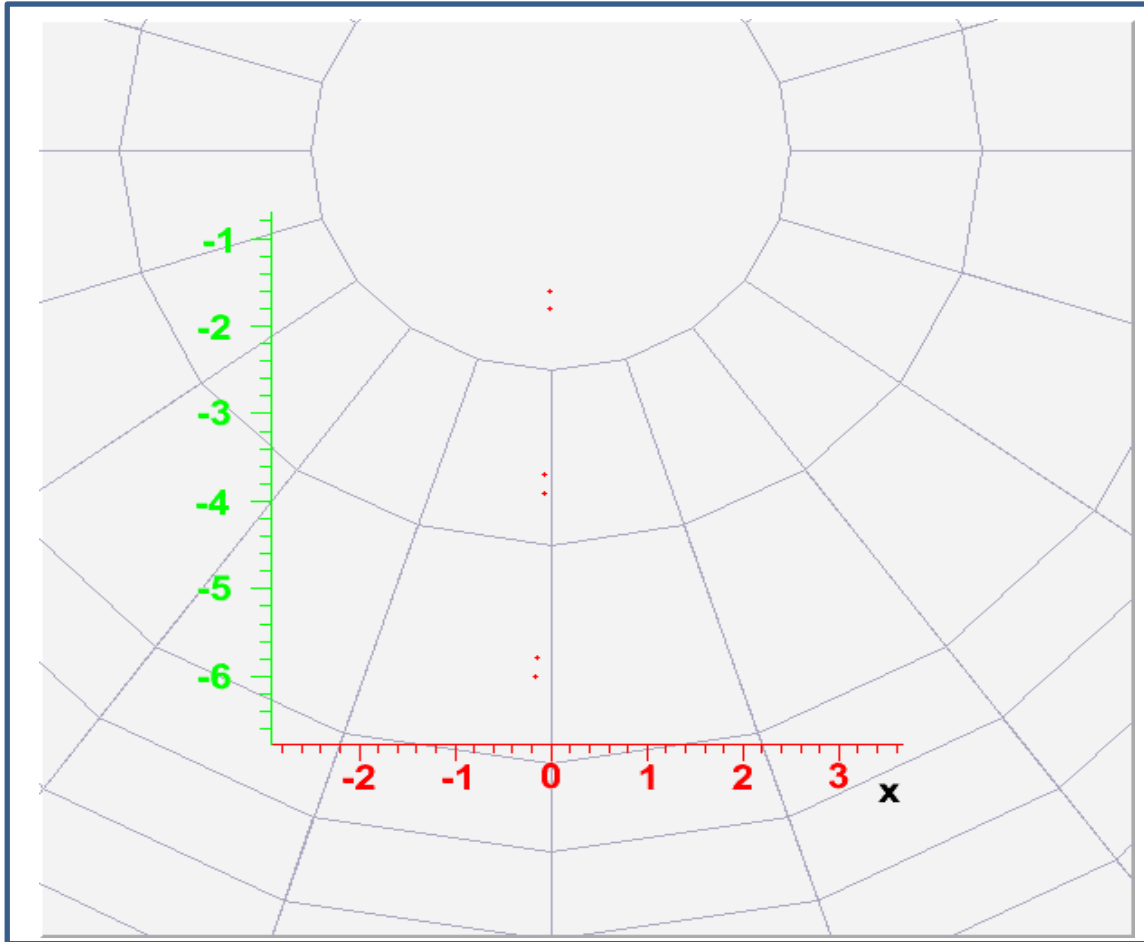
→ Replace a tube with structure of ladders.

Step 1. create a ladder.

Step 2. construct the VXD.(Simple polygonal column)

Step 3. construct more detailed layout.

- 6 layers of FPCCD VXD were plotted.



More detailed layer is under construction.