Status of
the FPCCD software

Physics and Software meeting

2011/06/24
D.Kamai (Tohoku University)

Current Status

■ Development of new silicon tracking processor.
   —Implementation of the 3 doublet layer structure into KalTest package was finished.

■ Code can be downloaded from DESY svn server.
   — svn co https://svnsrv.desy.de/public/kaltest/KalTest/trunk KalTest

Plan

- **KalTest**
  - — Check the IP & momentum resolution for FPCCD.
  - — Expand the versatility.
- **FPCCDDigi**
  - — Finalize FPCCD digitizer (by Granada?)

    Possible improvements
    - Energy conservation
    - Delta ray