



# Some personal reflections on LCTPC (LP)

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# Present: LP1

- Fieldcage, cathode, infrastructure (DESY)
- Endplate (Cornell)
- Modules:
  - A-GEM (double stack)
  - Micromegas (w. Resistive anode) (Saclay/Carlton)
  - D-GEM (triple stack); also with Timepix R/O (DESY, Bonn)
  - Octopuce (Saclay/NIKHEF)
- Electronics:
  - S-Altro
  - AFTER
  - Timepix
- Laser system

# Present: LP1

- Issues:
  - Mechanics modules
  - Stability HV connections/discharges
  - E/B field distortions
  - Gating grid
  - Coping with B-field distortions

# How to proceed from here

- Internal LCTPC review early Spring 2012 in preparation for ILD meeting in Japan end-May 2012
- Define ILD baseline design TPC end-May
- Can and will continue R&D on different technologies. At some moment: choices?