This is work done from 2002 to 2007 and presented at

Santa Cruz, June 2002; Arlington, January 2003; SLAC, January 2004; Paris, April 2004; Victoria, July 2004; Vienna, July 2005; DESY, May 2007

The main results are the track reconstruction efficiency as a function of pad size and noise occupancy.



Pattern recognition uses the CLEO program,

adapted to the TPC by separating the chamber into time slices.

Note regarding the time evolution of the resistive spreading: this is not an issue. Clustering can take into account the known time variation with respect to distance from the charge center.





The first result was efficiency as a function of "pad width" (PW)

Recall, this is for a PRF with RMS=0.7 x PW.

But, the effect on the efficiency is really due to a combination of pad width and PRF.

In the present case,

the RMS due to the pad width is 0.289 x PW and the RMS due to the PRF is 0.70 x PW. Simply, the combined RMS is 0.76 x PW.

When attempting to interpret this plot to predict the efficiency for a resistive spread Micromegas,I would use the combined RMS due to the pad and PRF as shown in the added horizontal axis.



Track Finding Efficiency vs TPC Pad Width



The second result was the efficiency as a function of voxel occupancy. Noise was pushed to 4.8%, or 2.5 x10⁶ hits in the detector. This was publish in the LOI, figure 4.3-4.