

7-Module Crosstalk

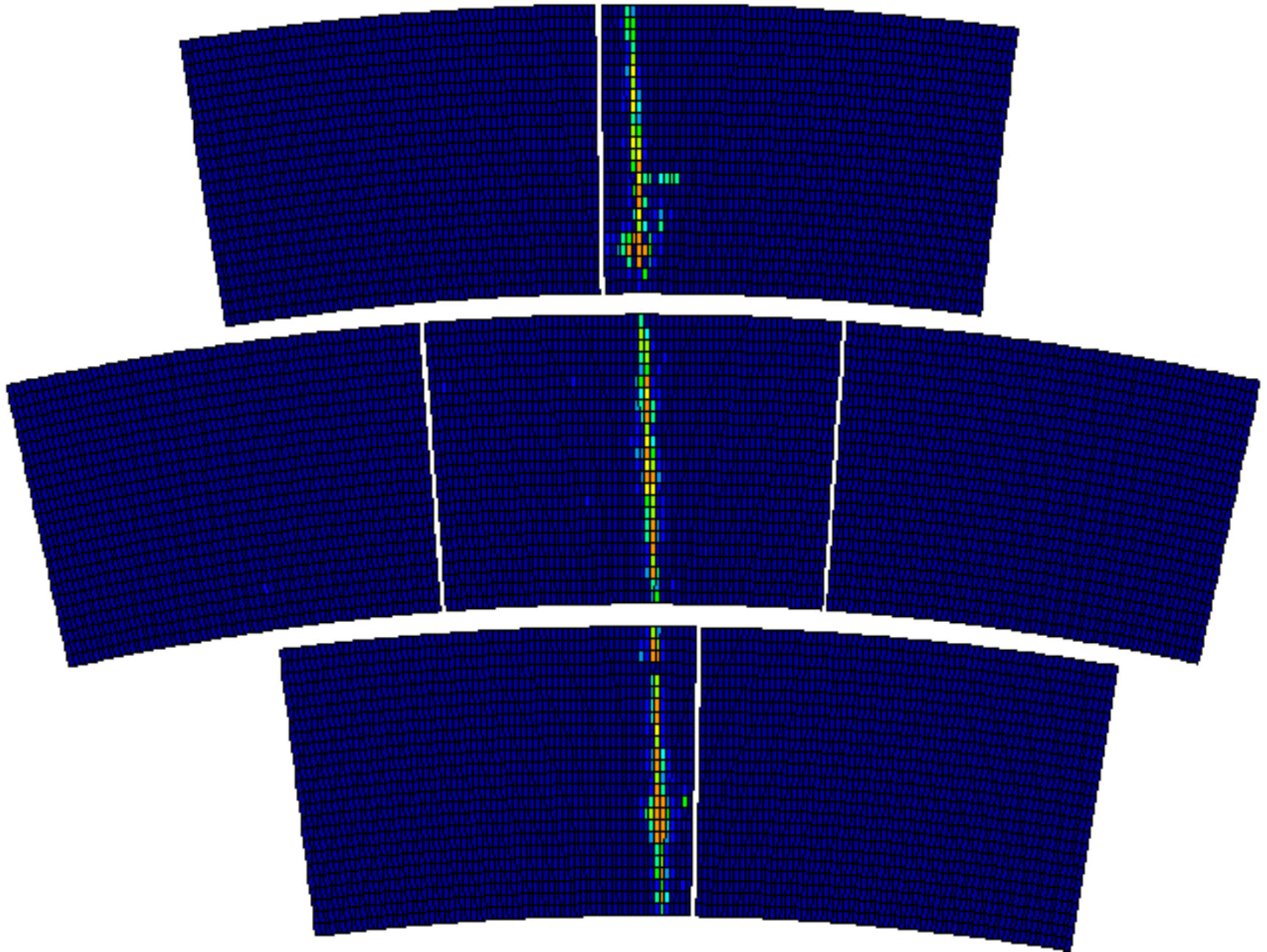
D. Attié

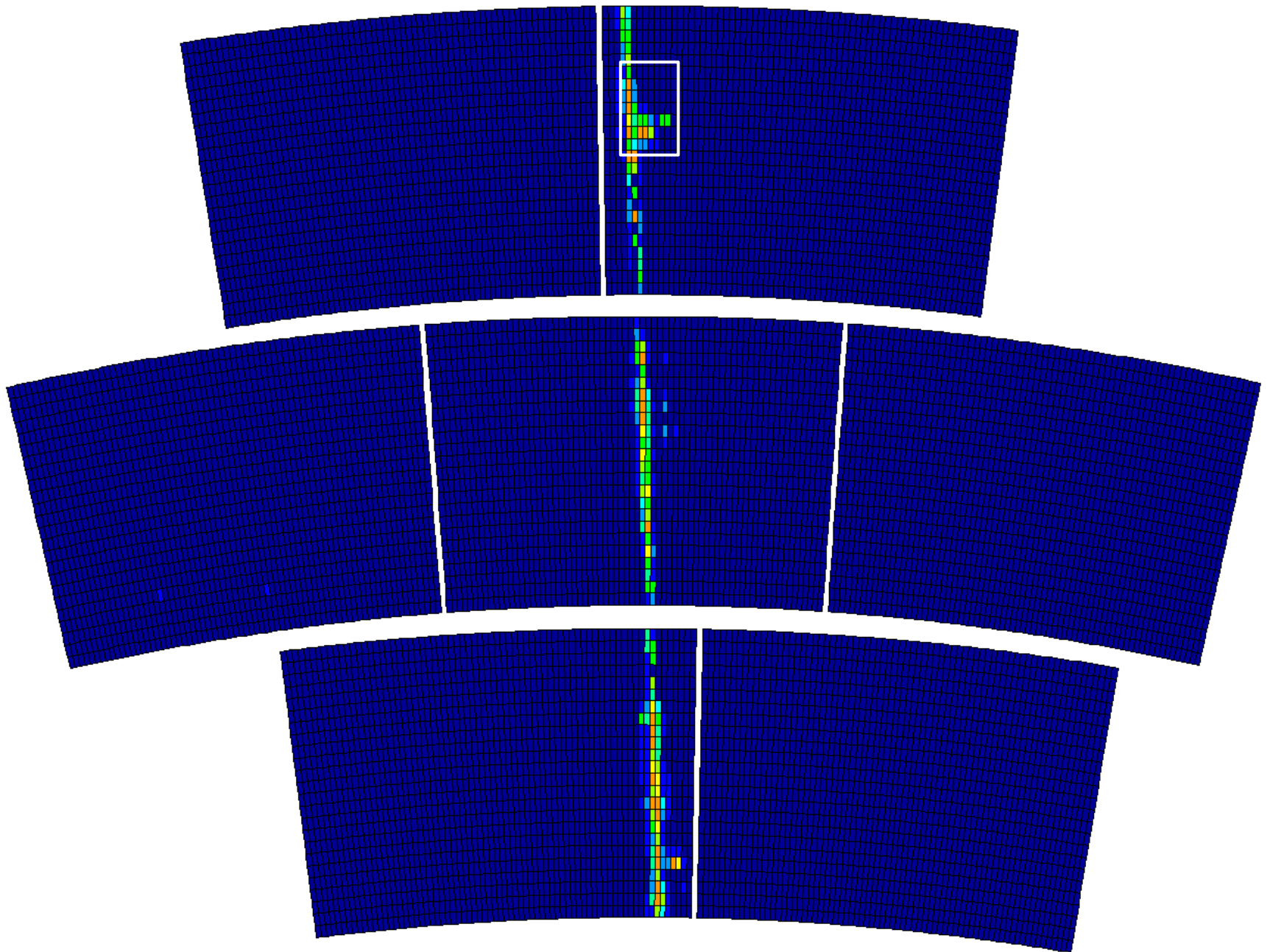
i r f u

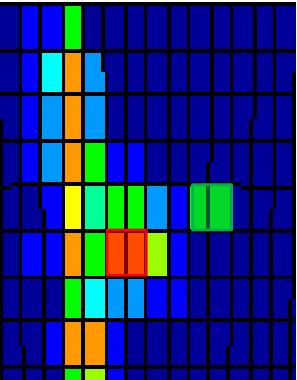
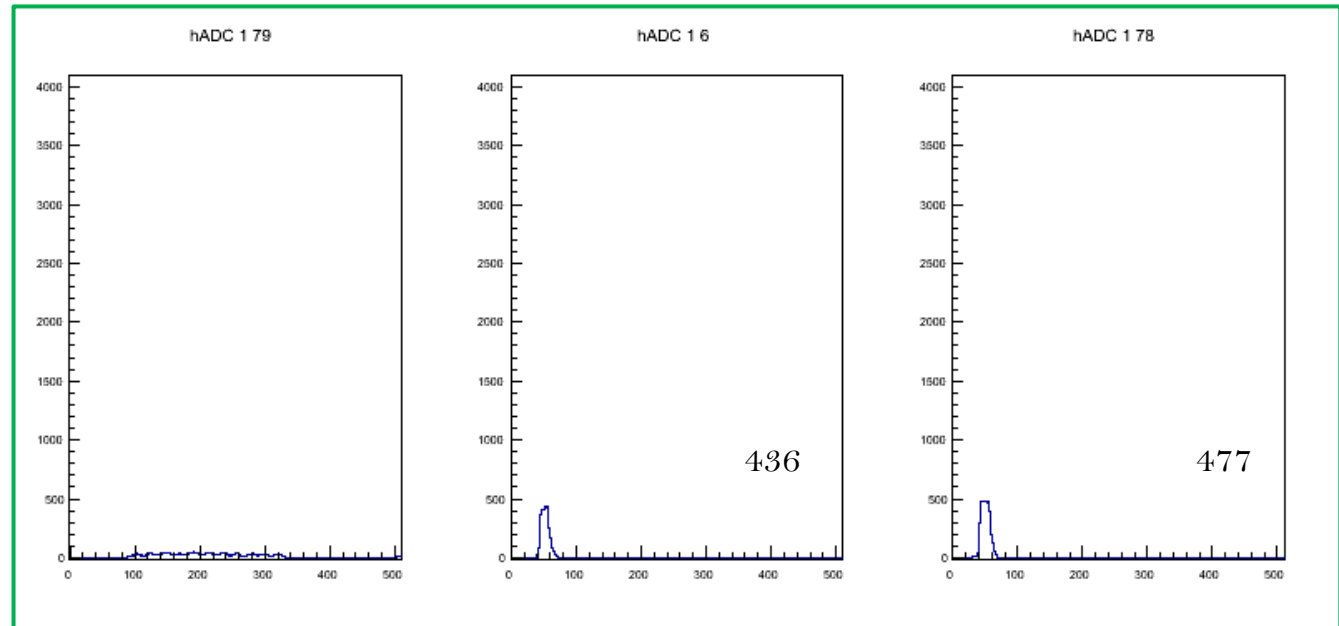
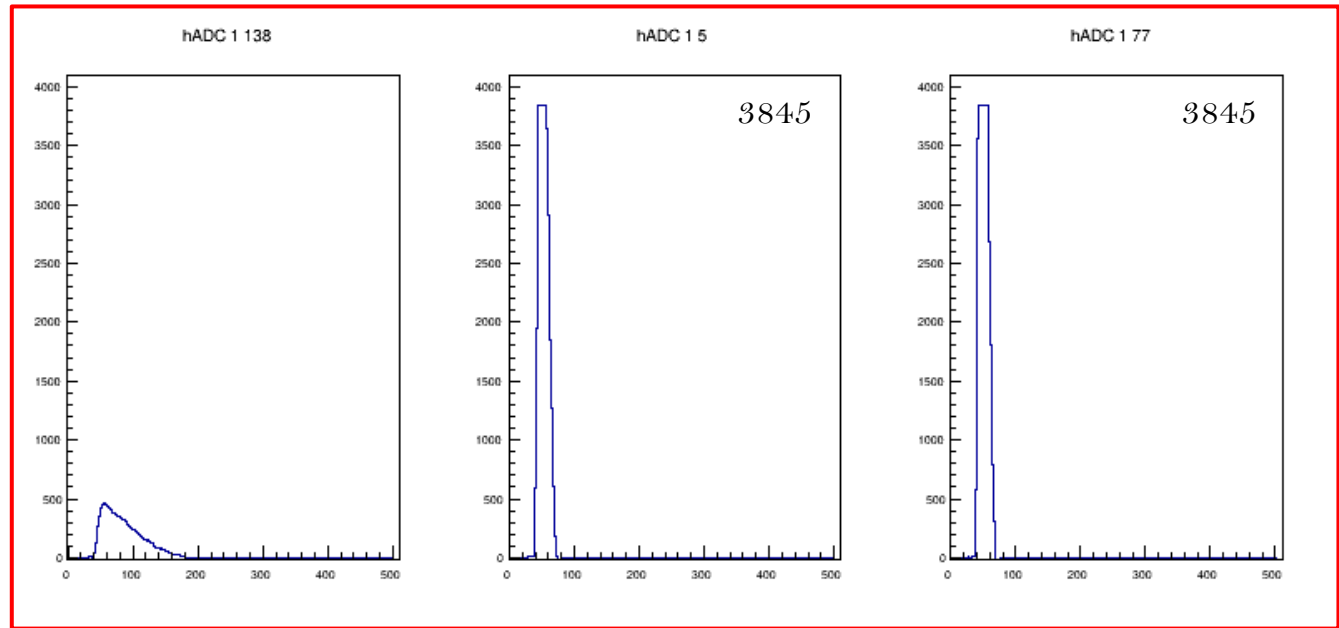
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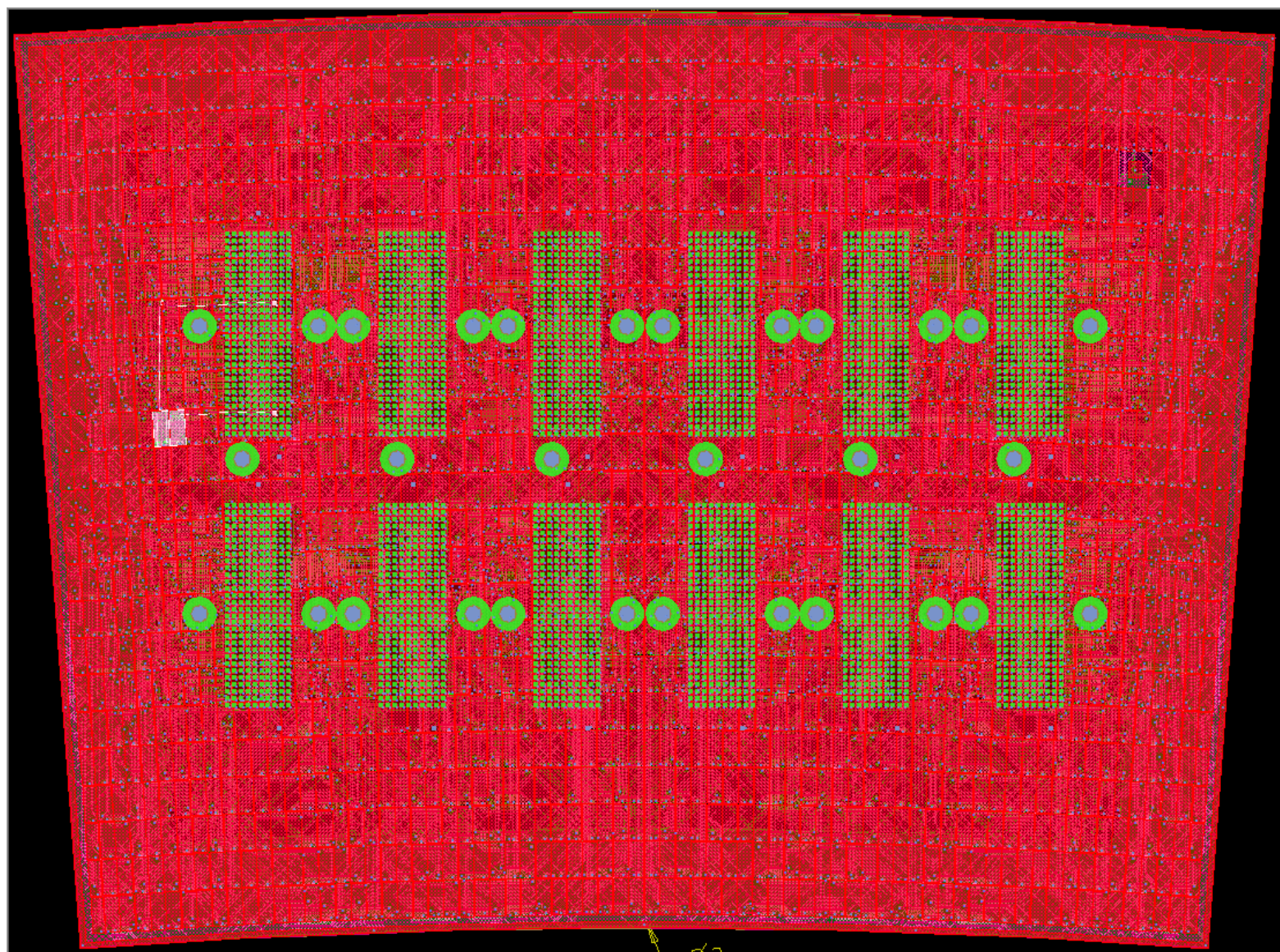
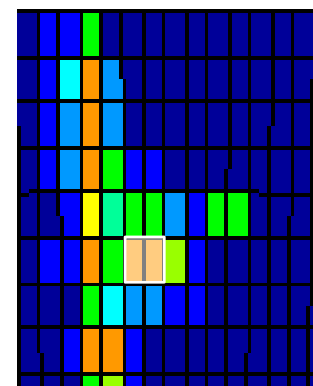
saclay

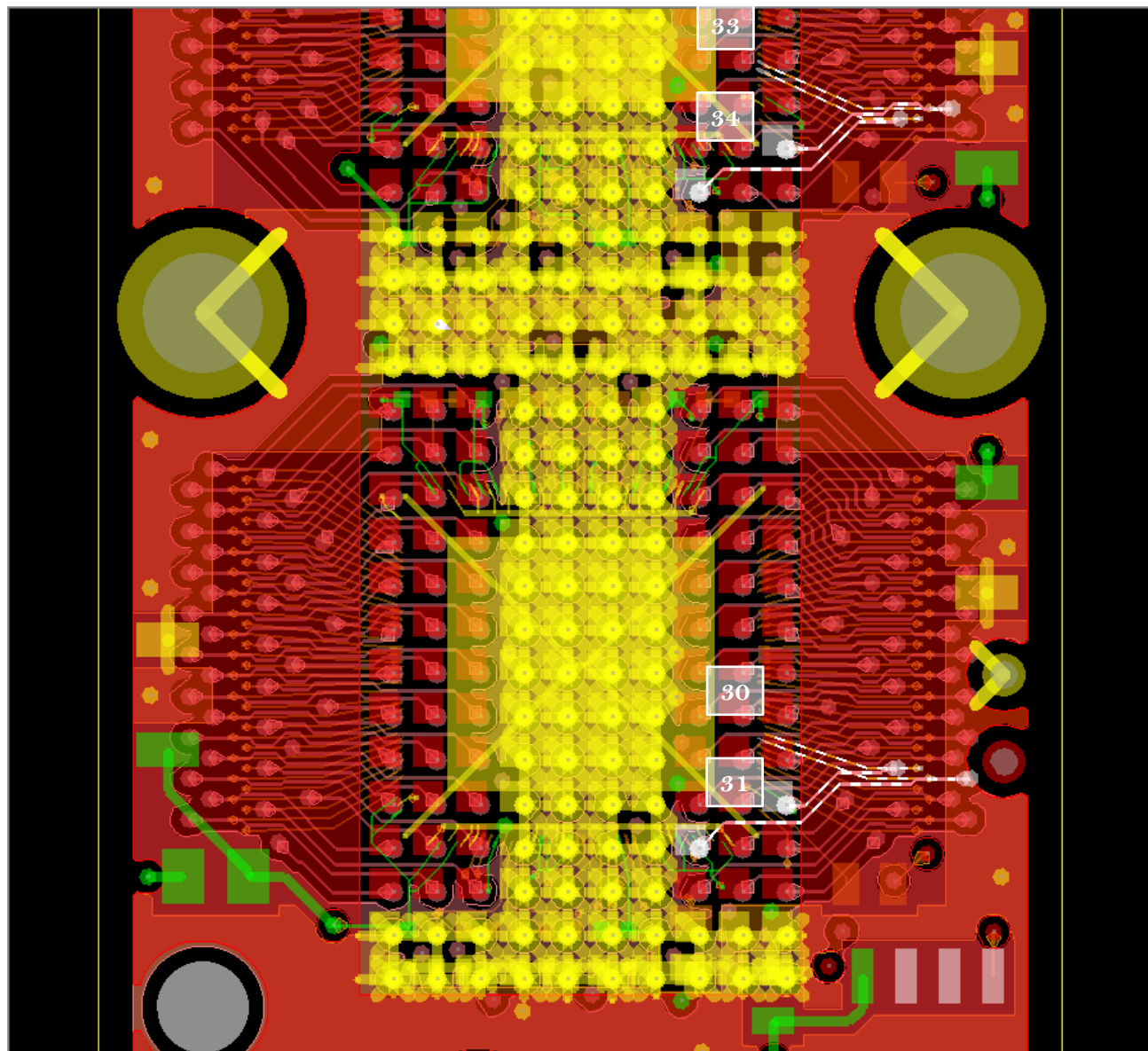
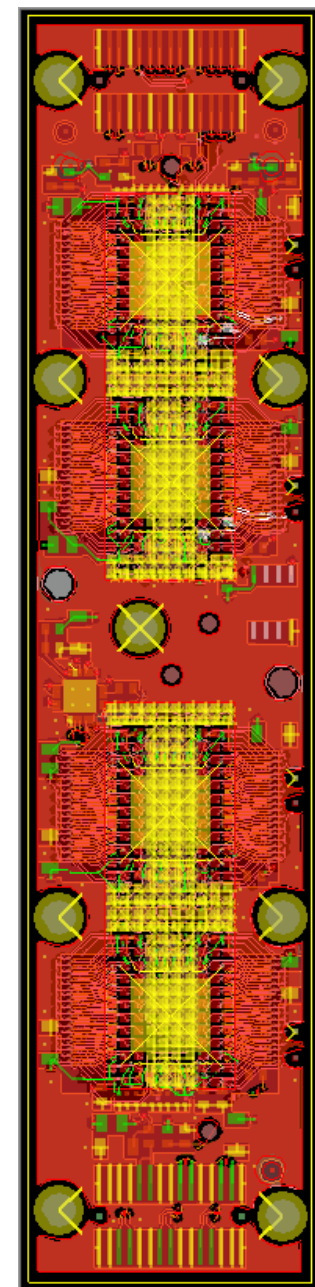


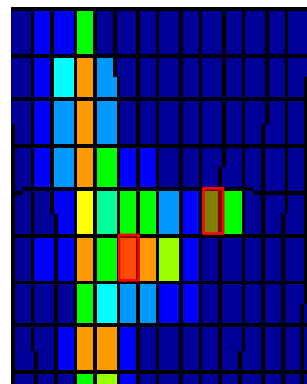
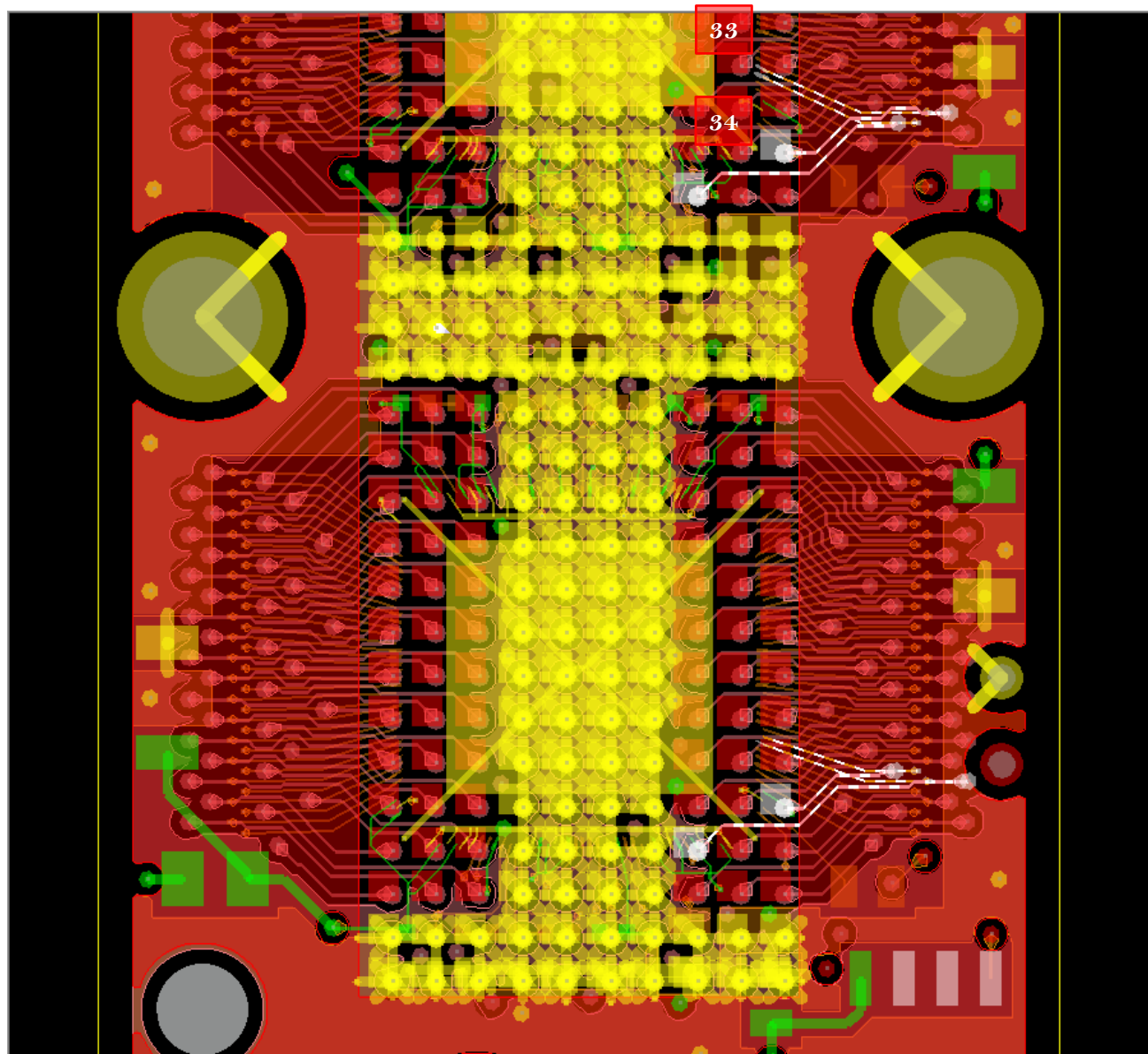


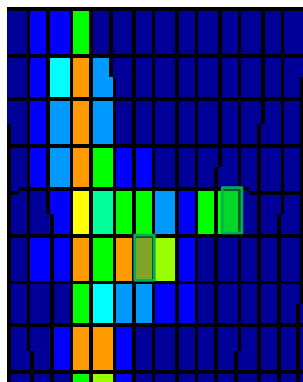
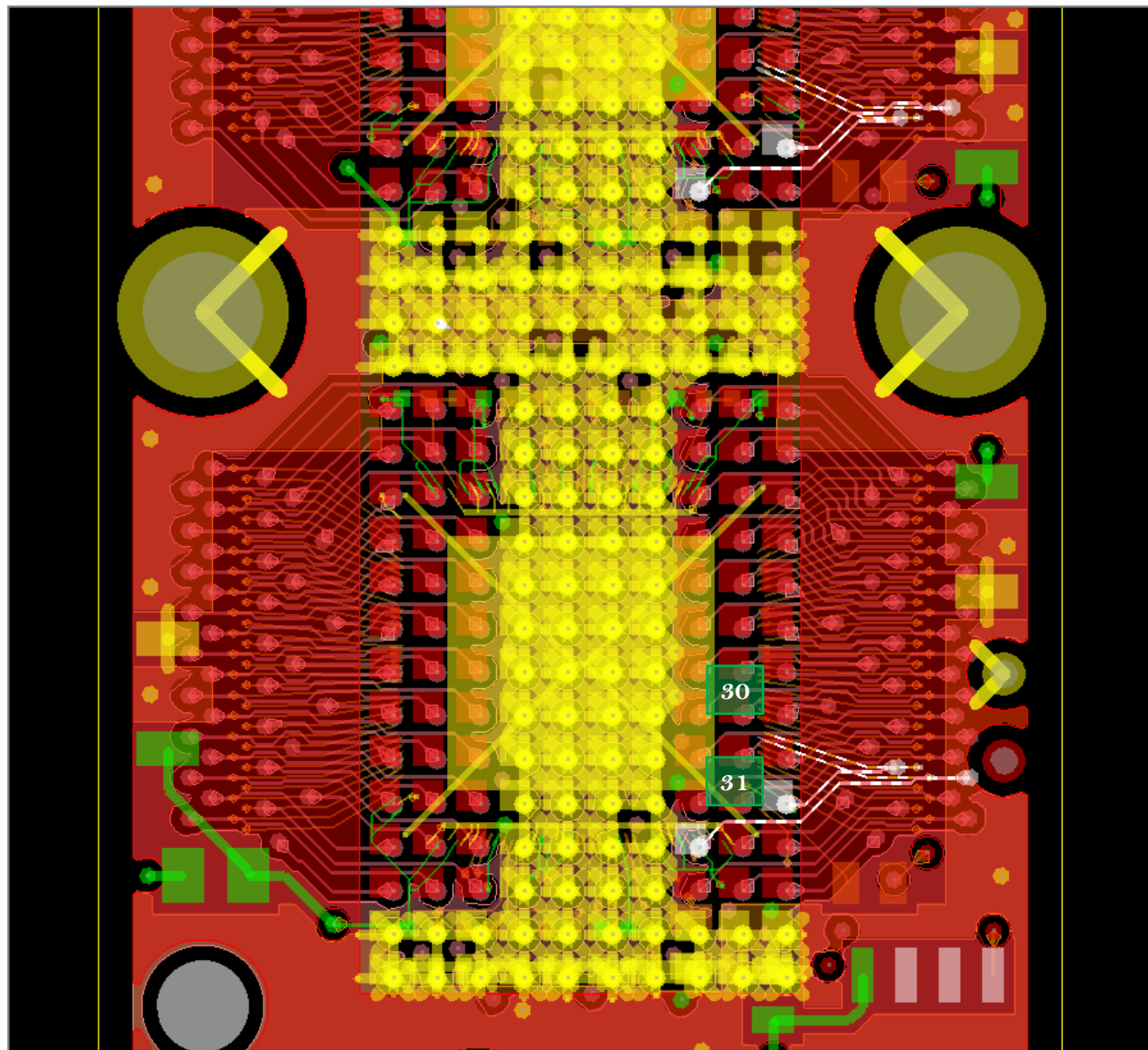












- Crosstalk not coming from detector board routing or FEC board routing
- ADC clock misalignment while reading analog output signal from ASICs?