FJPPL-FKPPL Workshop on ATF2 Accelerator R&D

Tuesday 12 February 2013

Review of IP-BPM electronics: electronics, performance testing and simulation, compare existing and new systems, discussion - Room 166 in building 200 (09:00 - 10:00)

-Conveners:	Philip	Bambade

time [id] title	presenter
09:00 [21] Preparation and tests of new KNU IP-BPM electronics	Mr JANG, Siwon
09:20 [22] Experience and comparison of present IP-BPM electronics set-ups	Ms KIM, YOUNGIM
09:40 [23] Discussion of issues switching from present electronics to new electronic	S