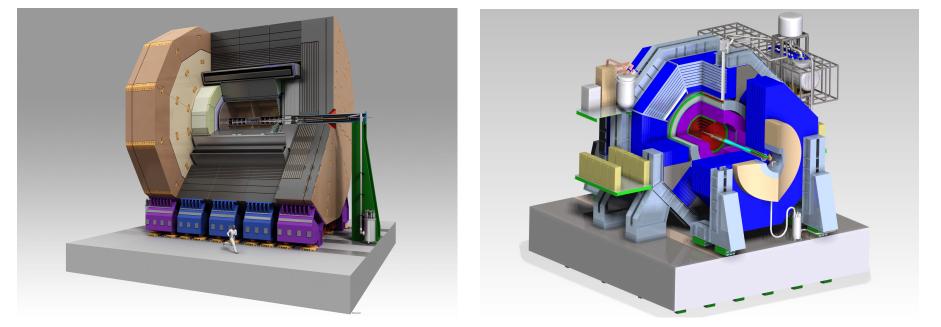
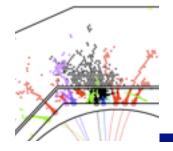
A Scintillator Analogue HCAL for ILD and SiD

Felix Sefkow





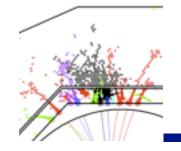
SiD Meeting, SLAC, 14-16. October 2013





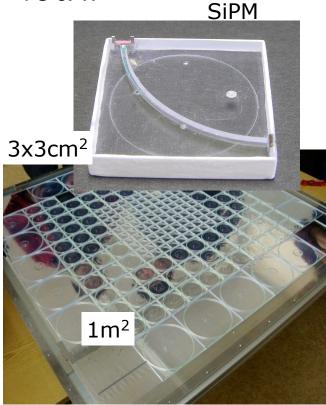
- The AHCAL experience
- The AHCAL in ILD
- AHCAL R&D
- The AHCAL in SiD

2



AHCAL physics prototype

7608 channels 38 layers Fe & W



1mm²

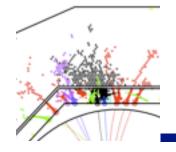
- Constructed in 2005-06: first device using SiPMs at large scale
- Now many followers: T2K, Belle2, CMS, medical applications,...
- Extremely robust: 6 years of data taking
 - 2006-7 CERN: Fe with SiW ECAL
 - 2008-9 FNAL: Fe with Si/Sci ECAL
 - 2010-11 CERN: Tungsten

Many trips with disassembly & reassembly of the calorimeter:

DESY - CERN - DESY - FNAL - DESY - CERN PS - CERN SPS

... and the SiPMs survived without problems!

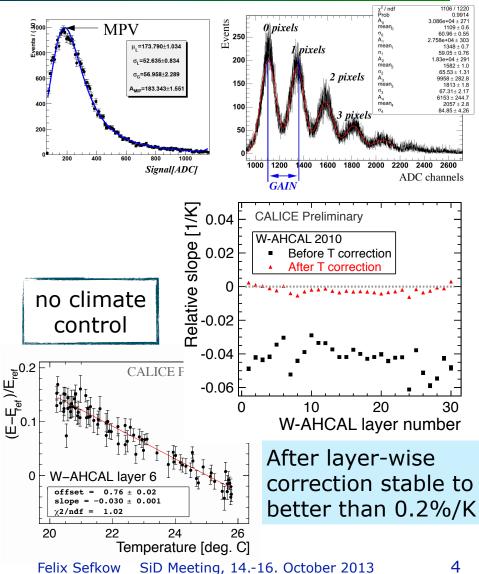
3



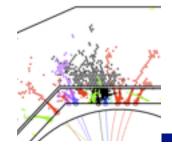
Calibration

- Cell-wise equalisation: MIP
- Saturation correction: gain
- All SiPM properties depend on one parameter
 - $-\Delta V = V V_{break-down}(T)$
- Needed time to find right procedures
 - some limitations from test bench data
 - large spread of SiPM parameters
- Guidance for future developments
 - e.g. gain stabilisation



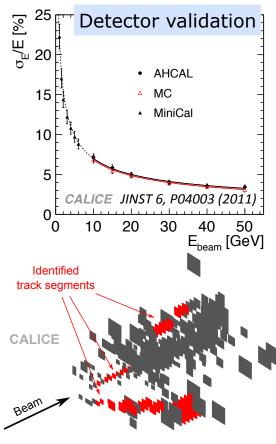


0 Distance between shower axes [rfm] Beam Energy [GeV]

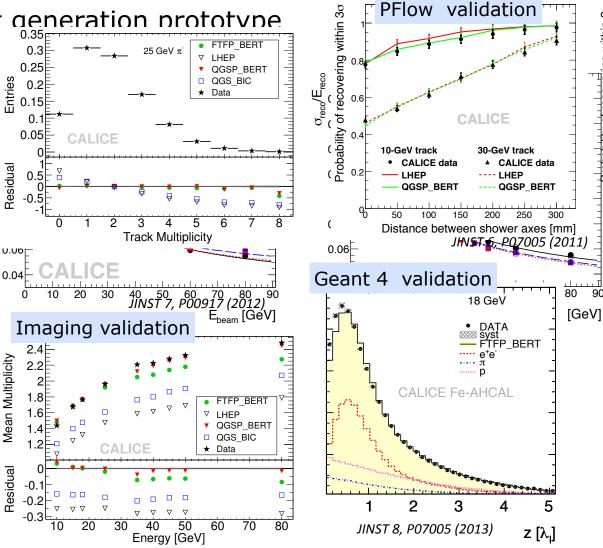


Validation of Simulation

- Validation with first generation prototype
- Published



JINST 8, P09001 (2013) Scintillator HCAL



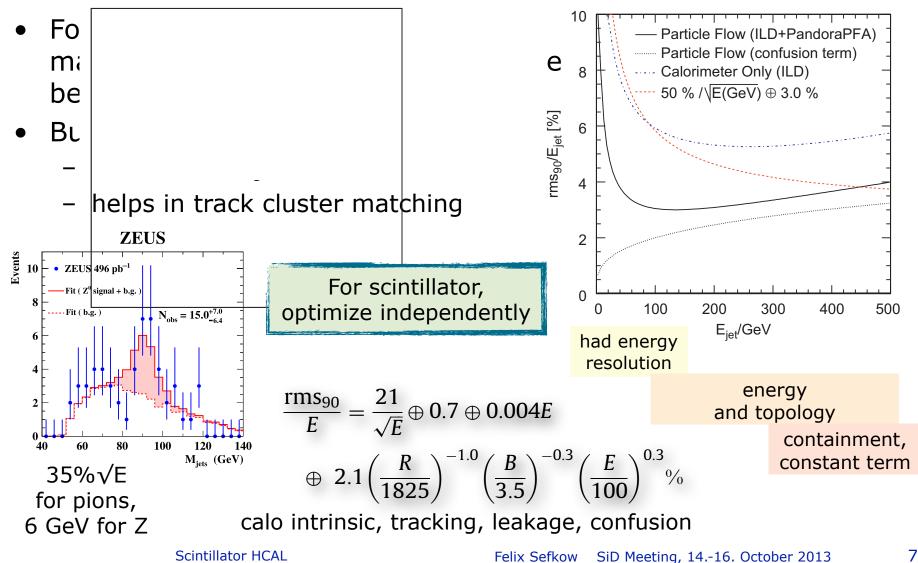
Felix Sefkow SiD Meeting, 14.-16. October 2013 Probability of recovering within 20 8 70 90 90 80

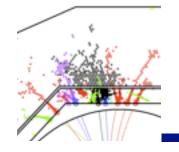
90

Proof of principle: done. Towards a real detector:



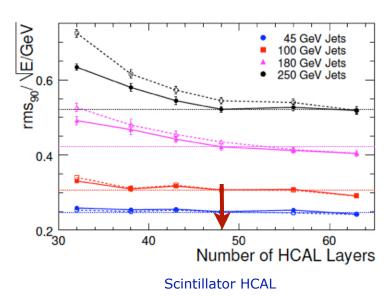
e flow

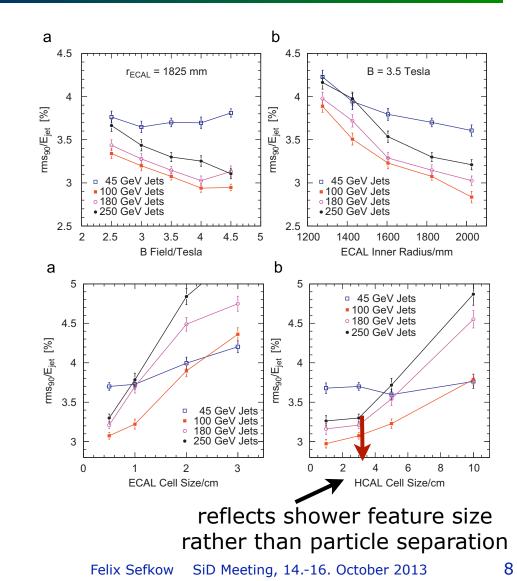


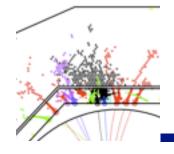


ILD optimisation

- Based of Pandora PFA
- Extensive studies done for the LOI
- Cost optimisation postponed

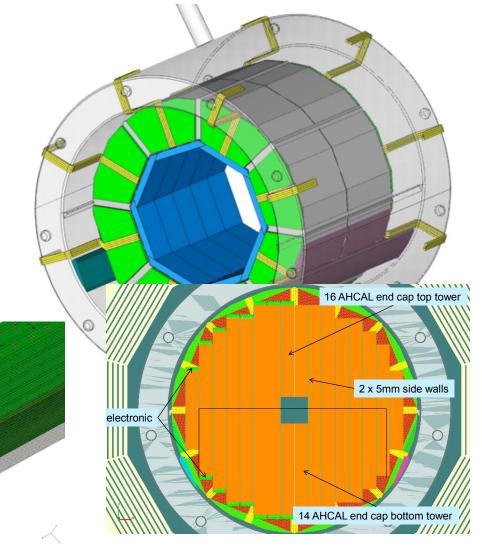


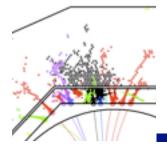




AHCAL implementation

- Short barrel (2x 2350 mm)
 - big endcap R = 3190 mm
- 8-fold symmetry
 - 16 sub-modules
- 6 λ deep, 48 layers x 2 mm
 - R = 2058-3410 mm
 - 8000m²
- Cracks filled with steel
- Embedded front end electronics
- Accessible interfaces

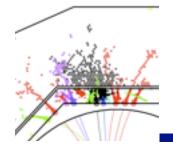




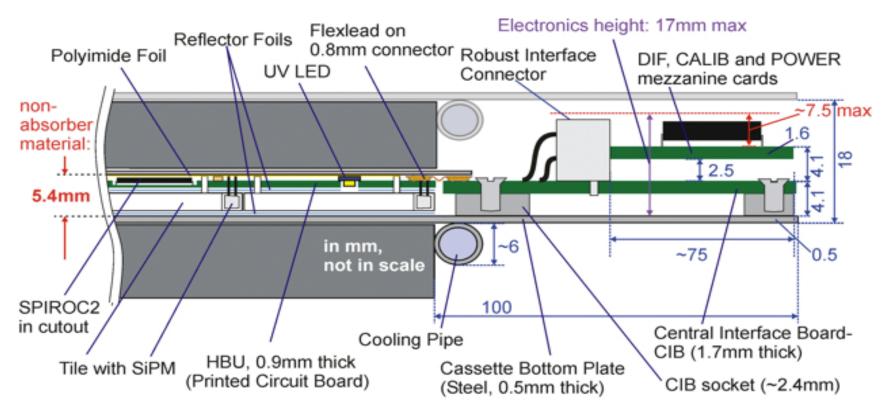
Mechanical prototypes

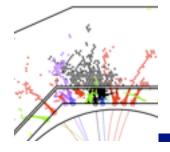
- Horizontal and vertical test structures built
 - used cost-effective roller leveling no machining
- Tolerances verified: 1mm flatness over full area
- To be used for integration studies, test beams
 - and earthquake stability tests





Layer cross section

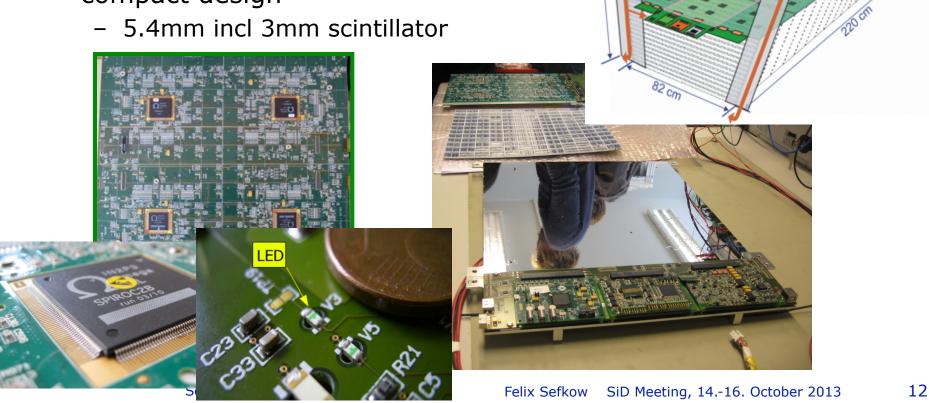


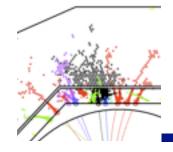


Electronics integration

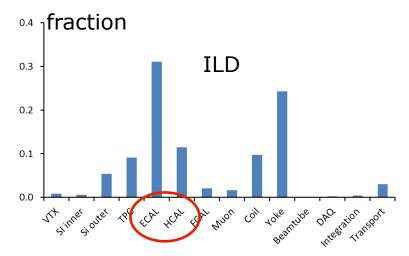
50 0

- Basic unit: 144 tiles, 36x36cm²
- 36 ch. ASICs, power pulsed
 - self-trigger, 16x memory, ADC
- embedded LED system
- compact design





AHCAL cost drivers and scaling

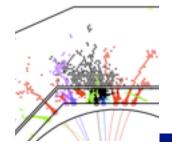


- DBD costing is far from final, but much better than anything we had before, largely based on 2nd gen. prototyping
- Many lessons to be learnt
- What are the real cost drivers at present?
- What are the scaling laws?

- Absorber 10M \sim volume
- SiPM 8M ~ channel
- PCBs 22M ~ area
- and not
- Scintillator 1.5M
- ASICs 1.8M
- Interfaces 1.4M

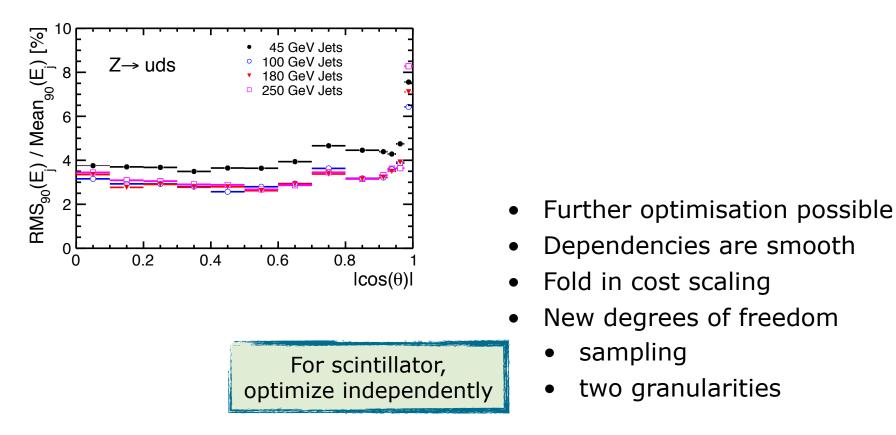
• ...

• total 50M



Performance

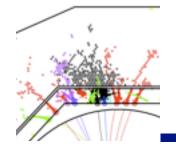
- Essentially all ILD DBD analyses were done with the AHCAL
- Dead regions, interfaces, services included in simulation



two granularities

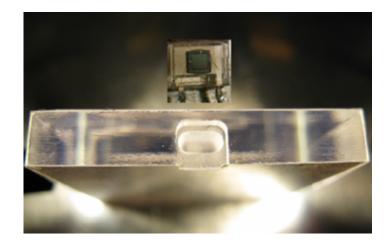
sampling

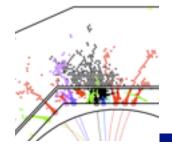
Conceptual design: done. Steps towards realisation:



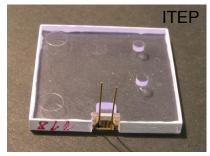
SiPM improvements

- Dynamic field, driven by medical applications (PET)
 - commercial use requires uniform devices
- 1€ per piece not unrealistic
 - Hamamatsu, SensL
- Improved performance in today's prototypes
 - e.g. Russian sensors have 100x less noise than in physics prototype



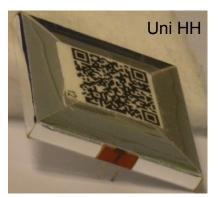


Scintillator tile options

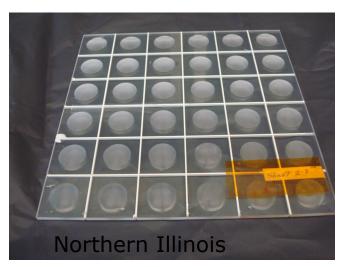


CPTA, KETEK or Hamamatsu sensors

no WLS fibre

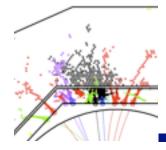


individually wrapped; KETEK sensors



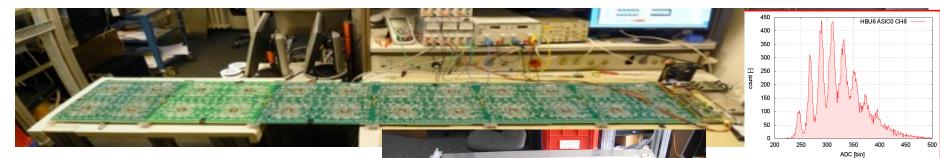
Hamamatsu sensors, on PCB surface

- Megatiles interesting alternative
 - need to understnd limitations and impact of optical cross talk
- need to optimise design and production together
- implication for QC chain: scintillator SiPM system independent of final electronics - or not



2nd generation prototype

• Full slab: signal integrity



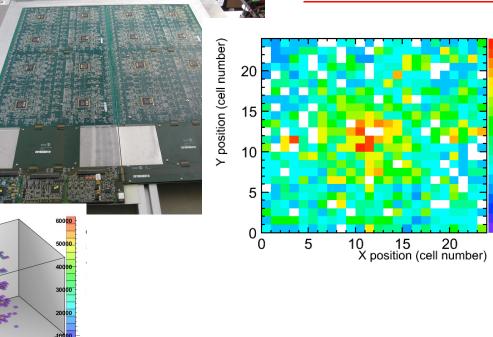
• Full layer: hadrons

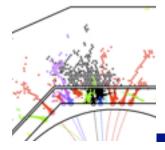


Scintillator HCAL

10

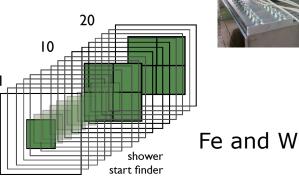
10



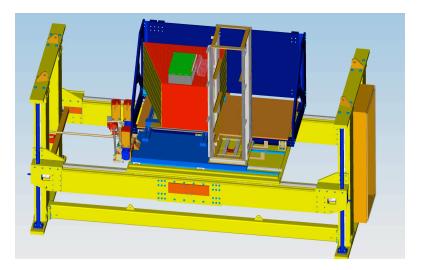


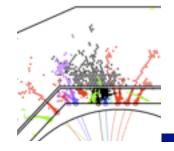
Flexible test beam roadmap

- 2013-14:
 - e.m. stack, 10-15 layers,
 ~200 ch
- 2015-16:
 - hadron stack with shower start finder, 20-30 HBUs, ~ 4000 ch
- 2017-18:
 - hadron prototype, 20-40 layers, 10-20,000 ch
- Gradual SiPM and tile technology down-select
- Exercise mass production and QC procedures









AHCAL groups in CALICE

Google



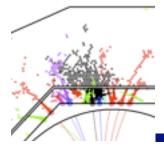
thanks, Katja!

Realisation: on the way. AHCAL for SiD:

Realisation: on the way. AHCAL for SiD:



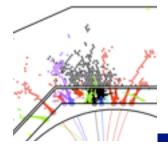
Stanitzki, options!



SID HCAL

- Similar architecture
 - accessible electronics
- Barrel: smaller but longer
 - not a problem
- Same sampling

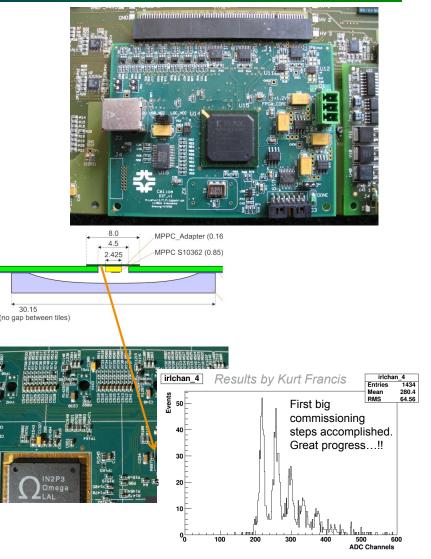
- To be done:
- Optimise granularity
 - expect no changes
- Implement active layers in mechanical design
 - HBUs are 8,9, 10, 12 tiles wide, smaller is easy
 - space for interfaces and services

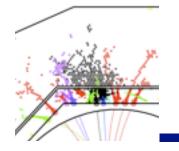


Electronics

• Cooperation DESY, NIU (+FNAL)

- DIF: DAQ interface
 - design: DESY
 - prod: NIU (+FNAL)
- IRL: HBU with NIU coupling concept
 - design: DESY and NIU
 - prod: DESY
- To be done:
 - evaluate combined operation with kPix
 - SPIROC and kPix both have time-stamping, memory, digitisation and readout between trains
 - common beam test at SLAC?

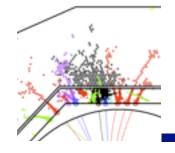






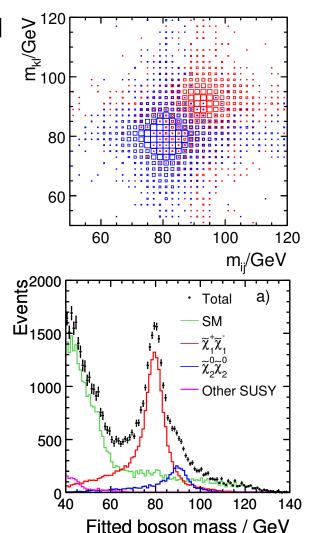
- Scintillator optimises energy resolution and imaging capability
- SiPM technology is established, understood and holding further potential
- Nothing on the AHCAL is particularly closely related to detector size or tracking technology
- There is relatively little required to arrive at a fully qualified option for SiD

Back-up



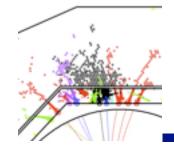
Jet energy resolution

- At the ILC, must separate hadronic W and Z line D+ and Ds at Belle
- Famous "blue plot": study strong electroweak symmetry breaking at 1 TeV
 - WWvv, ZZvv production
 - but this is not the only one
- $H \rightarrow WW^*$, ZZ* (total width)
- $H \rightarrow cc, Z \rightarrow vv$
- Chargino neutralino separation
- In contrast, multi-jet final states like ttH are rather insensitive
 - jet finding dominates

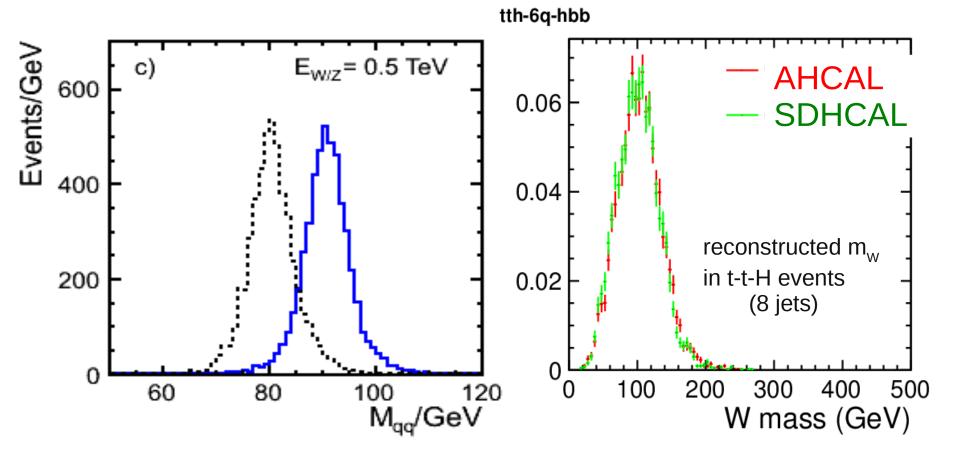


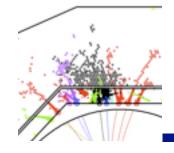
Events

Events



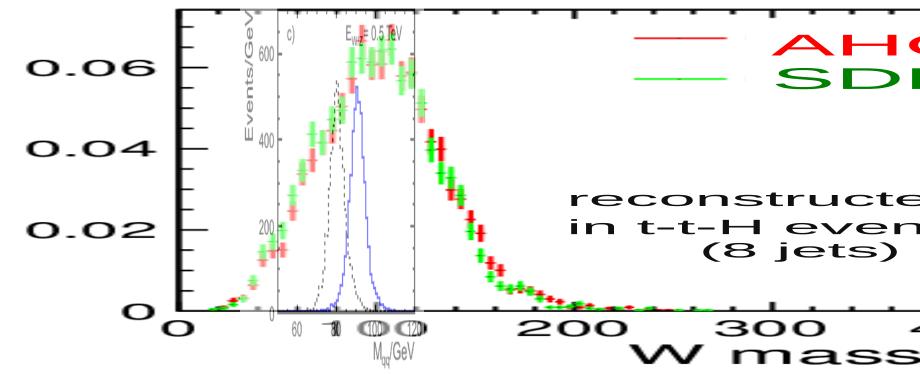
W Z separation

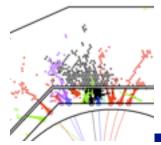




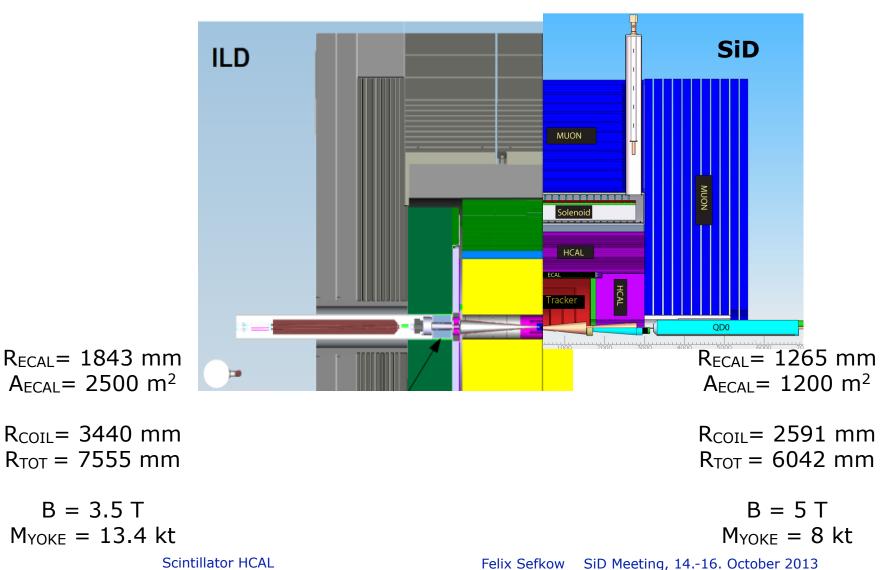
W Z separation

tth-6q-hbb

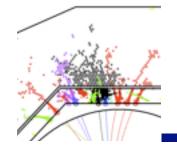




Design parameters



29

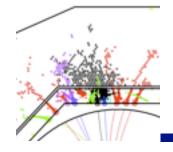


Parameter optimisa tion

- Was done for the LOI
 - see Angela Lucaci's talk at ILD meeting in Cambridge, 2008
 - tile size
 - tile thickness
 - absorber material
 - absorber plate thickness
 - total thickness
 - dead zones
- Revise main cost drivers with^{*}/₇ new Pandora
 - absorber plate thickness d
 - tile size g; varying?
 - total thickness T
- Strip option: need SSA

Absorber thickness 0.58 Absorber thickness که و. 29% کو 10.54, 10 GeV K⁰,'s GeV 0.050 ੋ_ਡ 0.045 ш 0.52 SMB 0.50 0.040 SWB 0.035 0.48 0.030 Absorber/scintillator ratio Absorber/scintillator ratio note zero suppression ECAL+HCAL 100 90 60 Raspereza 2004: Quality, 50 transverse and longitudinal 1x1cm²x1 30 3x3cm²x1 sampling important 5x5cm²x1 20 3x3cm²x2 for shower separation 10 0 5 10 15 20 25 30 Distance between showers [cm]

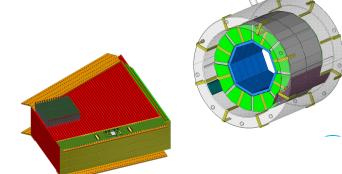
single hadrons and jets:



Industrialisation: Numbers!

ITEP

- The AHCAL
- 60 sub-modules
- 3000 layers
- 10,000 slabs
- 60,000 HBUs
- 200'000 ASICs
- 8,000,000 tiles and



- One year
- 46 weeks
- 230 days

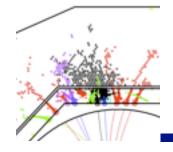


• 2000 hours

• 100,000 minutes

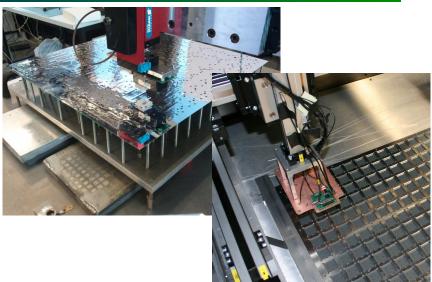
• 7,000,000 seconds

31

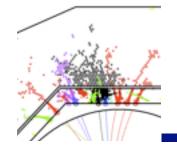


Quality control and production

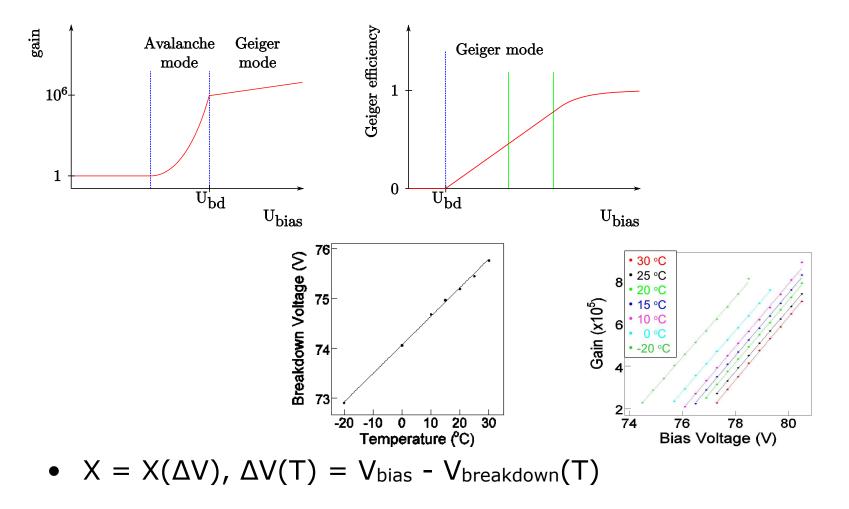
- Can be done: our engineers are looking forward to it
- There are interesting problems to solve
- Some efforts started:
- SiPM and tile QC and characterisation
 - with UV light and beta source
 - fully automatised, fast parallel readout
- LED ad ASIC test
- HBU assembly
 - place tiles, sc
 - close relatior coupling
 - impact on (CALICE Meeting Annecy 09 / 2013

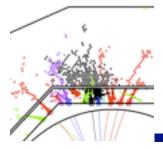






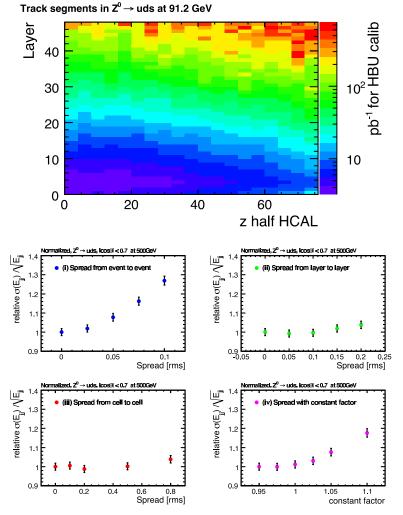
SiPM response



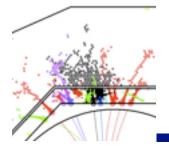


Calibration: look at full chain

- LOI validation: IDAG triggered study of required precision and luminosity for calibration
- MIP (= cell energy) scale well understood
- LEDs for gain monitoring: issue is not cost, but time, and bandwidth
- need to optimize strategy, possible feedback on design and specs
- Test bench is part of calibration study required precisions vs. time needed for procedures
- "Precision" = measurement accuracy or device-to-device non-uniformity



Calice Analysis note 18 and ILD noteFelix SefkowSiD Meeting, 14.-16. October 201334



ILD ECAL

- 2500 m² sensor area: main cost driver
- work with industry on unit cost
- re-visit optimisation with new degrees of freedom
 - hybrid scintillator silicon transition
 - variable transverse segmentation
 - vary sampling (no of layers)
 - account for inter-calibration
- strip alternative

