

# Status of pixel readout

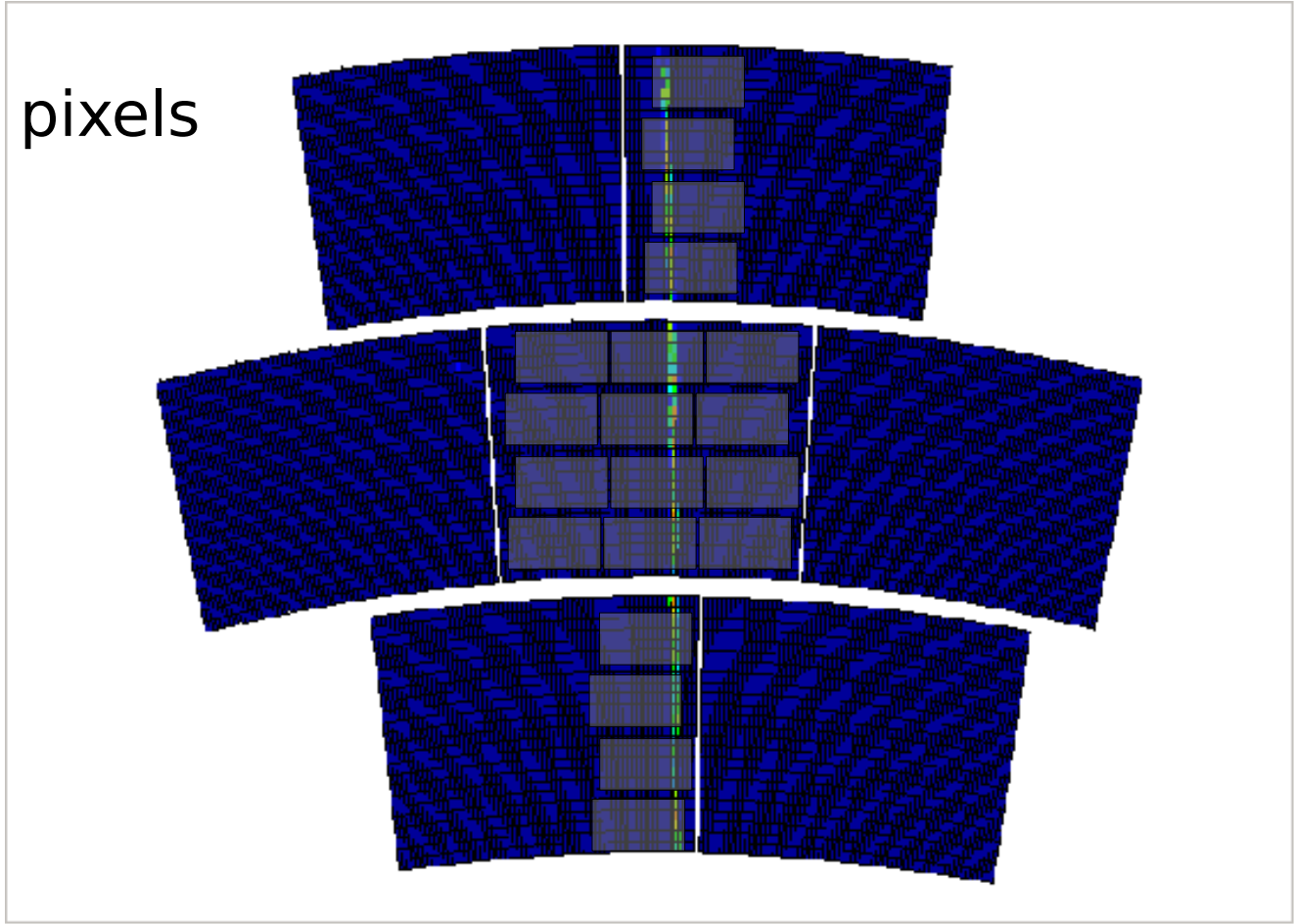
Jochen Kaminski  
University of Bonn

WP meeting 211, 8.1.2015

# Plan for test beam 2015



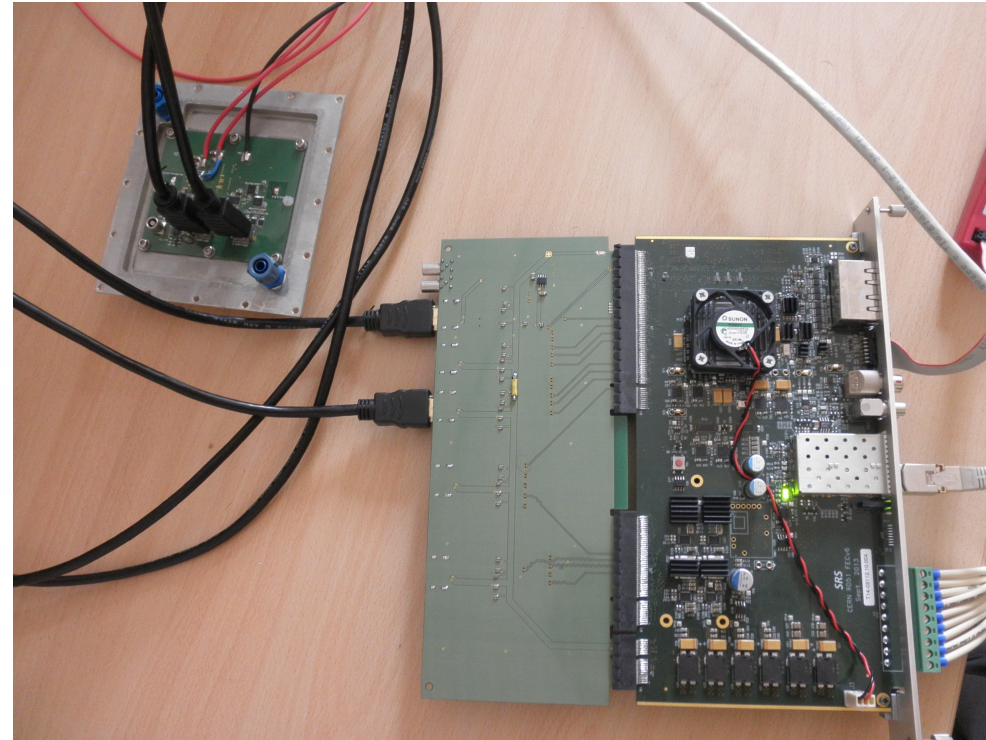
- 1 fully and two partly equipped modules with InGrids
  - 160 chips
  - 10,485,760 pixels



# Readout System



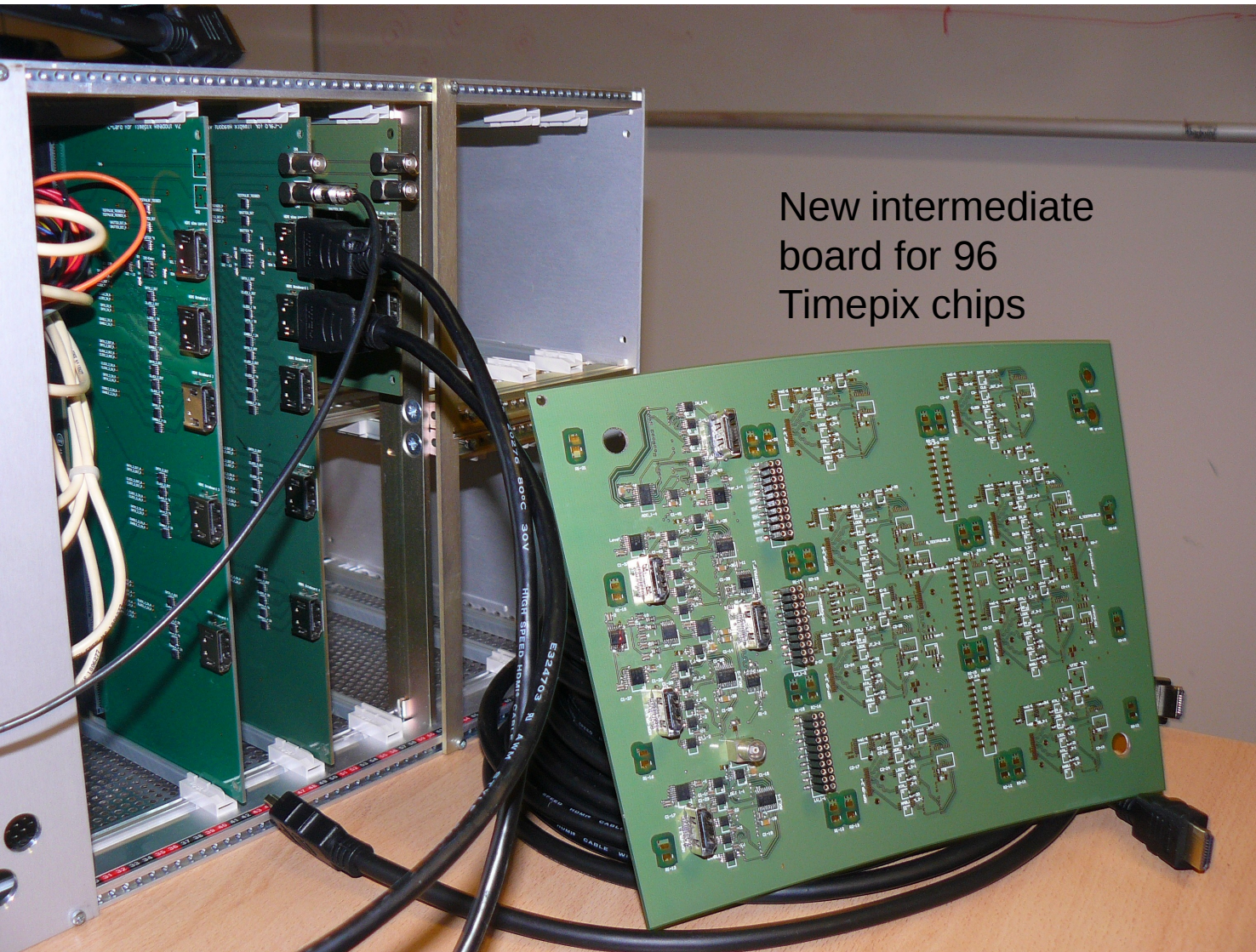
C Card on FEC6



5 C-cards needed – 5 available  
5 FECs needed (preferentially v6) – 3 available



# Readout system



New intermediate board for 96 Timepix chips

3 FEC6 with C-Cards

Each FEC/C-Card:  
5 HDMI plugs:  
1 for slow control  
1 per octoboard

=> 4 octoboards/  
FEC

Up to 15m HDMI



# Readout system



3 FEC6 with  
C-Cards

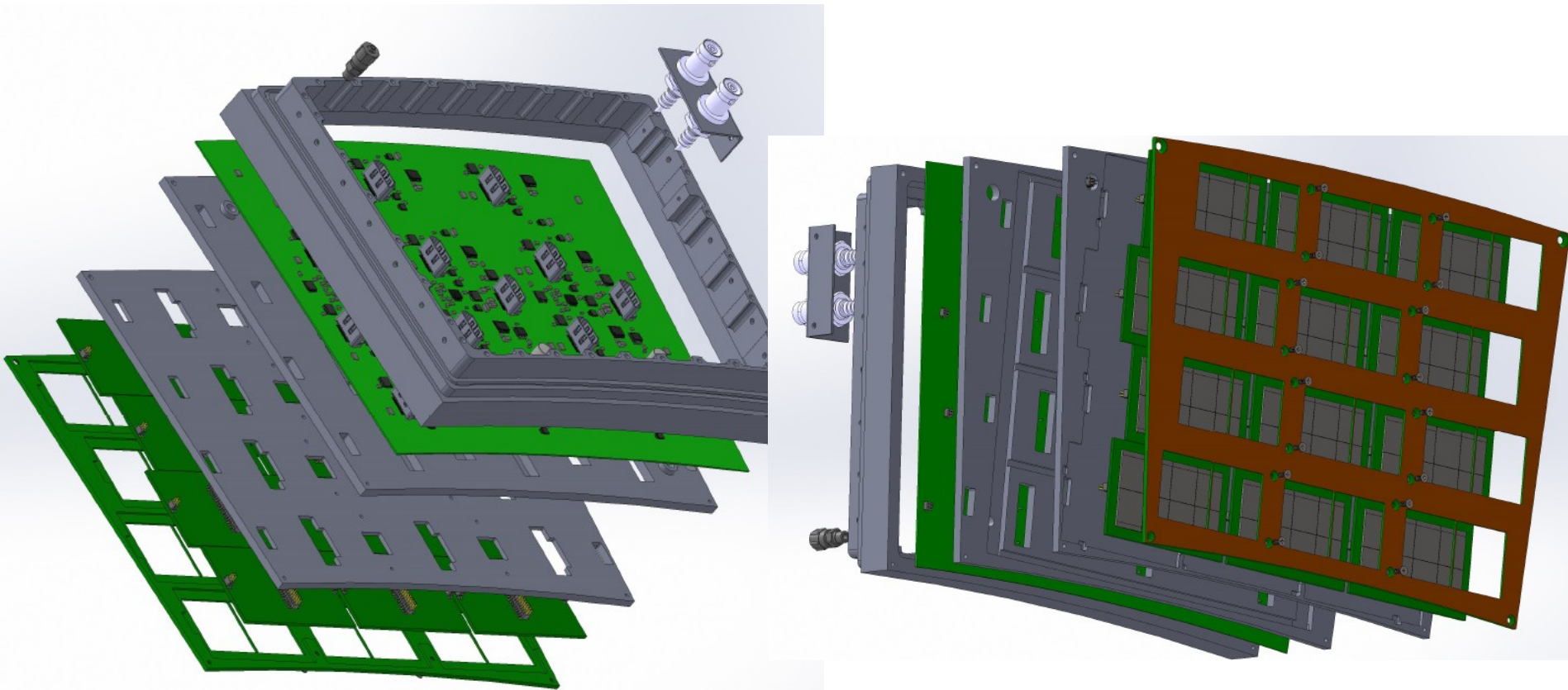
Each FEC/C-Card:  
5 HDMI plugs:  
1 for slow control  
1 per octoboard

=> 4 octoboards/  
FEC

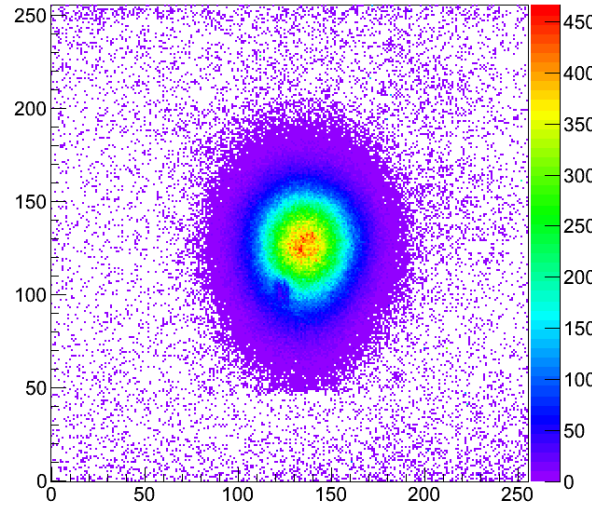
New intermediate  
board for 96  
Timepix chips







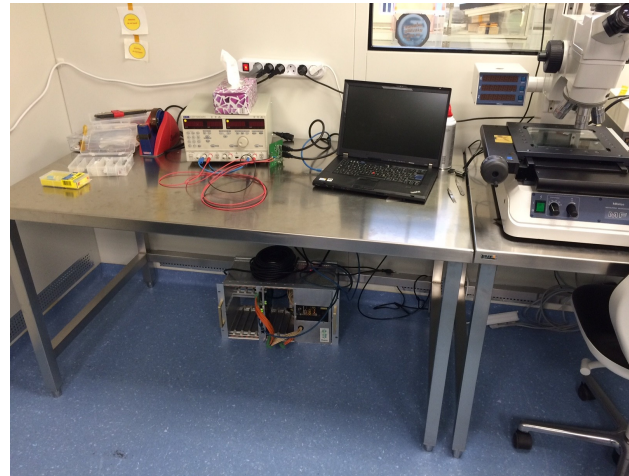
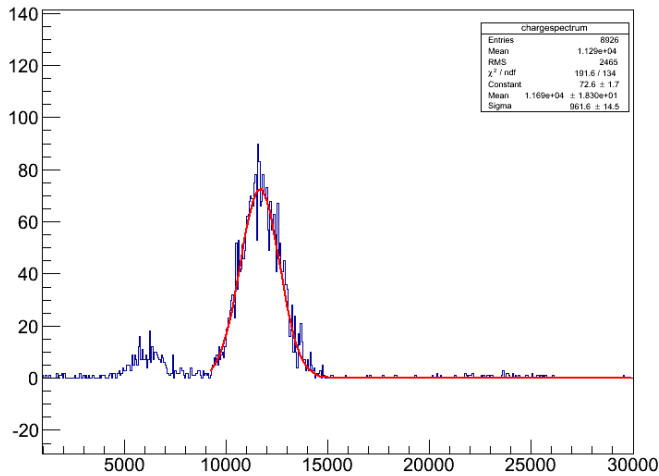
# InGrids - IZM6



~130 InGrids from IZM6

1 InGrid is being tested:  
Good performance

Test stand in clean  
room is ready



# Summary and Outlook



Backframes:	3/3 available
Intermediate boards:	1/3 available
Cooling modules:	in the workshop
Octoboards:	30/20 ordered
InGrids:	168/160 available
Anode:	ordered
C-Cards:	5/5 available
LV-cards:	1/4 available
FECs:	3/5 available
HDMI cables:	23 still to order
Tubes for cooling:	still to be ordered