

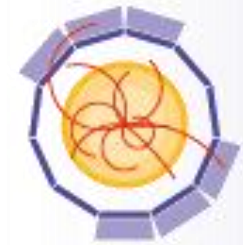


AIDA

Advanced European Infrastructures
for Detectors at Accelerators

AIDA-2020 Common DAQ Links to CALICE DAQ





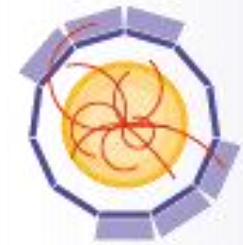
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AIDA-2020

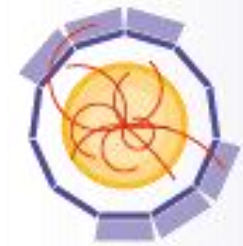
- AIDA-2020 funded.
- Started 1 May
- Work package for assisting ILC beam-test DAQ systems (WP5)
- Approximately 4 staff years of EU funded effort, the same in matching effort.
- Will only be a success if it succeeds in engaging with detector development communities.





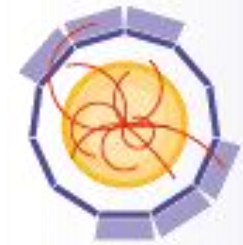
CALICE common DAQ: Existing Plan

- Use EUDAQ for run control messages
- (Probably) use TLU to control one or more CCC
- Will use TLU for interface to other detector types
(tracking etc.)



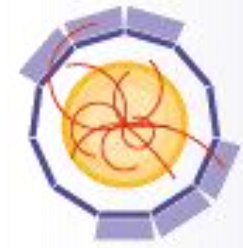
Hardware - Issues

- Document definition of signals TLU \leftrightarrow CCC
 - Already done informally at ALCW2015 and earlier
 - **See** <http://www.ohwr.org/projects/fmc-mtlu/wiki> , http://svn.ohwr.org/fmc-mtlu/trunk/documents/firmware/latex/AIDA_TLU_note.pdf
- Technical tests needed
 - e.g. Is TLU clock jitter low enough? (Jitter peaking for chained PLLs ?)
- Share firmware modules ?



Software - Issues

- EUDAQ original designed as a framework
 - user provides small, detector specific pieces, then complete DAQ system built including these pieces
- Work done on interfacing existing CALICE DAQ to EUDAQ run control (Taikan S.)
- Needs to be generalized and put back into EUDAQ repository.
- Responsibility for EUDAQ maintenance being moved from Richard Peschke to Jan Dreyling



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AIDA-2020 “Kick-off” meeting

- At CERN, 3-5 June.

