



Plans for ASIC tests at Wuppertal

C. Zeitnitz
Bergische Universität Wuppertal

People

- Matthias Götze and Oskar Hartbrich (at DESY) focus completely on Analysis of testbeam data
- New PhD student for hardware activities will start in January
- Peter Kind (electronic technician)
 - Part time available
- Support by mechanical engineer and workshop
- CZ
- Attract students as soon as the activities in the Lab in Wuppertal get started again



Towards a test setup for SPIROC Chips

- Training of new PhD student at DESY
 - Starting in February
 - Need to transfer knowledge about the operation and testing of the SPIROC
- Obtain test setup for single SPIROC chips
 - Gain experience and develop software
 - Start with SPIROC2D and switch to version 2E asap
 - Will need two setups (TQFP as well as BGA)
- Design and build larger scale system for more automatized tests
 - Hope to get this done by summer/early fall 2016 (at least first stage)
- Start testing SPIROCs for production in fall 2016



What else we could do

- LED selection measurements
- LED uniformity measurements for fully equipped HBUs
 - Before installing the tiles
 - Do we need this?
- Get involved in testbeam activities
 - Software
 - Operation
 - Analysis

