

Session Program

19 November 2005

SILC meeting

R&D on Electronics

Vienna, 0--FJ2

Saturday 19 November

11:20

R&D on Electronics

Session | Location: Vienna, 0--FJ2

11:20-11:50

(Very) Deep Sub-Micron CMOS frontend Chips

Speaker

Jean-Francois Genat

11:50-12:10

ILC Trigger & DAQ issues

Speaker

Patrick Le Du

12:30-13:00

Pitch adapters, packaging, cabling: new techs

Speaker

Manuel Lozano

13:00