

SiLC meeting

Thursday, 2 February 2006

Beam tests and simulations (14:00 - 22:00)

time	[id] title	presenter
14:00	[9] General plan and present situation with beam tests	ZDENEK DOLEZAL
14:20	[12] Design and construction of FE electronics	JEAN FRANCOIS GENAT
14:45	[13] Detector prototypes, Electronics readout and DAQ system for the beam tests	GENERAL DISCUSSION
15:40	Coffee break	
16:30	[14] Review of the Simulation Boulder Workshop	FREDERIC KAPUSTA & FOR J. MC CORMICK
17:00	[10] Performance of a Silicon Tracking system with a TPC	MIKAEL BERGGREN
17:20	[11] General discussion on Simulation issues	W. MITAROFF
20:00	Dinner	