



# Snowmass Track/Vertex Parallel Sessions

## Wednesday 24 August 2005

### SiD Track/Vertex Parallel Session VI - Mountain Chalet--DALY (08:30 - 10:05)

time	[id] title	presenter
08:30	[32] Oregon-Yale CMOS Pixel Work	DAVID STROM
08:50	[29] Status of front-end readout chip development at Paris	JEAN-FRANCOIS GENAT
09:15	[30] Implementation of different detector concepts in MOKKA	VALERI SAVELIEV
09:40	[31] Medium-sized ladder development and test results	AUORE SAVOY-NAVARRO