

# High Voltage for amplification

## Integration in ILD

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# outline

- Module connection
- Technology dependence
- Cable
- Connectors
- Supplies
- Location

# Technology dependence

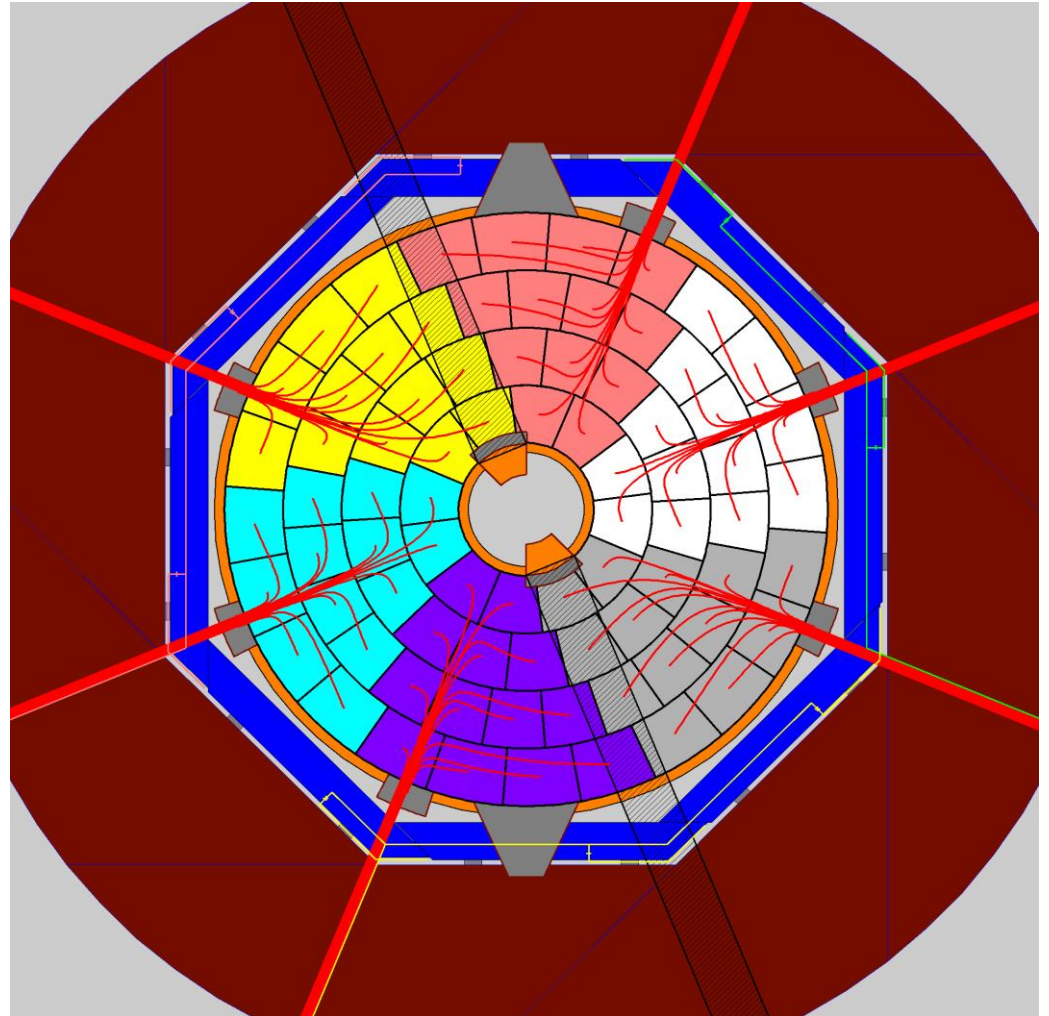
- Micromegas : 1 cable per module
- GEM : 1 cable per module, plus charge division
- Pixels : 1 cable per module, then distributed over ~100 chips?
- Note that the expected current is small anyway

# Cables and connectors

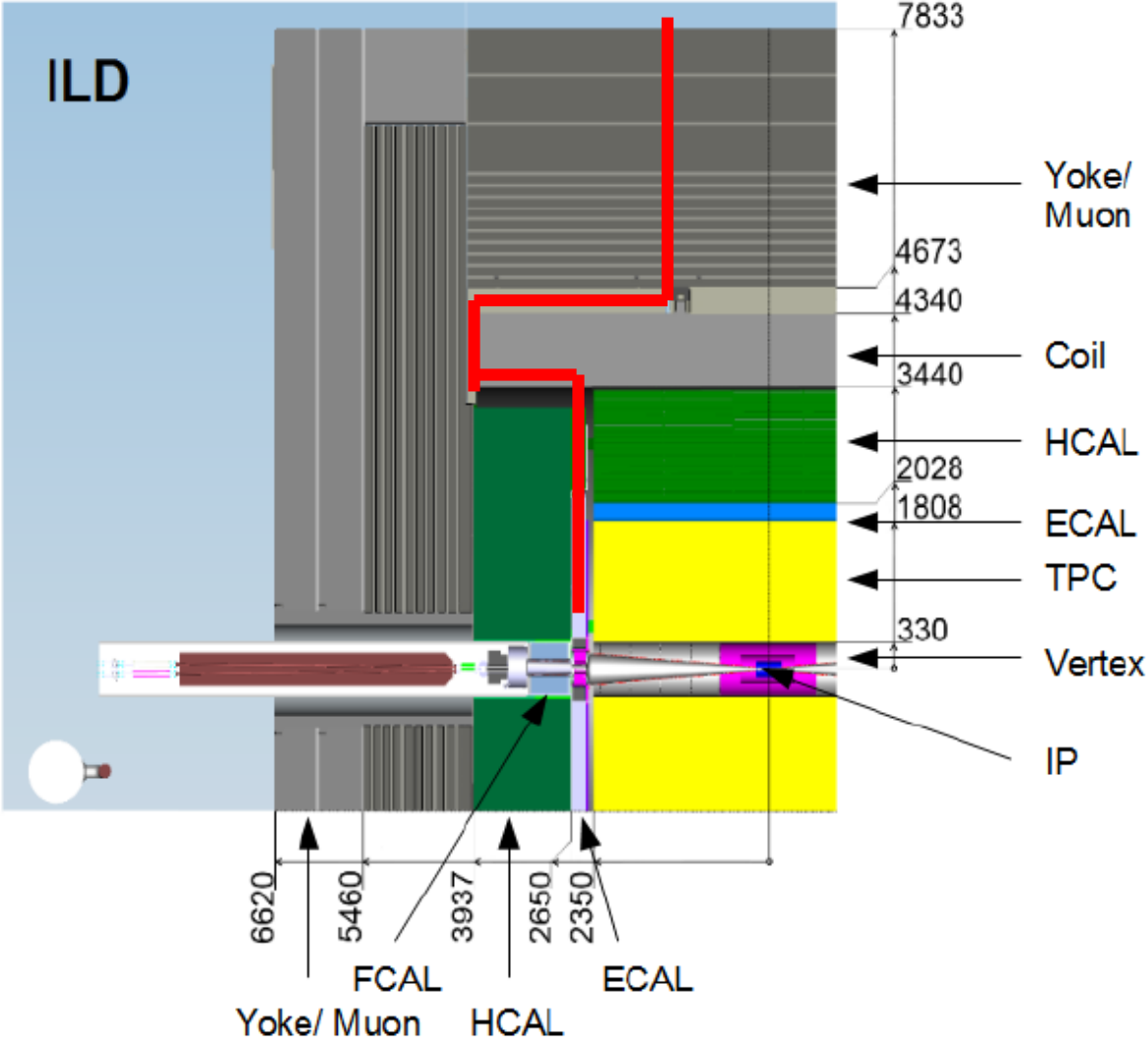
Filter box on each module

SHV connectors? Or smaller but as reliable (requires special study)

Cables can be thin but well insulated and shielded



# Cables path



# Supplies

2 MPOD Crates with 8 Wiener 16-channel HV supplies on each side



# Location

- In principle cables can be long ( $O(50\text{ m})$ ) but we have to check that the trip is not delayed by the length of the cable, and stored energy
- So either the platform (with difficult maintenance) or the gallery