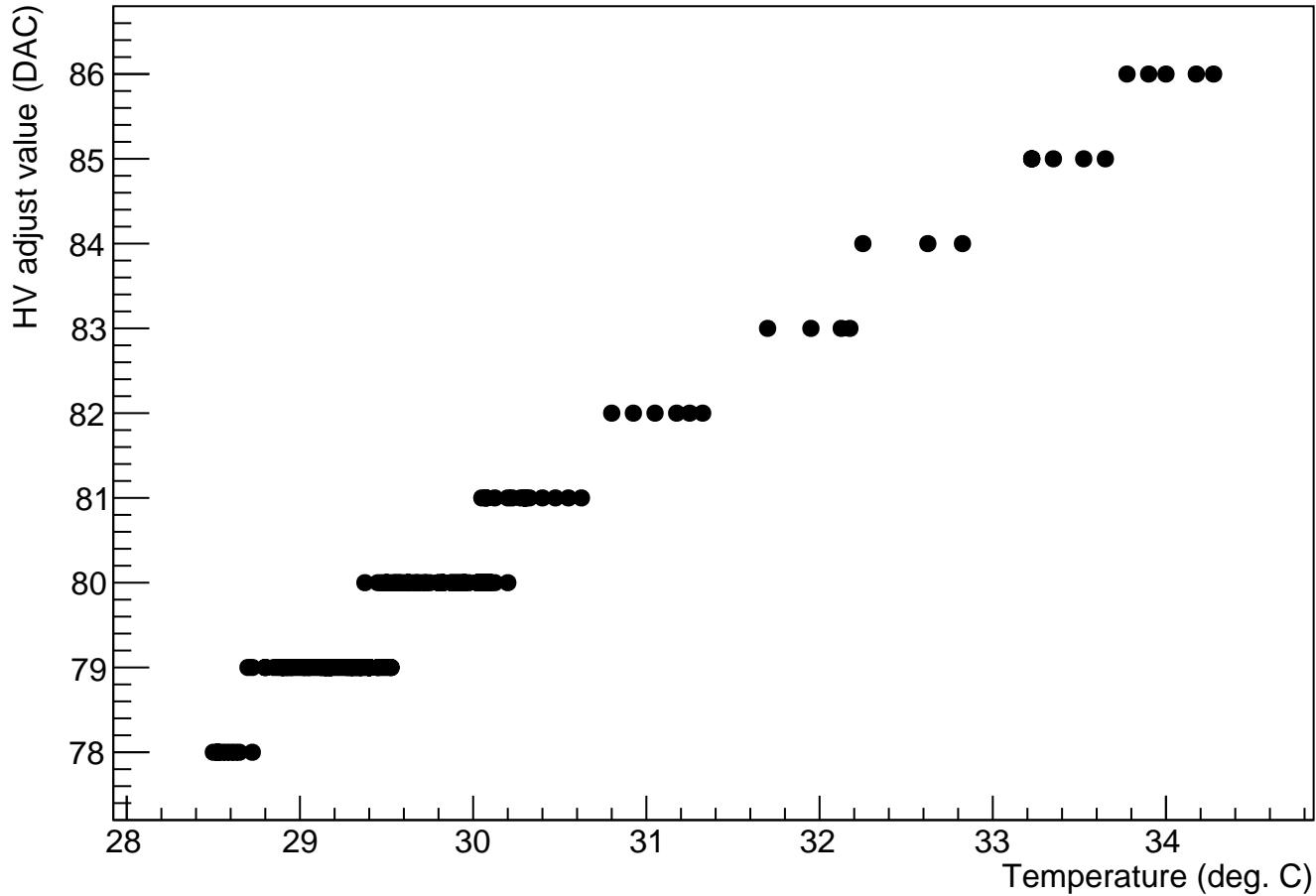
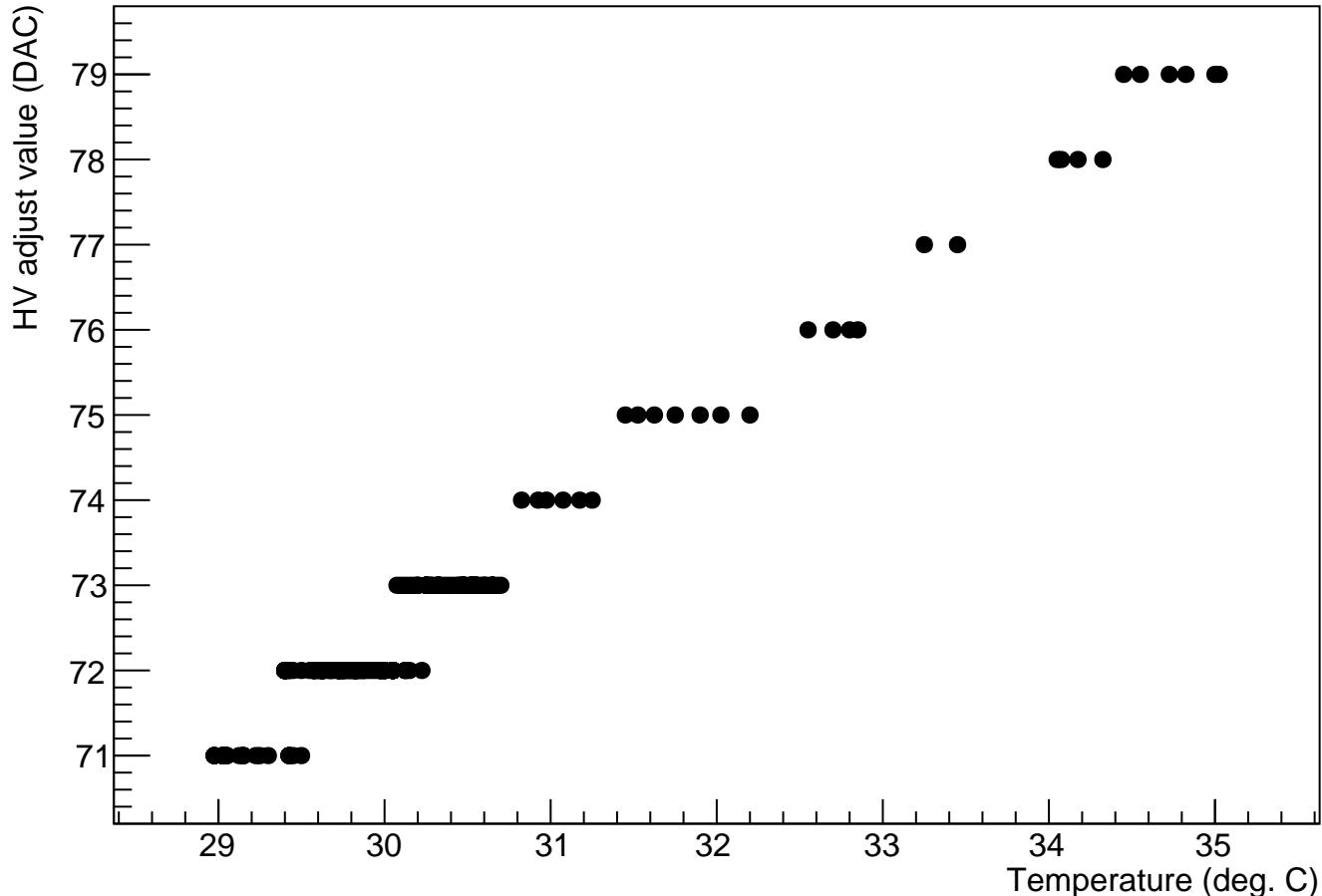


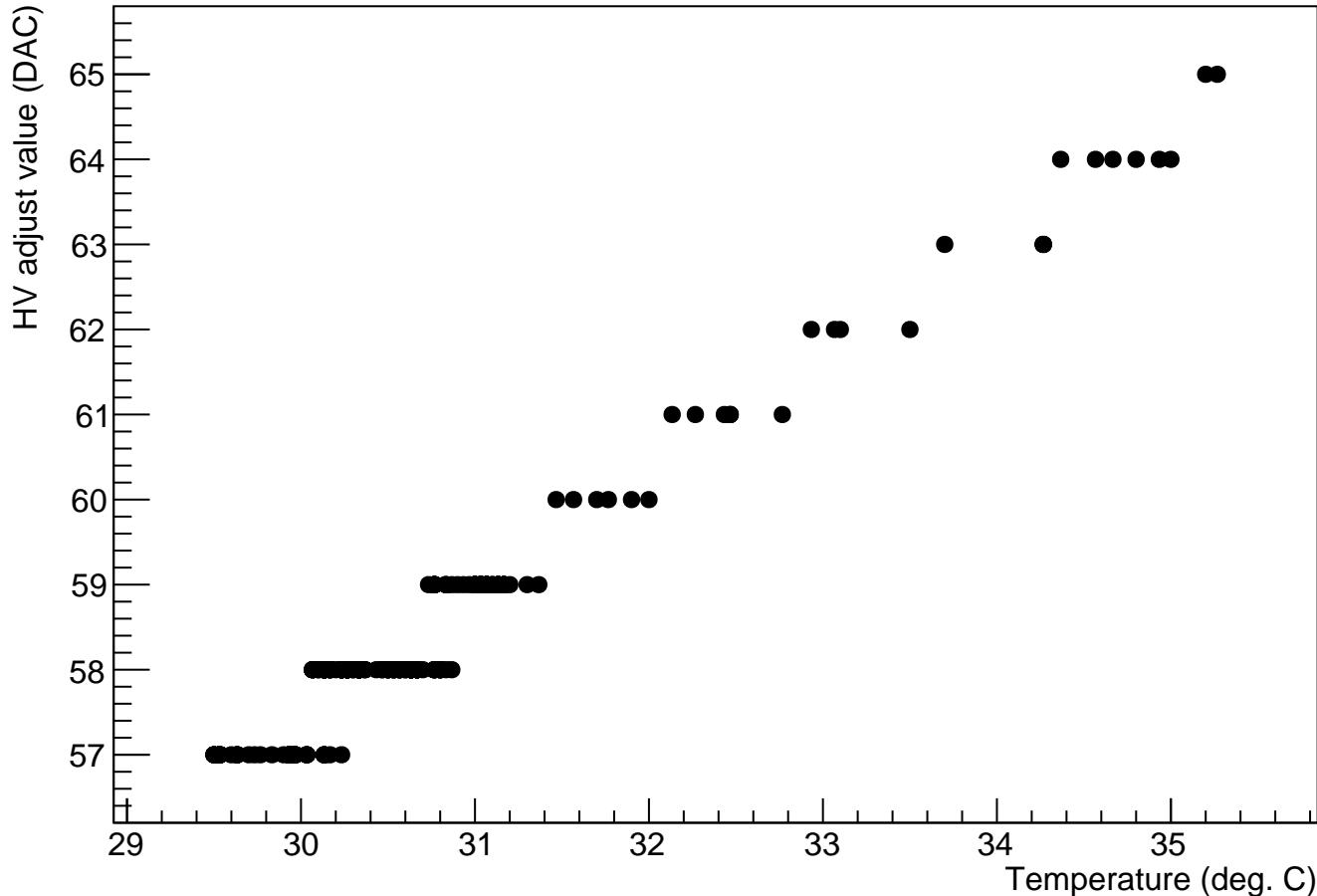
# gra\_hv\_temp\_lda0\_port1\_module2



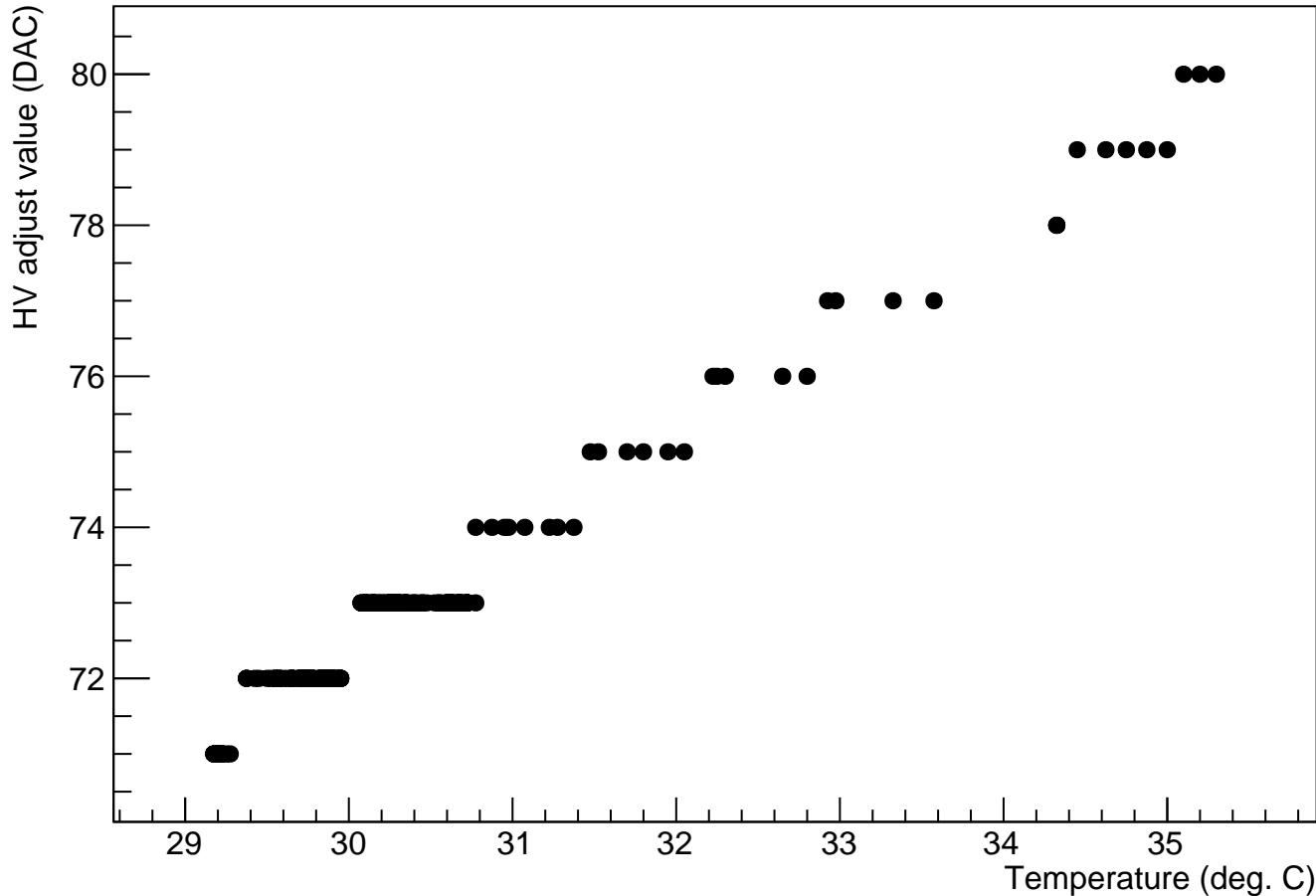
# gra\_hv\_temp\_lda0\_port2\_module3



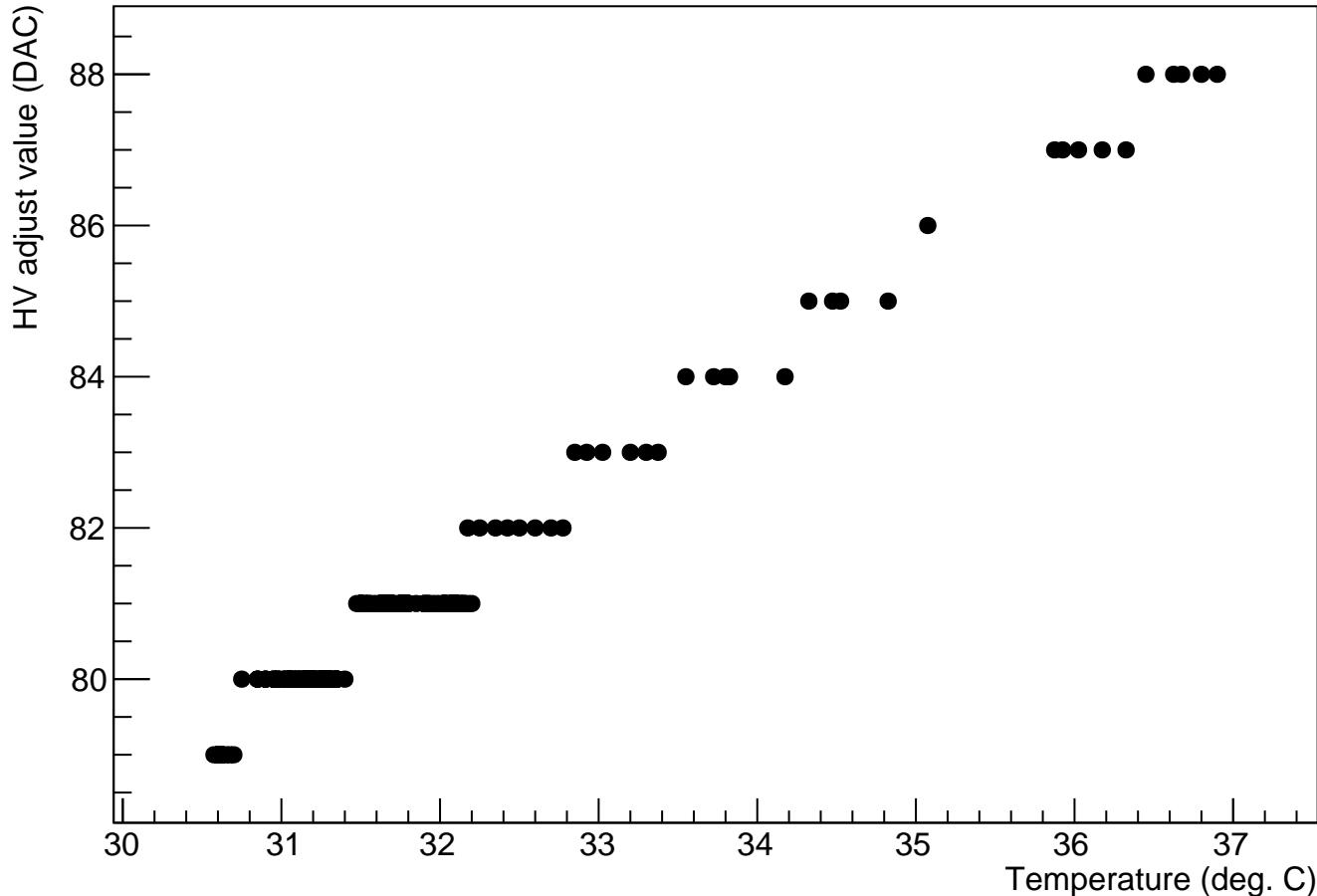
# gra\_hv\_temp\_Ida0\_port3\_module4



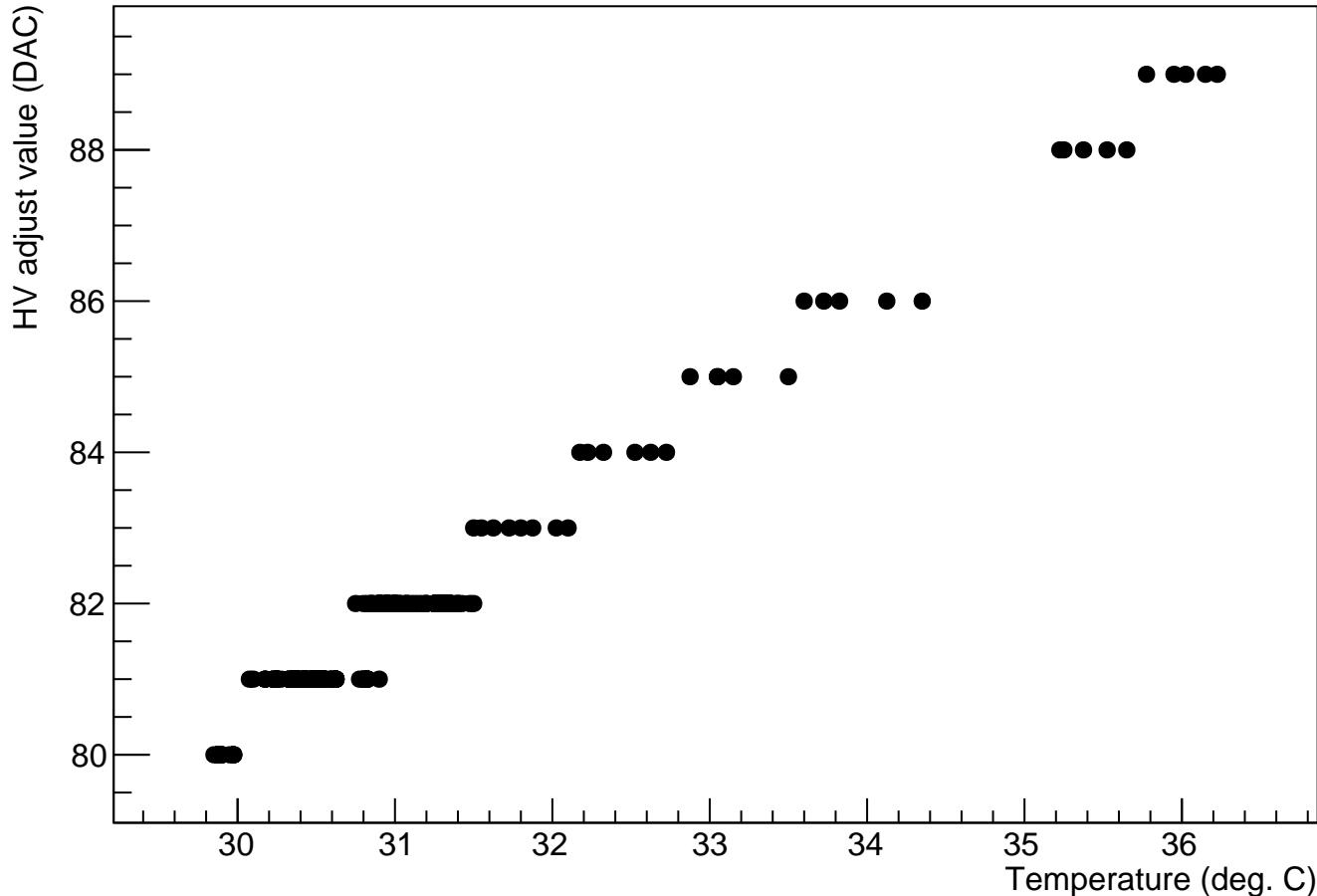
# gra\_hv\_temp\_lda0\_port4\_module6



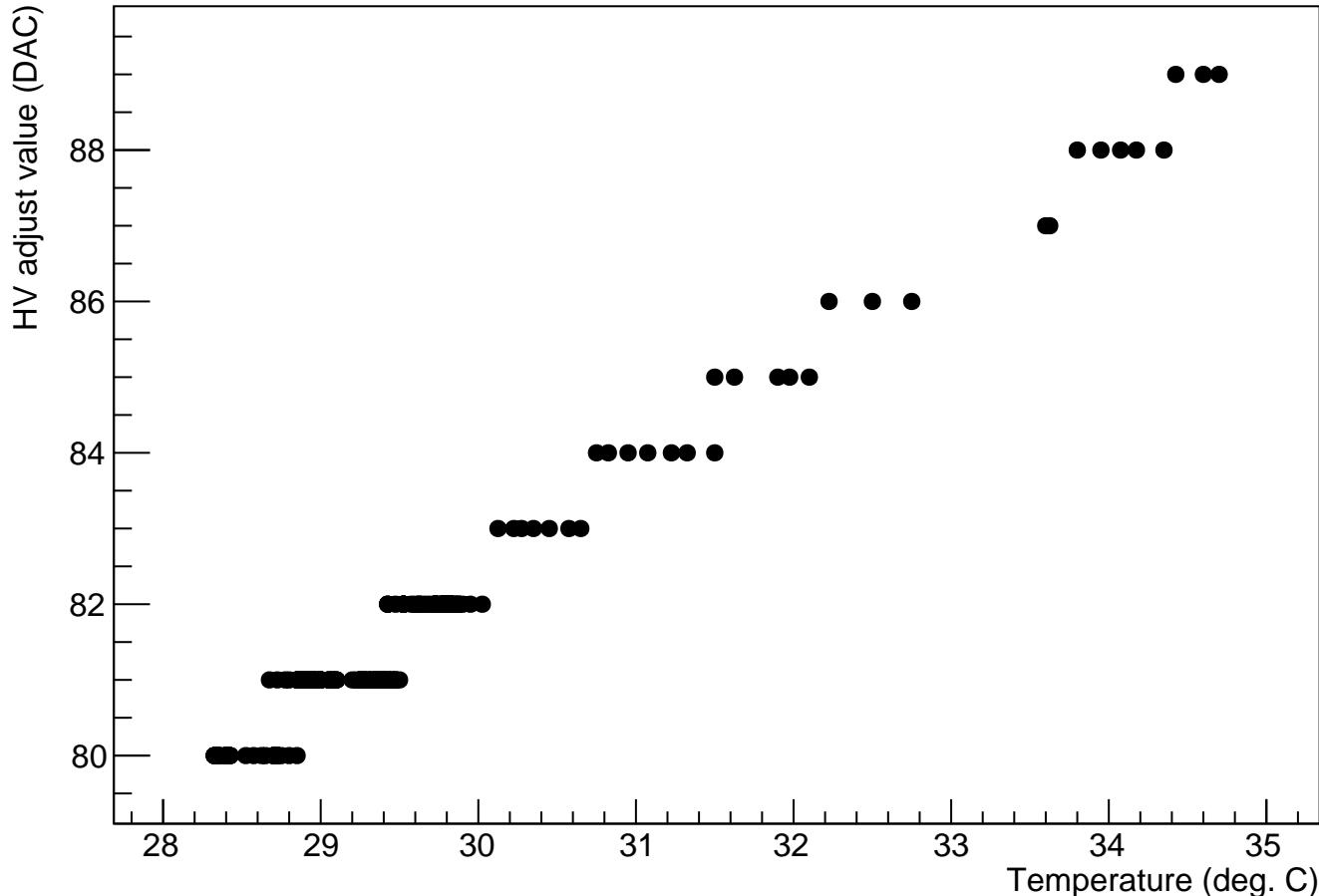
# gra\_hv\_temp\_Ida0\_port5\_module8



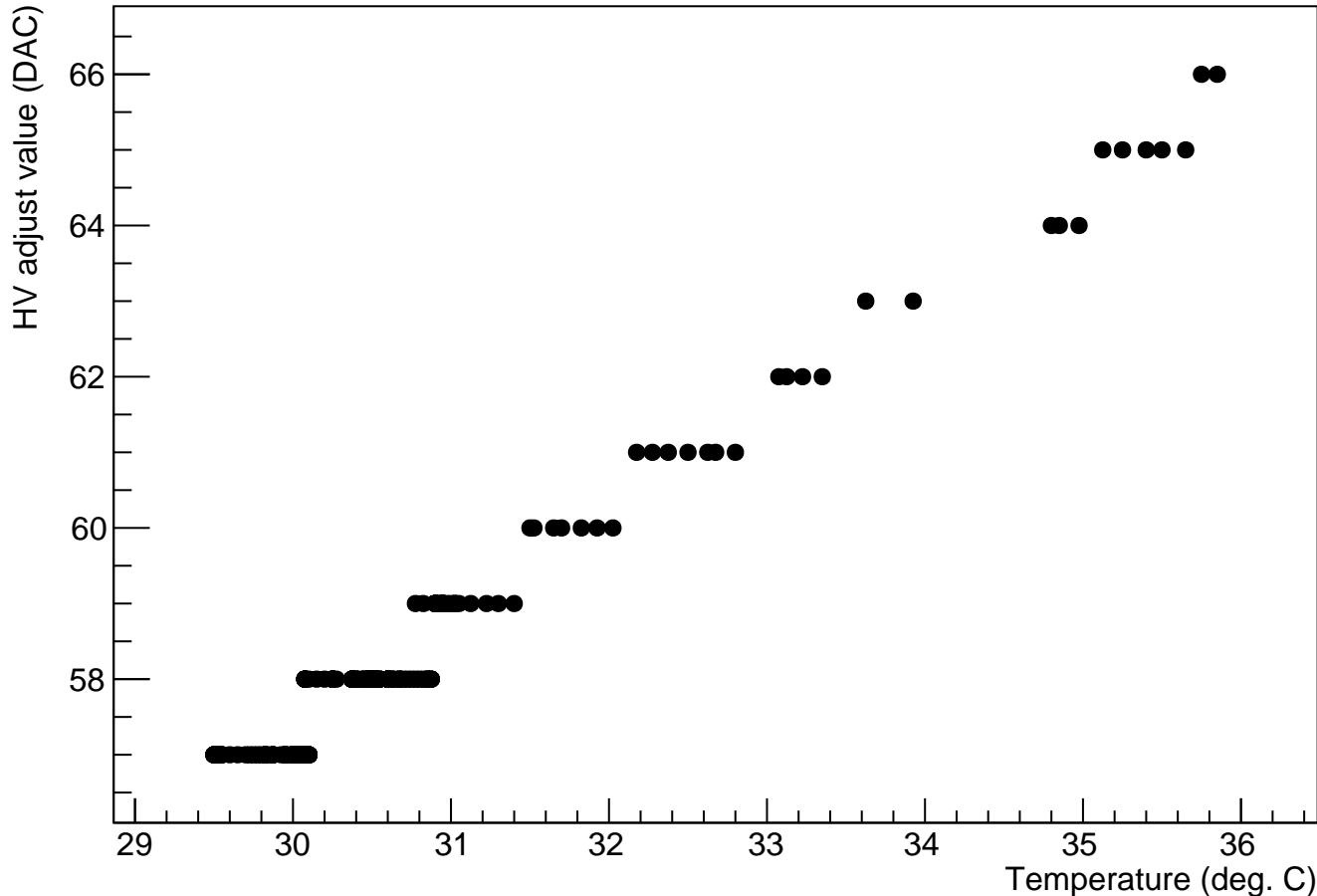
# gra\_hv\_temp\_lda0\_port6\_module9



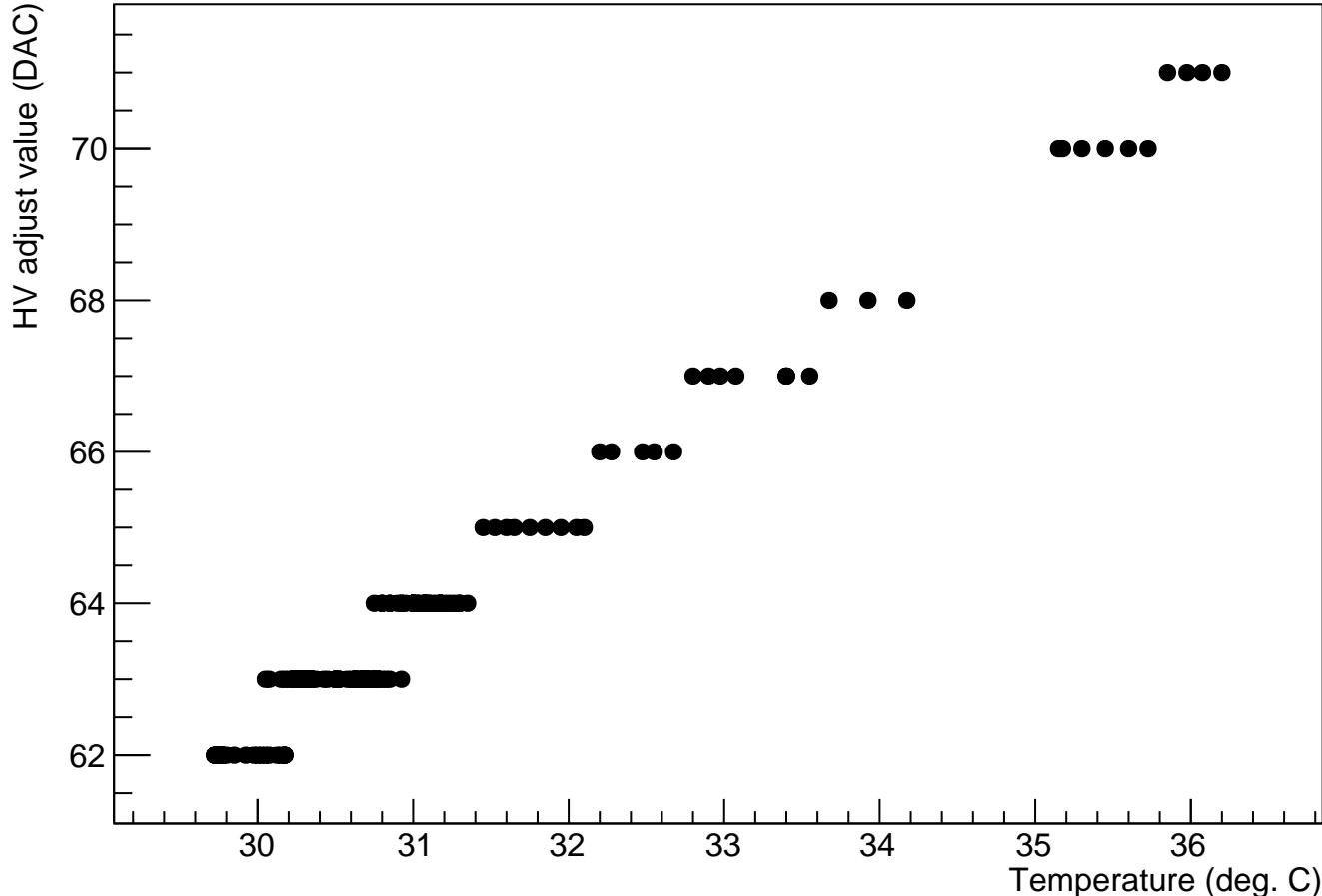
# gra\_hv\_temp\_lda0\_port7\_module10



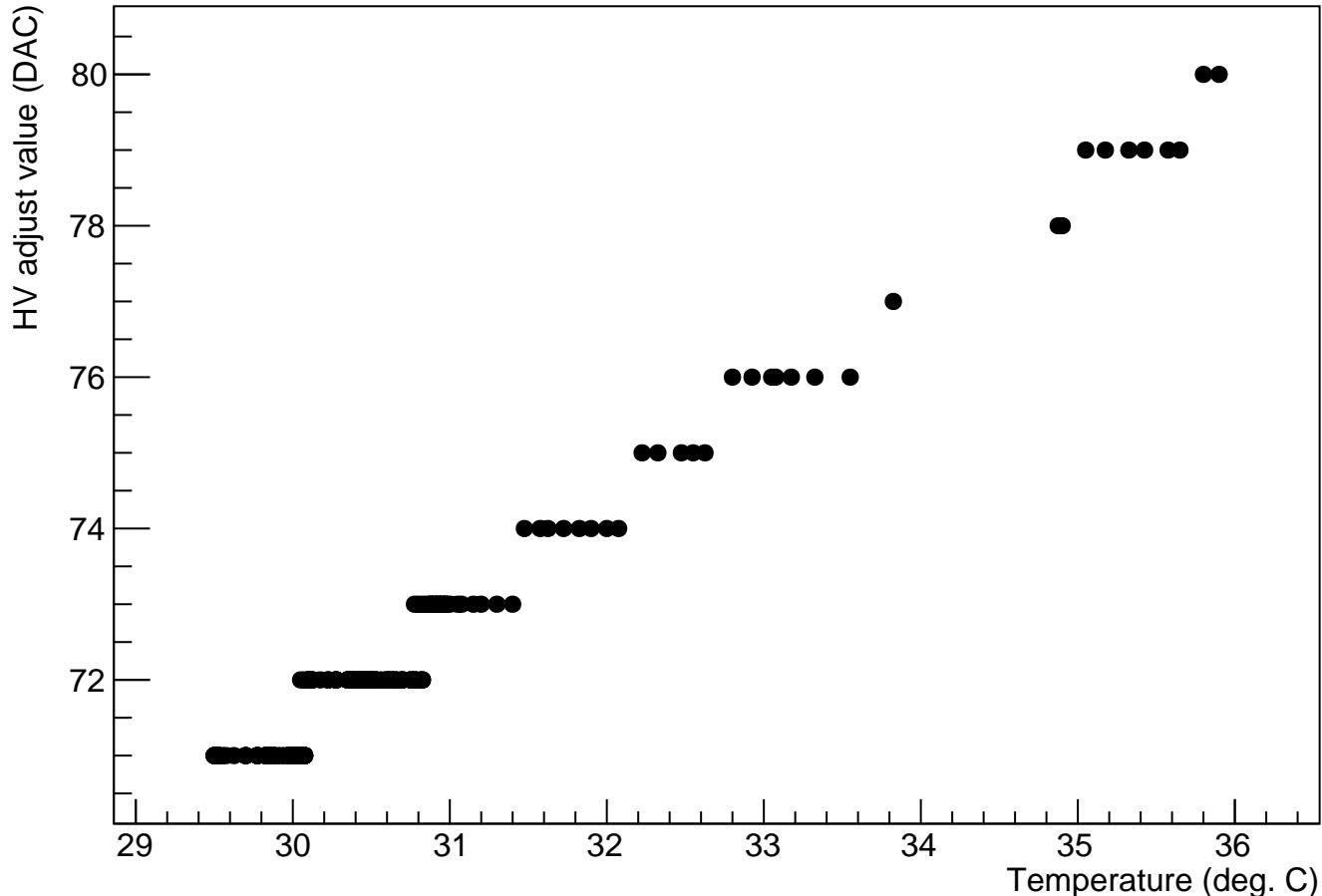
# gra\_hv\_temp\_lda0\_port8\_module11



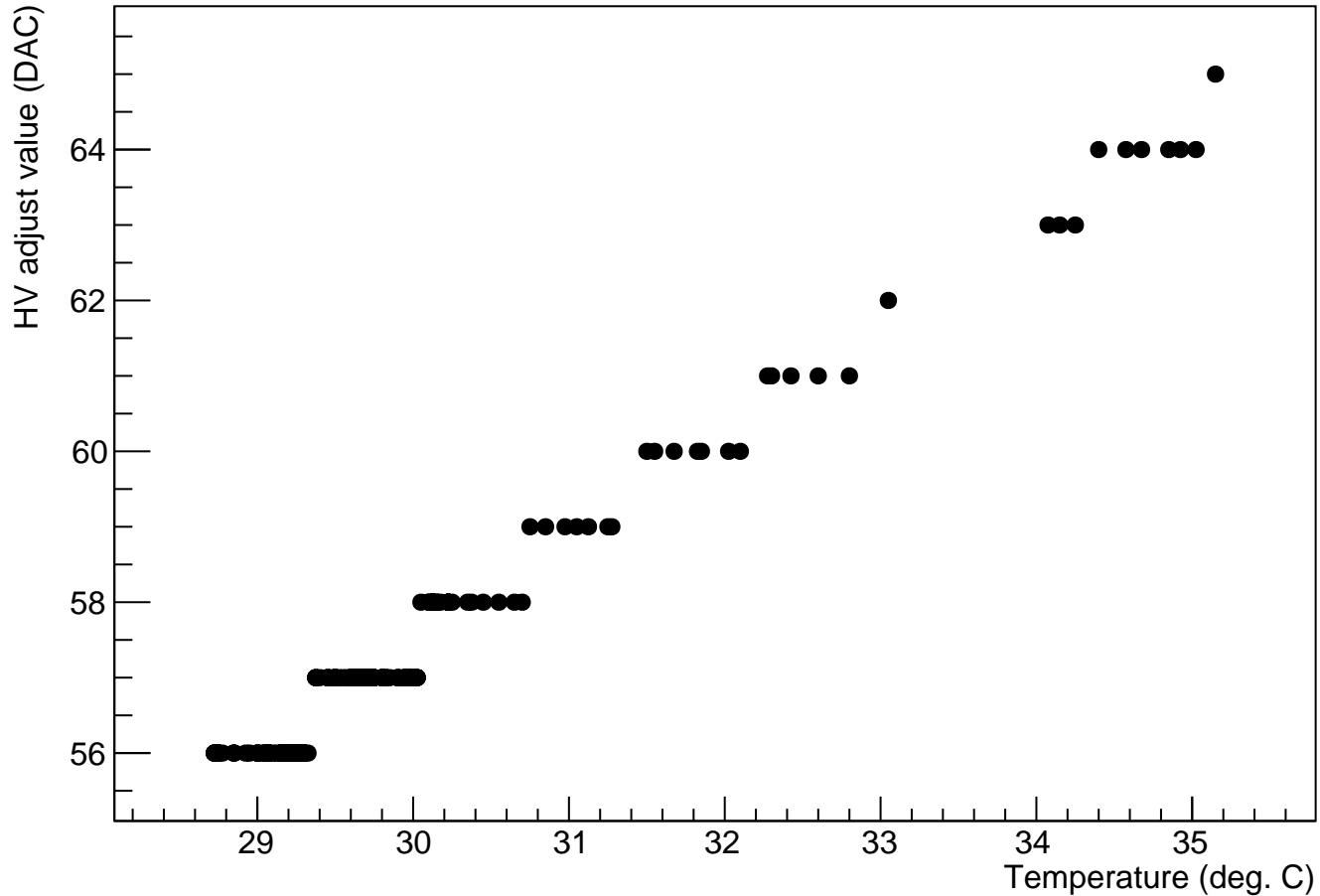
# gra\_hv\_temp\_lda0\_port9\_module14



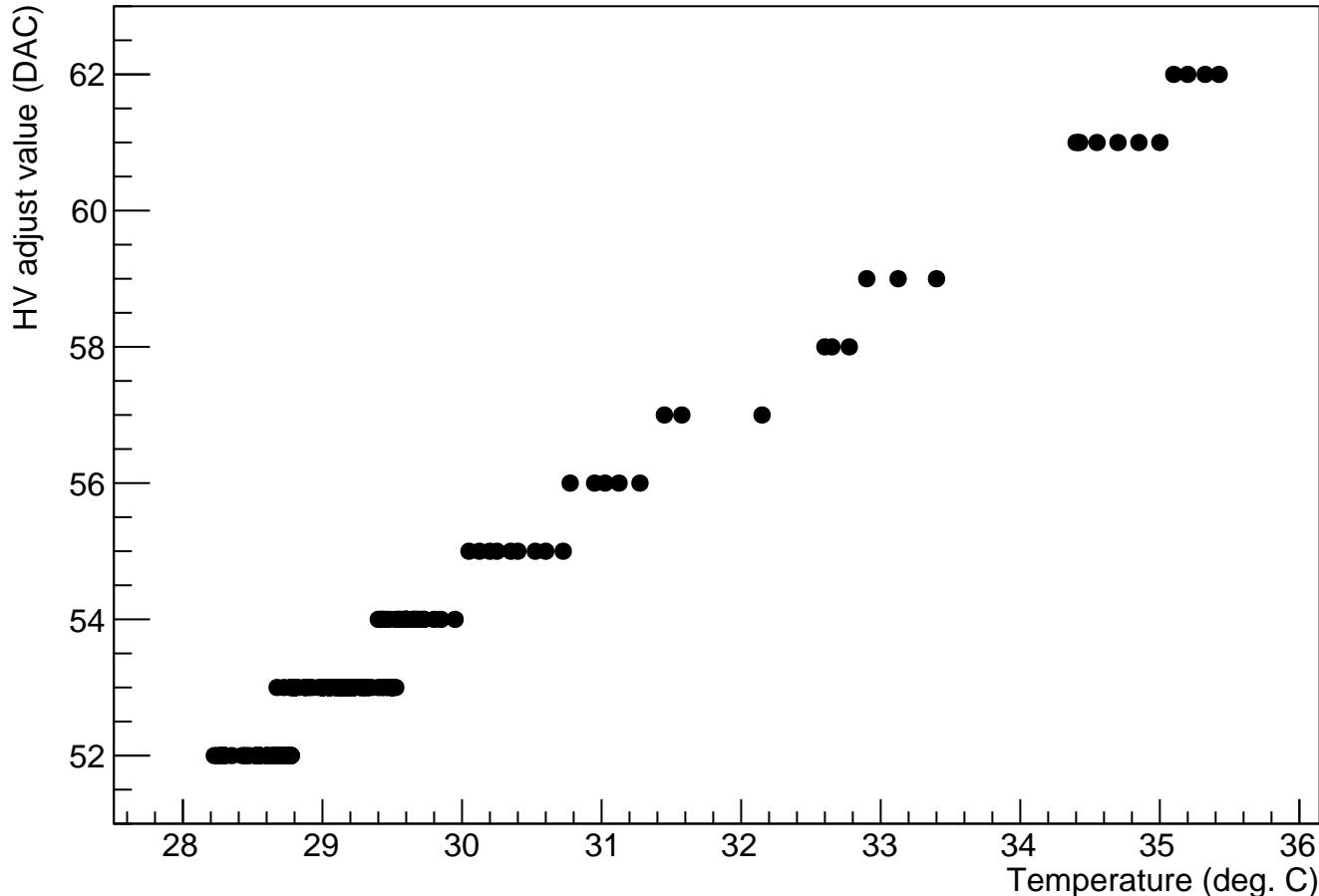
# gra\_hv\_temp\_lda0\_port10\_module19



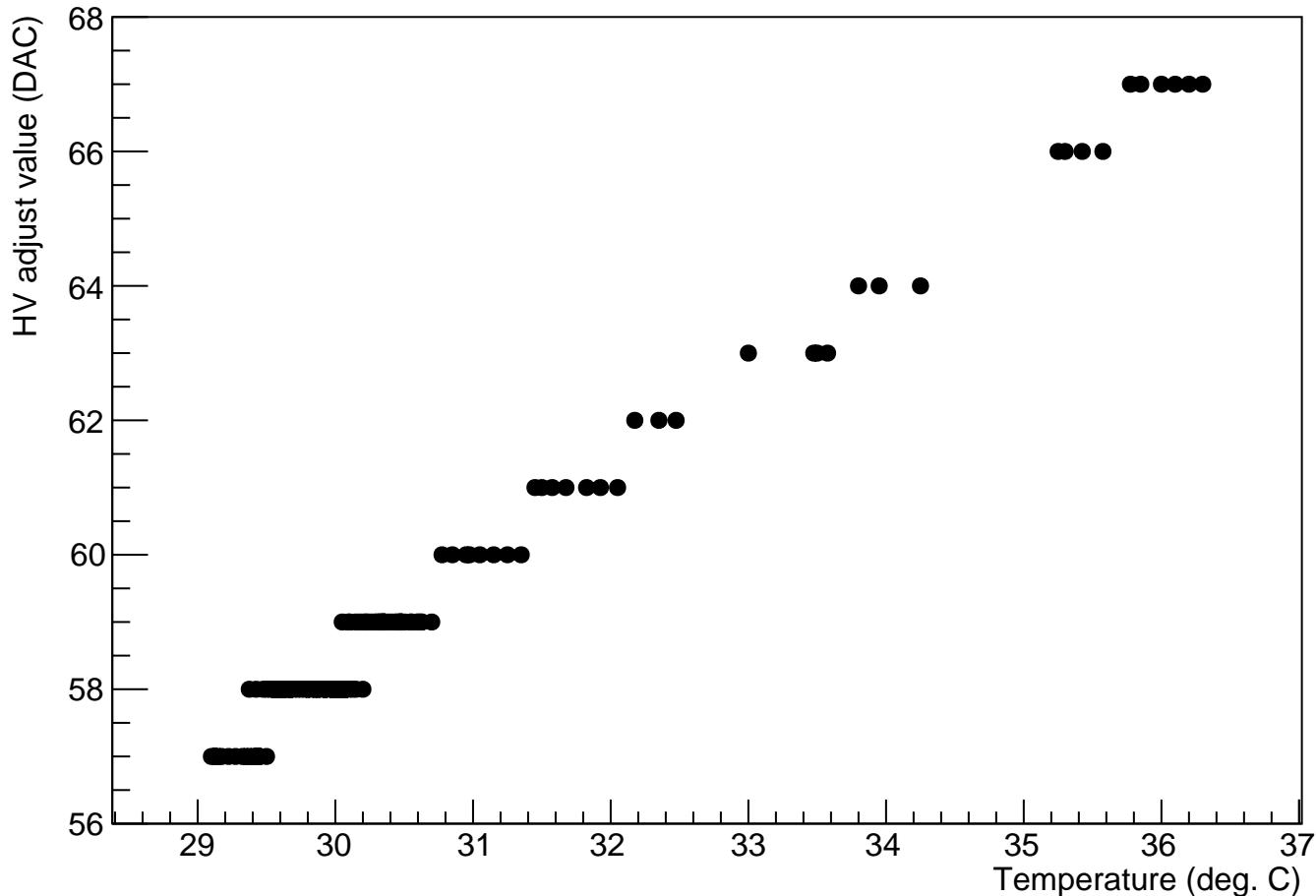
# gra\_hv\_temp\_lda0\_port11\_module21



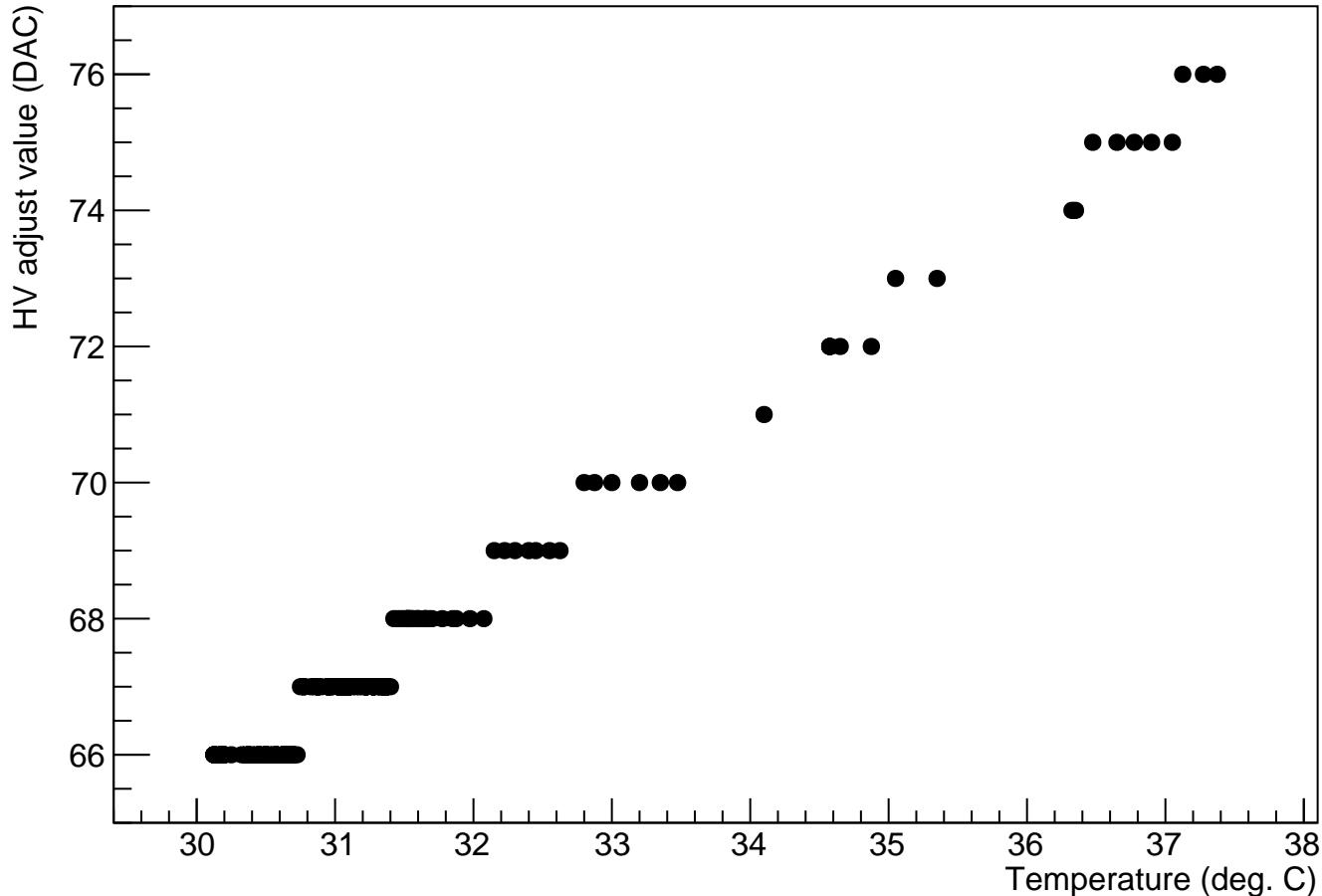
# gra\_hv\_temp\_lda0\_port12\_module23



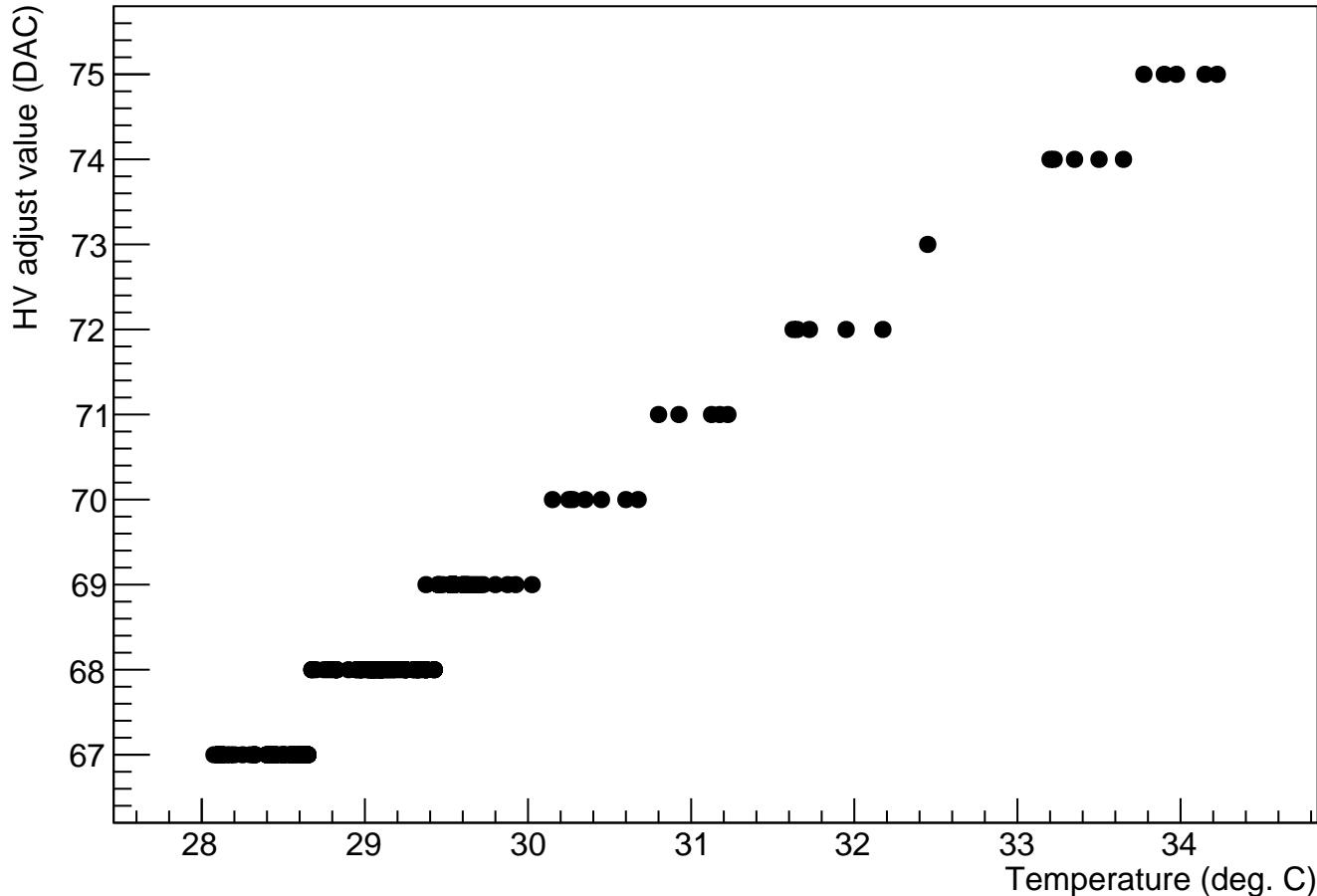
# gra\_hv\_temp\_lda0\_port13\_module24



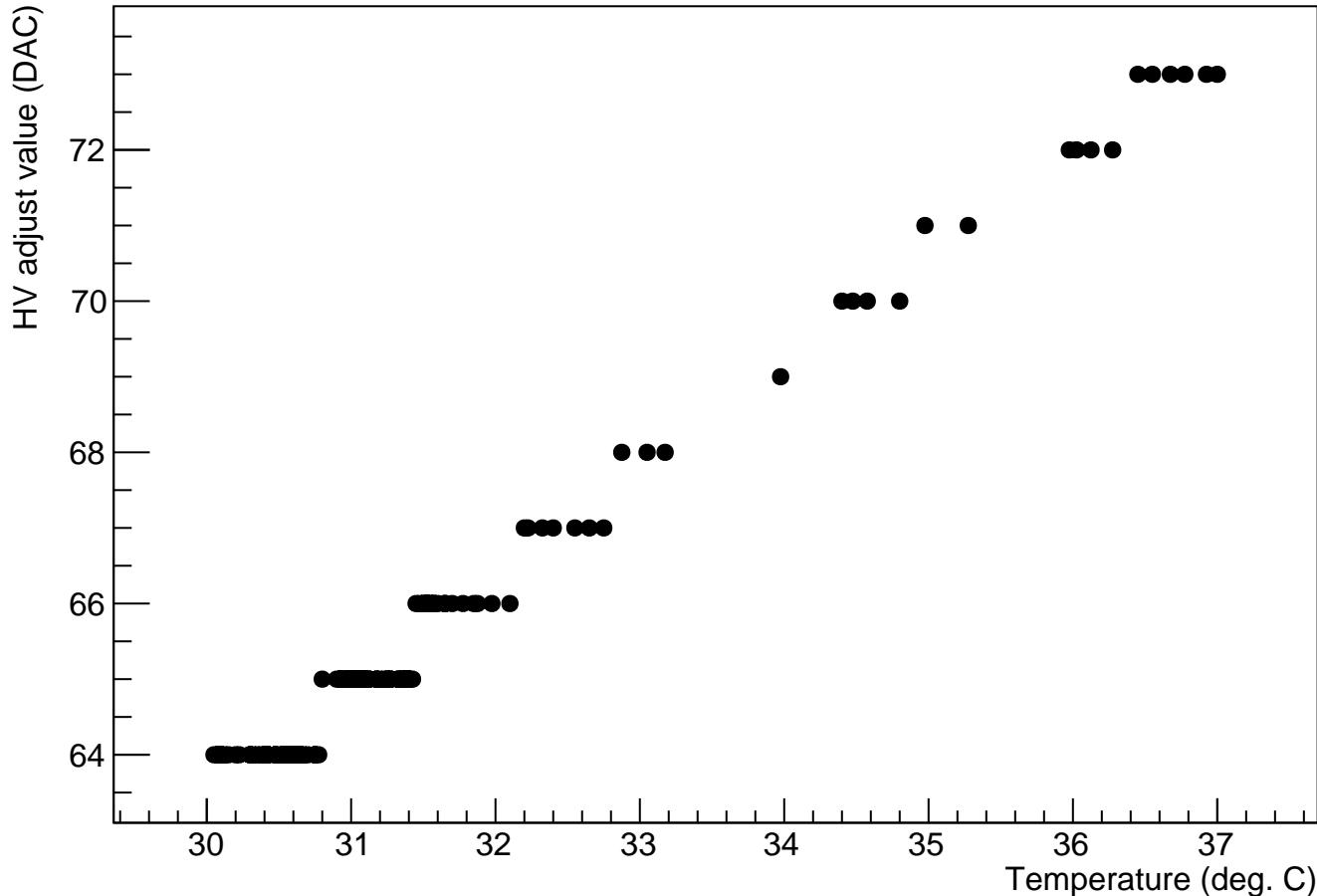
# gra\_hv\_temp\_lda0\_port15\_module22



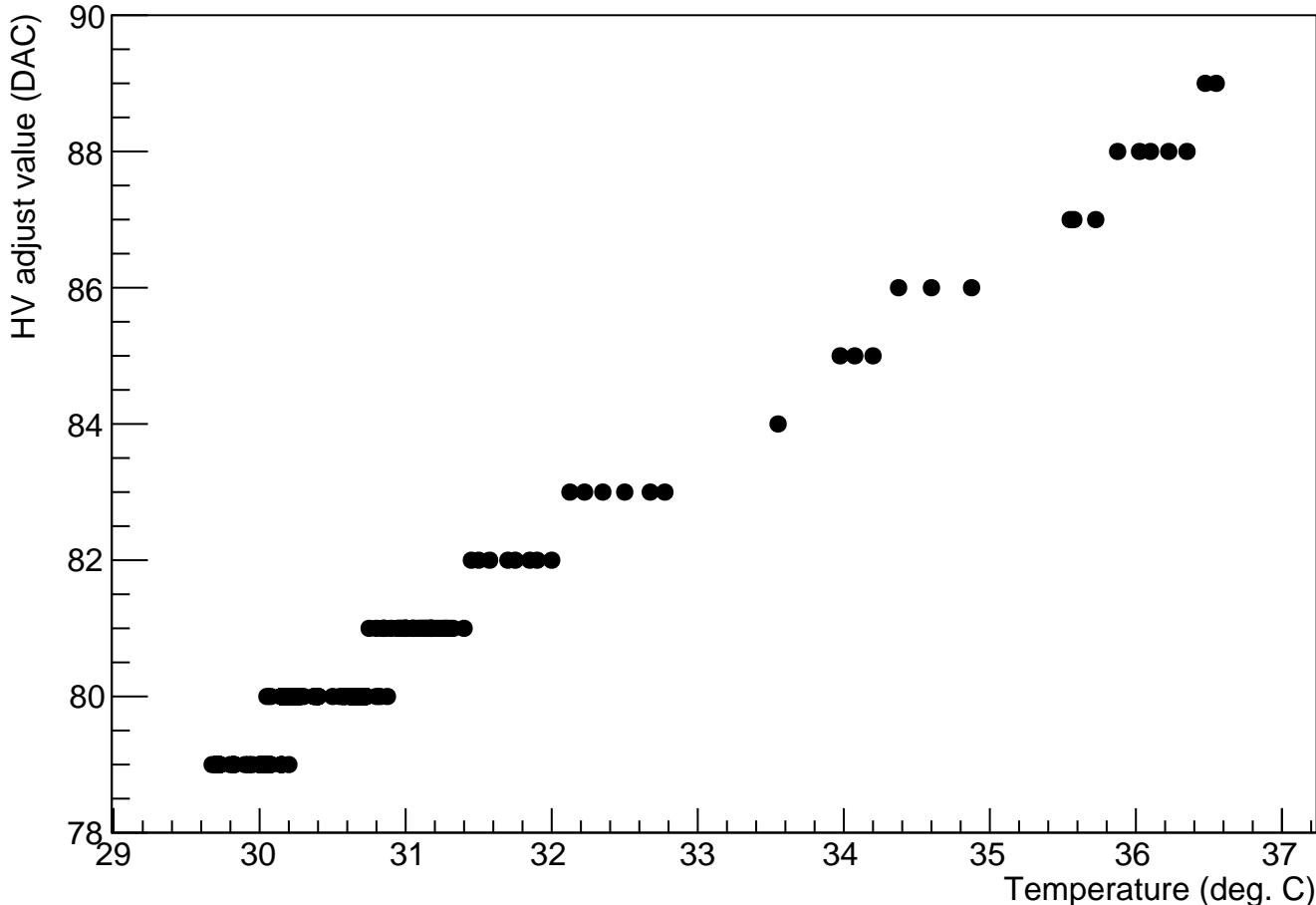
# gra\_hv\_temp\_lda0\_port16\_module20



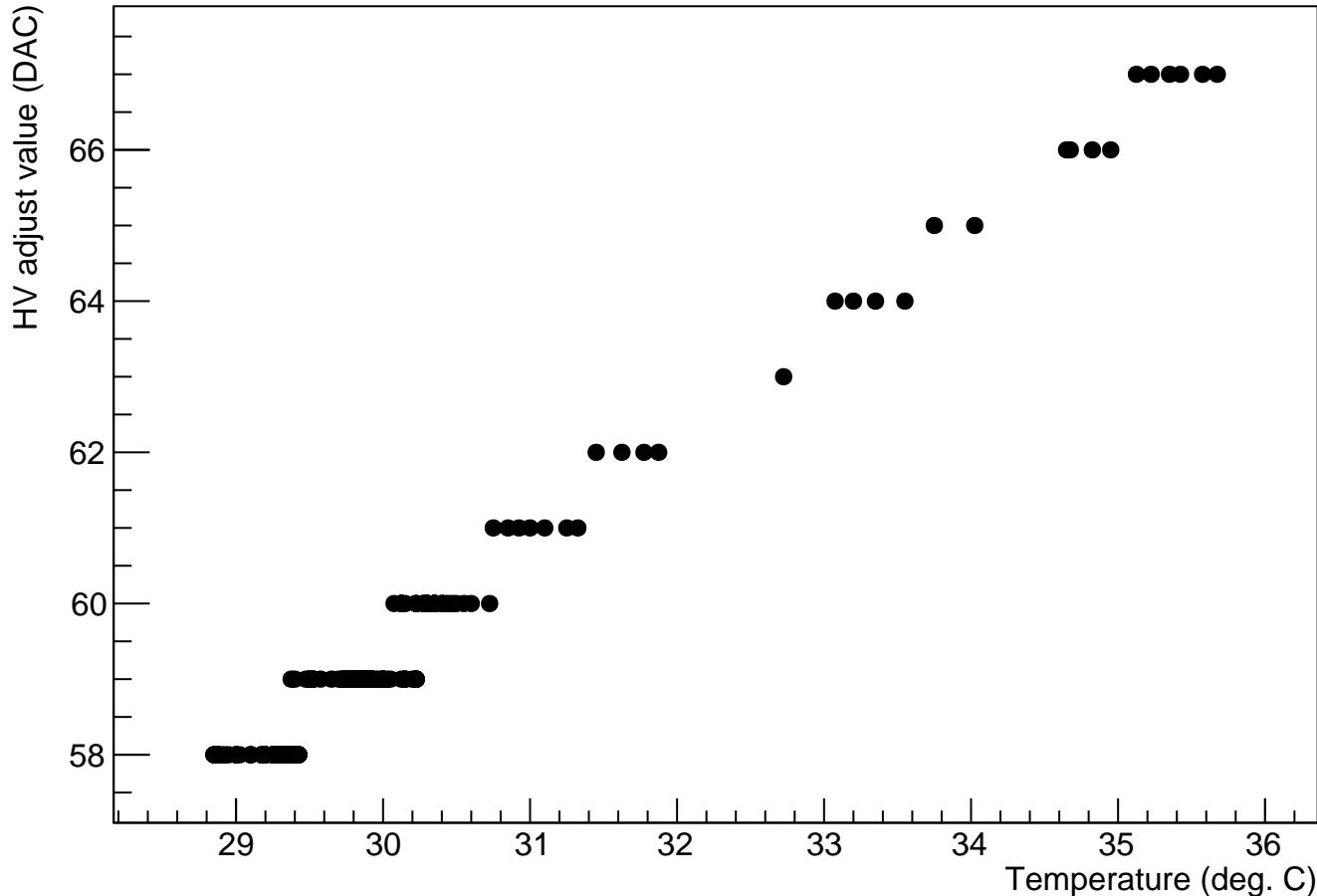
# gra\_hv\_temp\_lda0\_port17\_module26



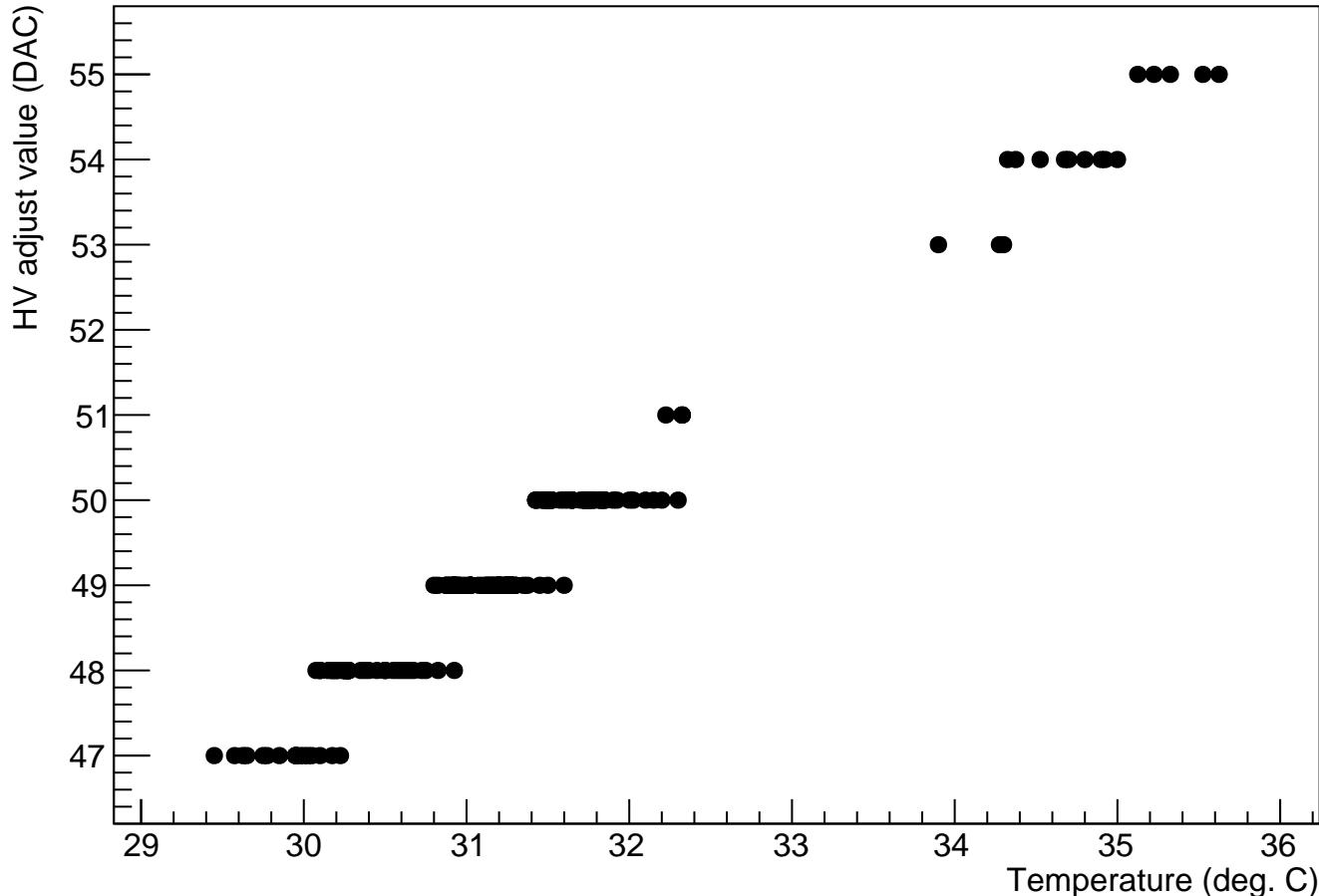
# gra\_hv\_temp\_lda0\_port18\_module32



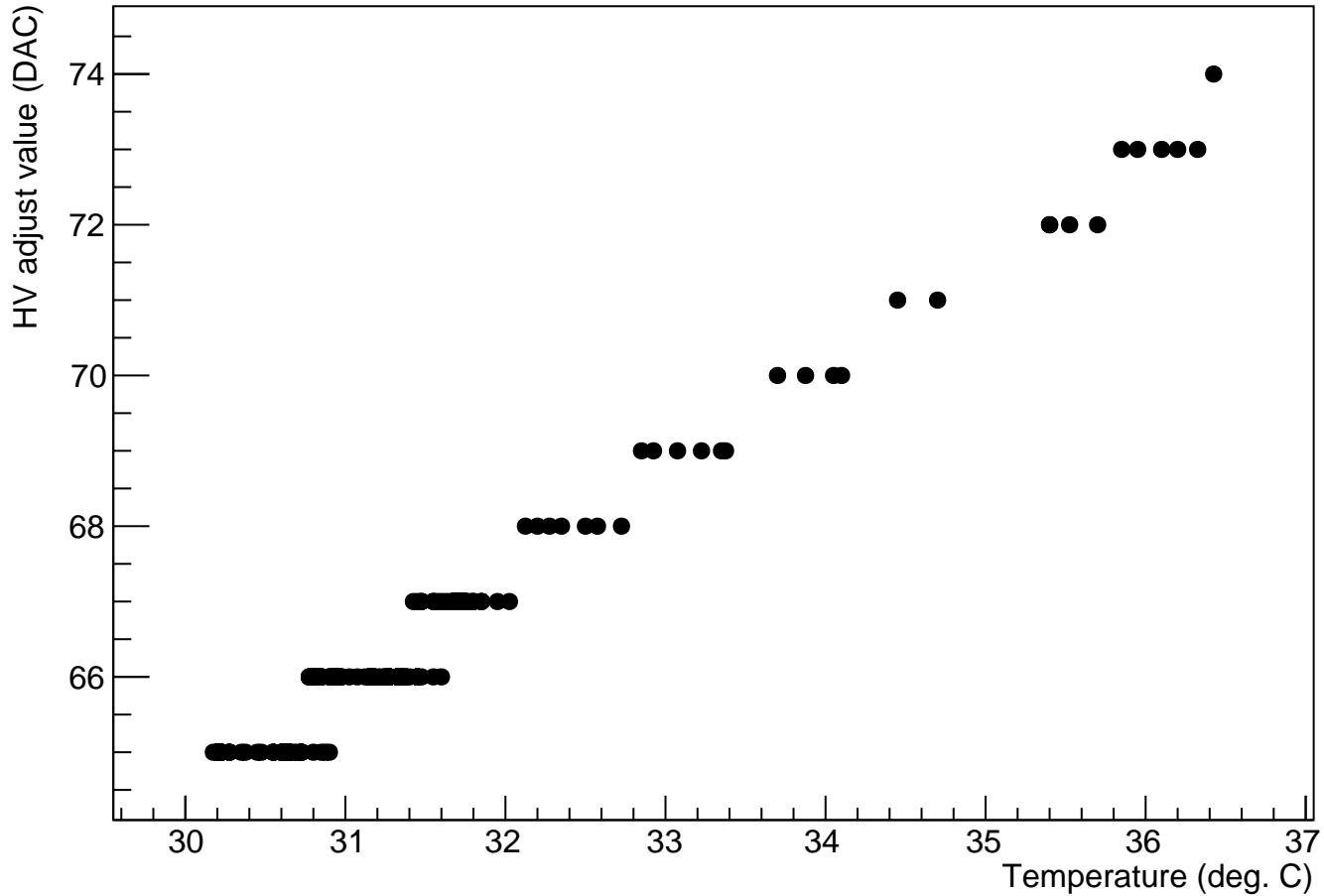
# gra\_hv\_temp\_lda0\_port19\_module40



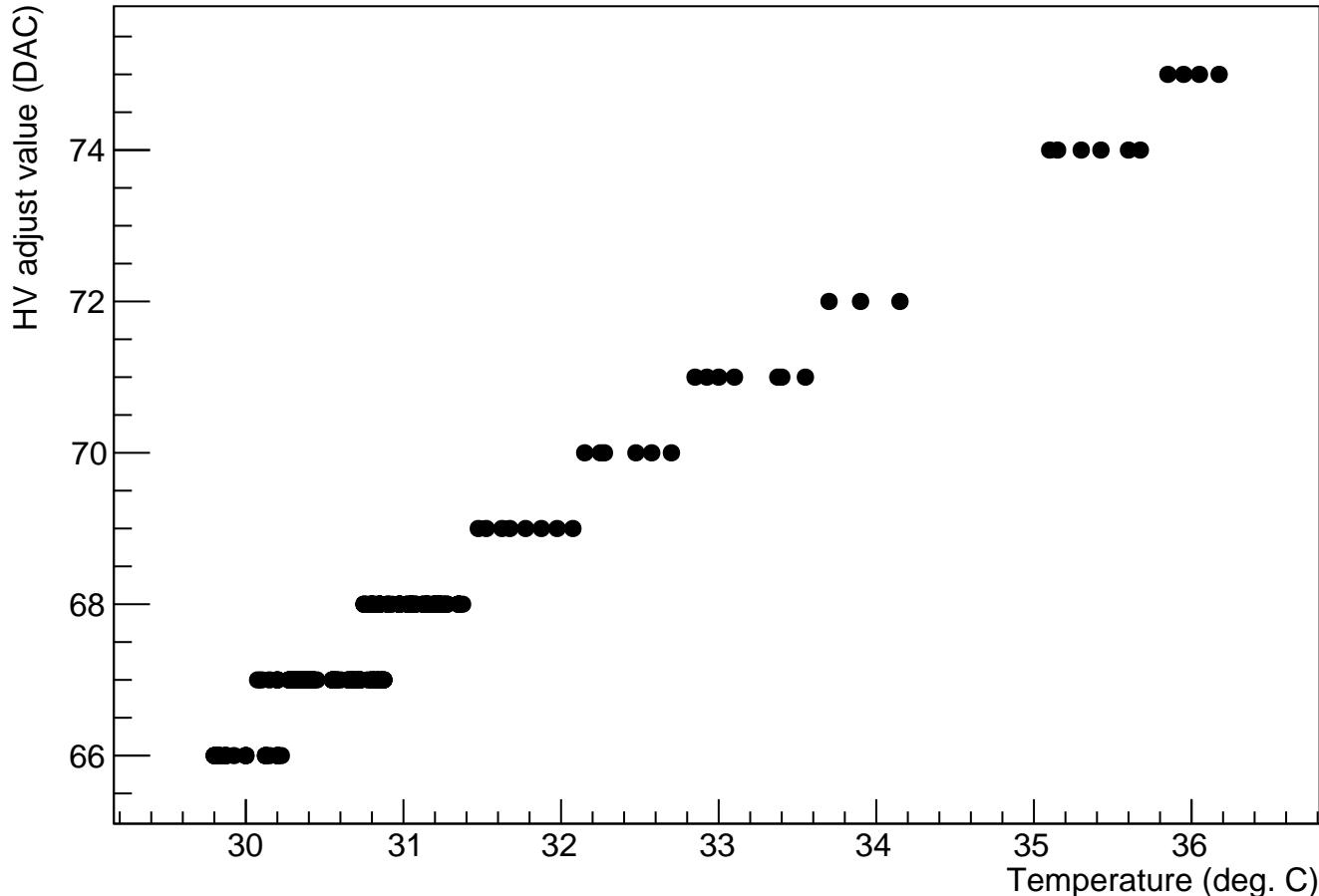
# gra\_hv\_temp\_lda0\_port20\_module36



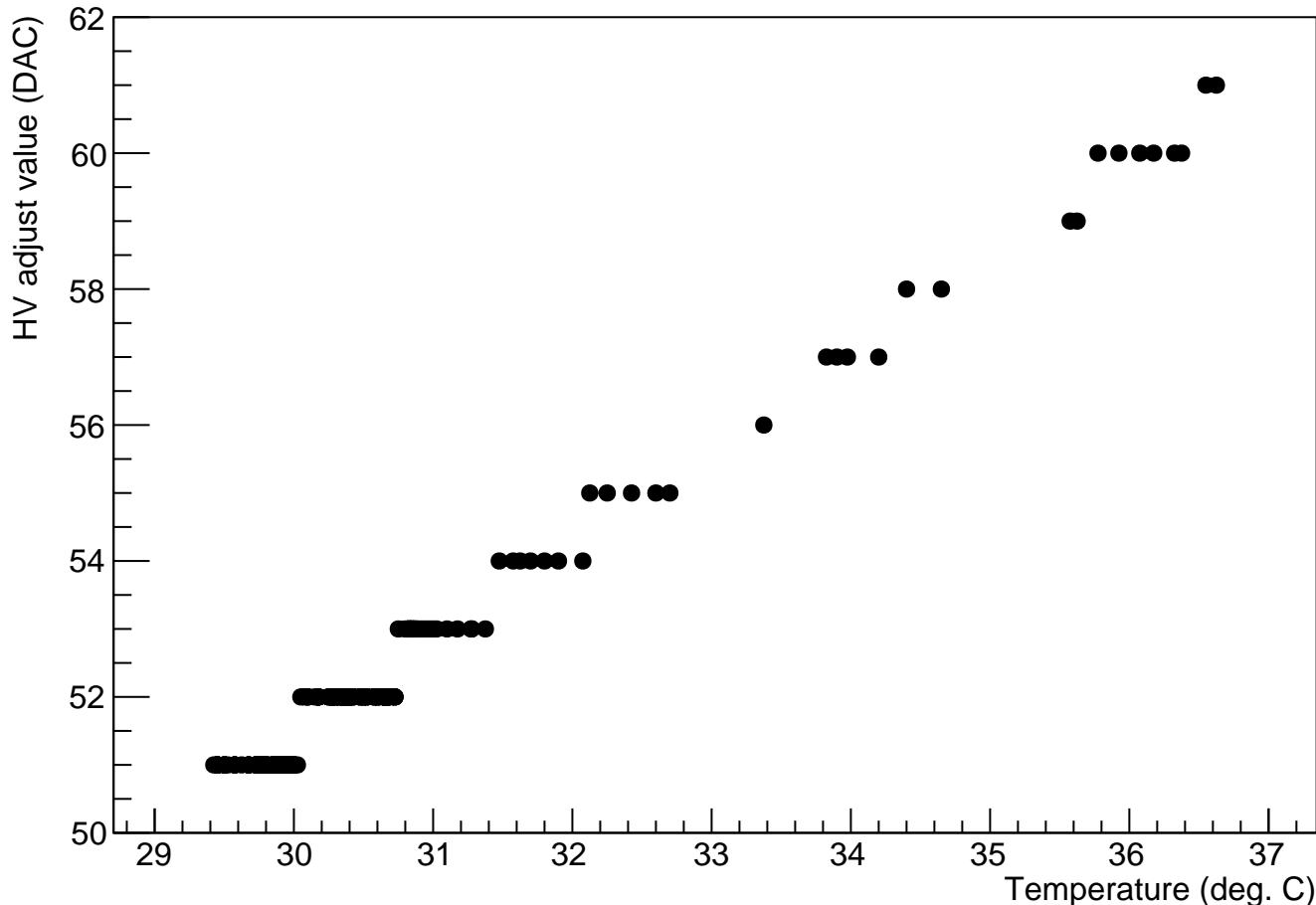
# gra\_hv\_temp\_lda0\_port24\_module5



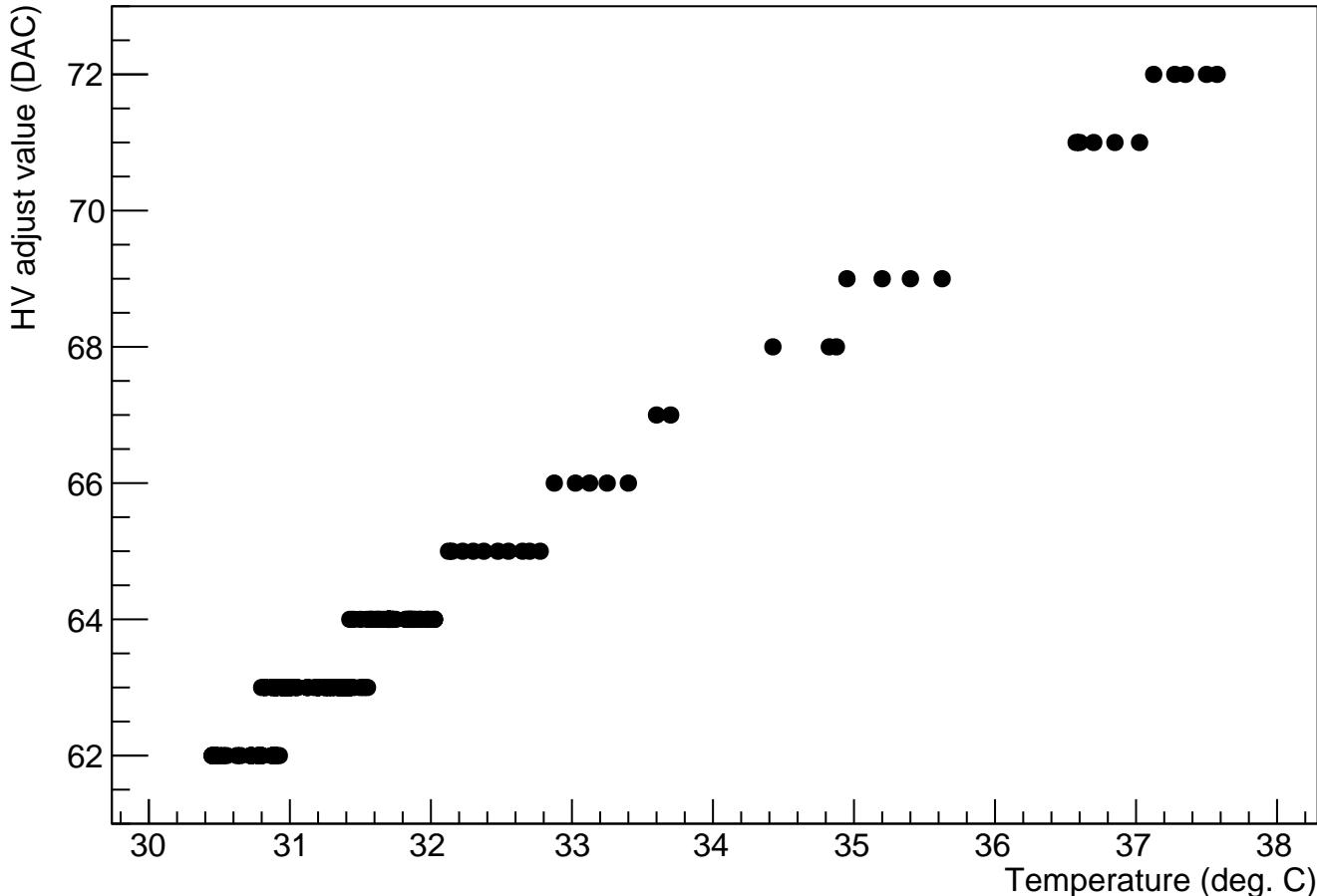
# gra\_hv\_temp\_lda0\_port25\_module13



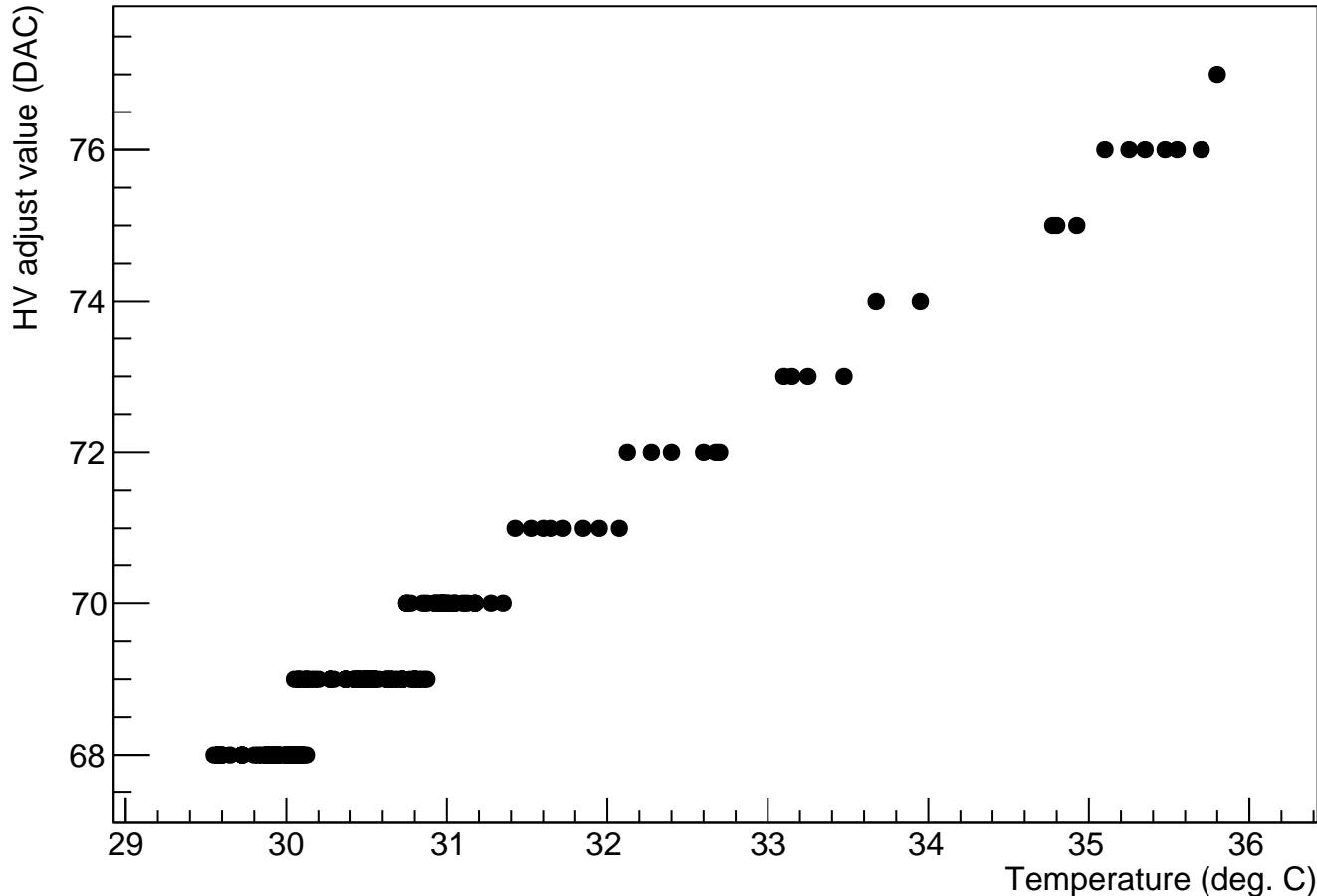
# gra\_hv\_temp\_lda0\_port26\_module28



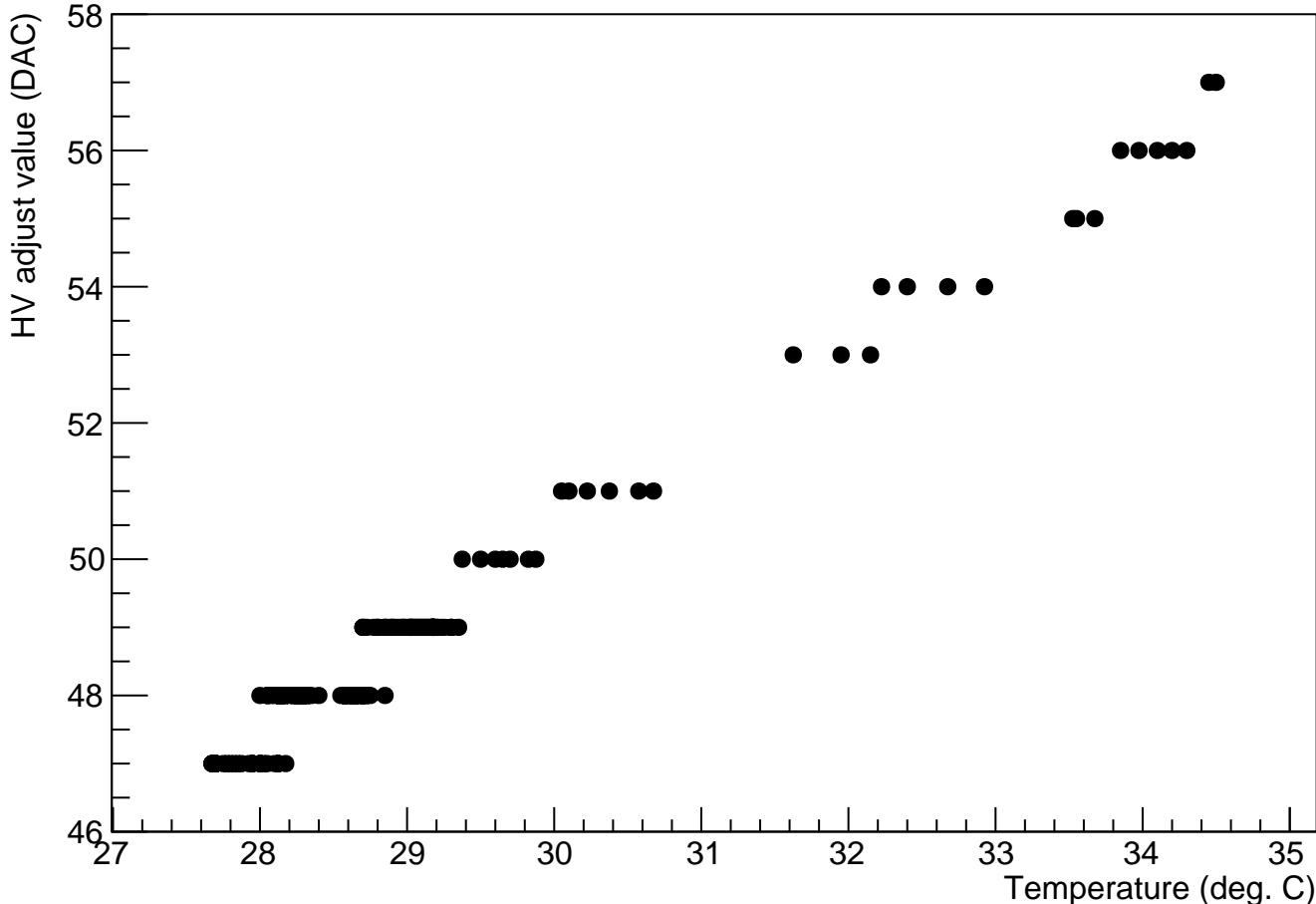
# gra\_hv\_temp\_lda0\_port27\_module39



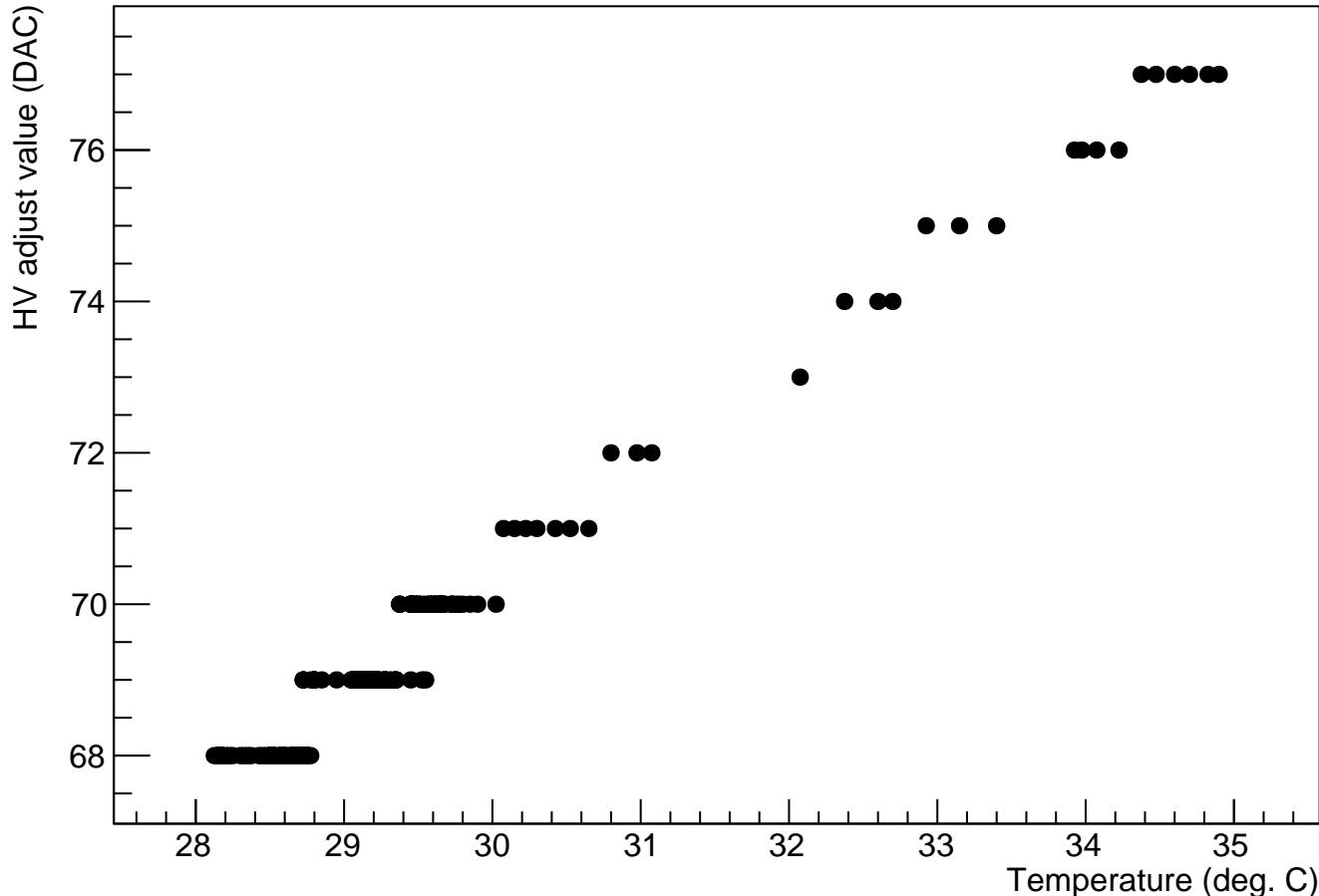
# gra\_hv\_temp\_lda0\_port28\_module1



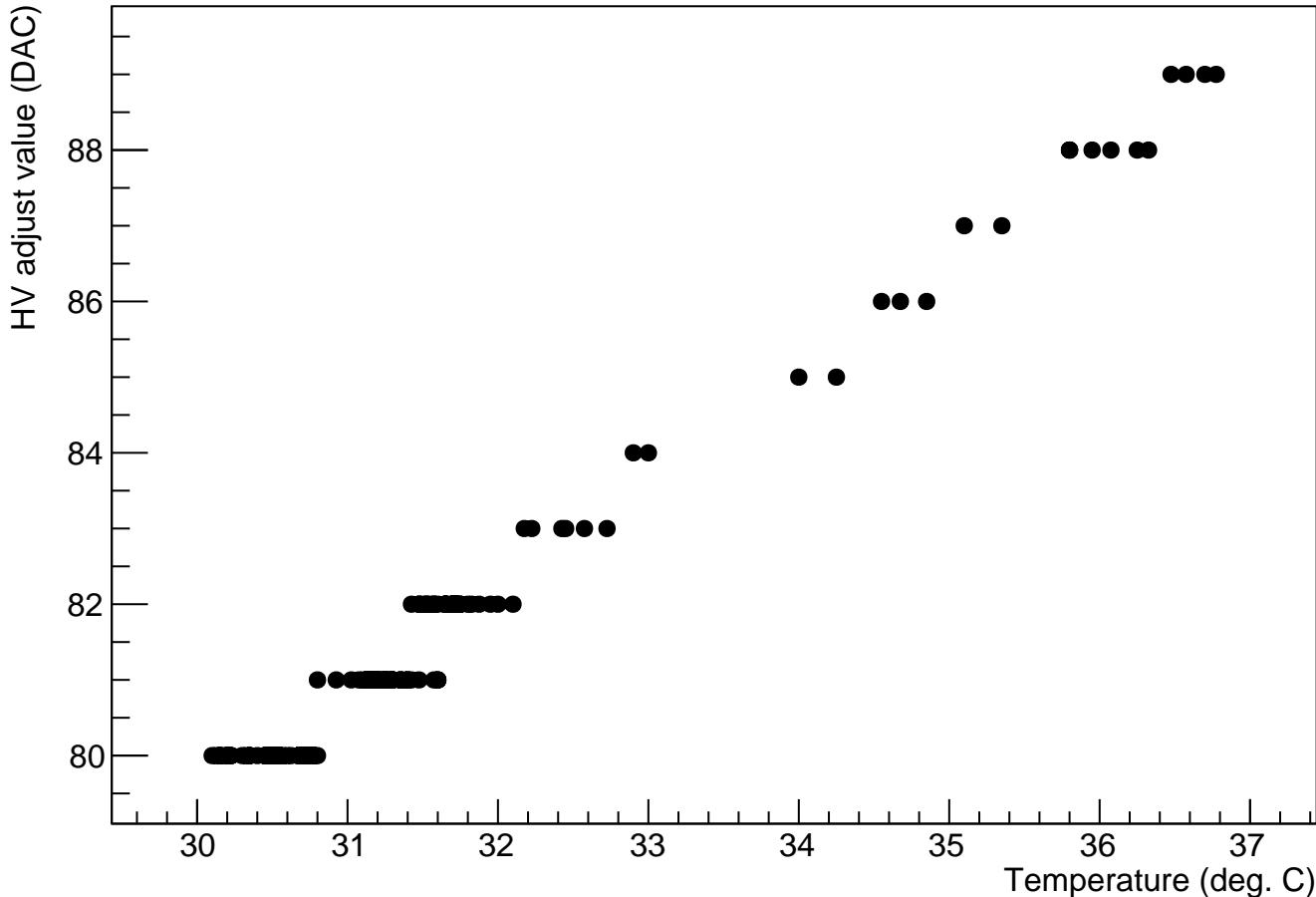
# gra\_hv\_temp\_lda0\_port29\_module27



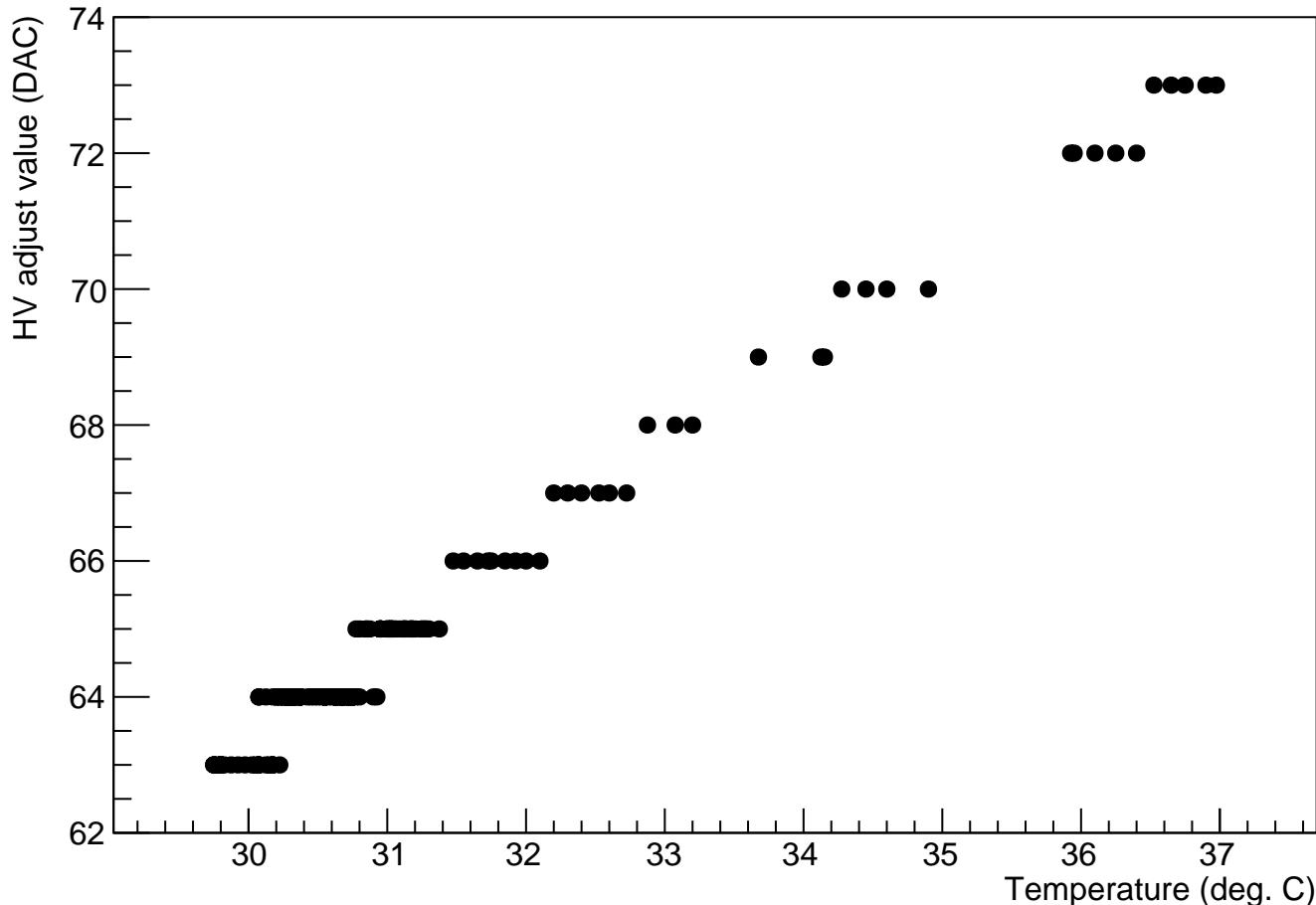
# gra\_hv\_temp\_lda0\_port30\_module31



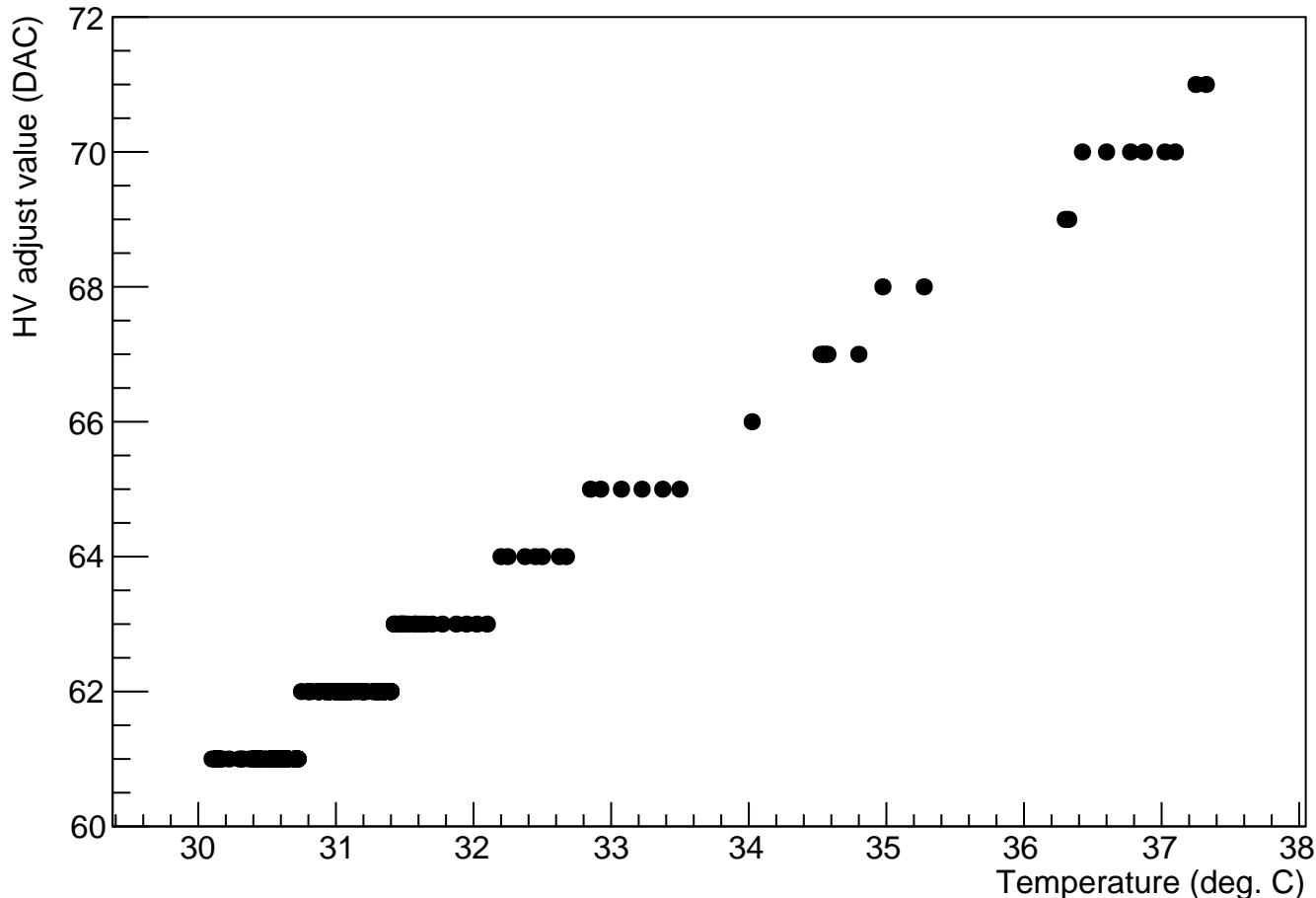
# gra\_hv\_temp\_lda0\_port31\_module38



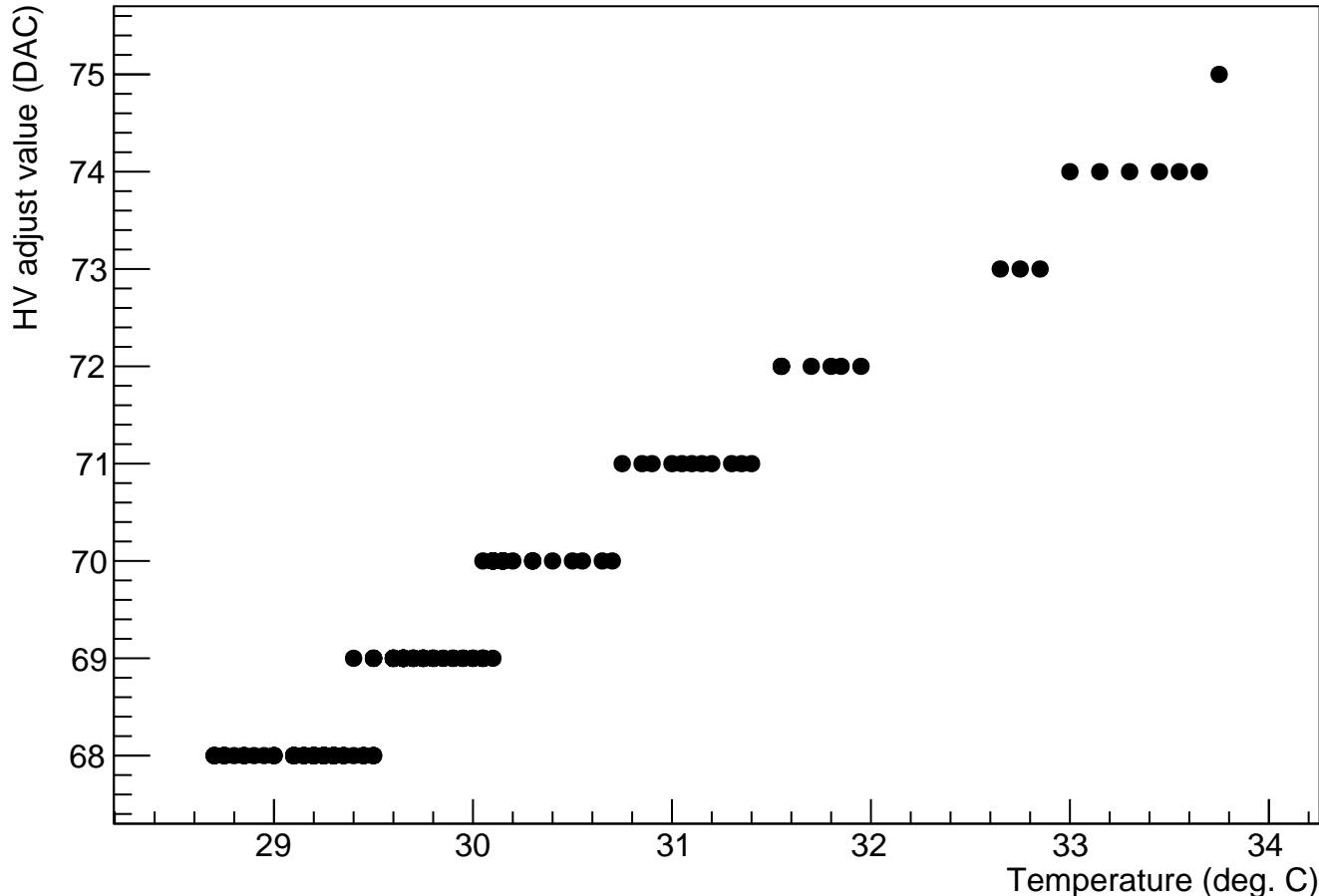
# gra\_hv\_temp\_lda0\_port33\_module25



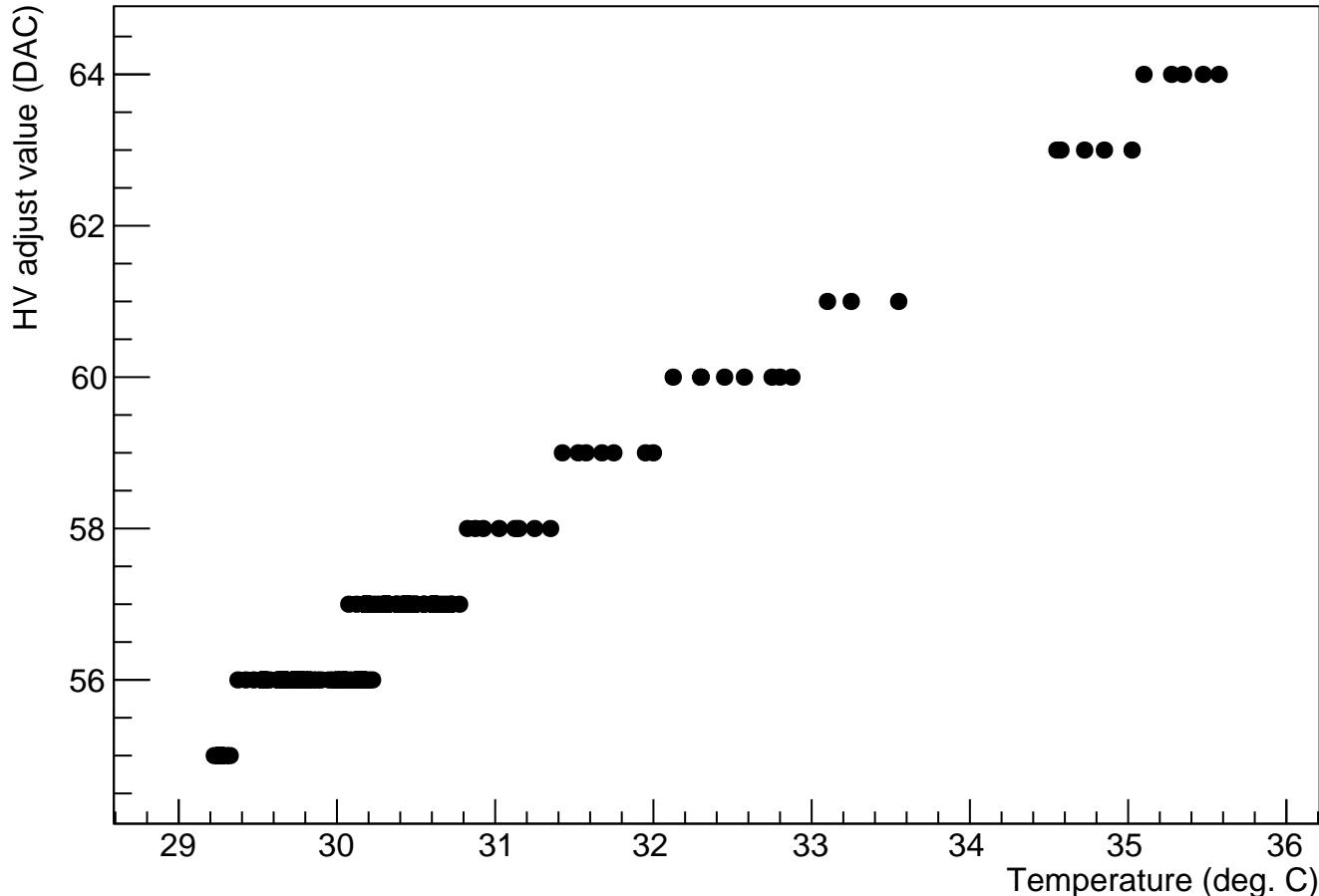
# gra\_hv\_temp\_lda0\_port34\_module30



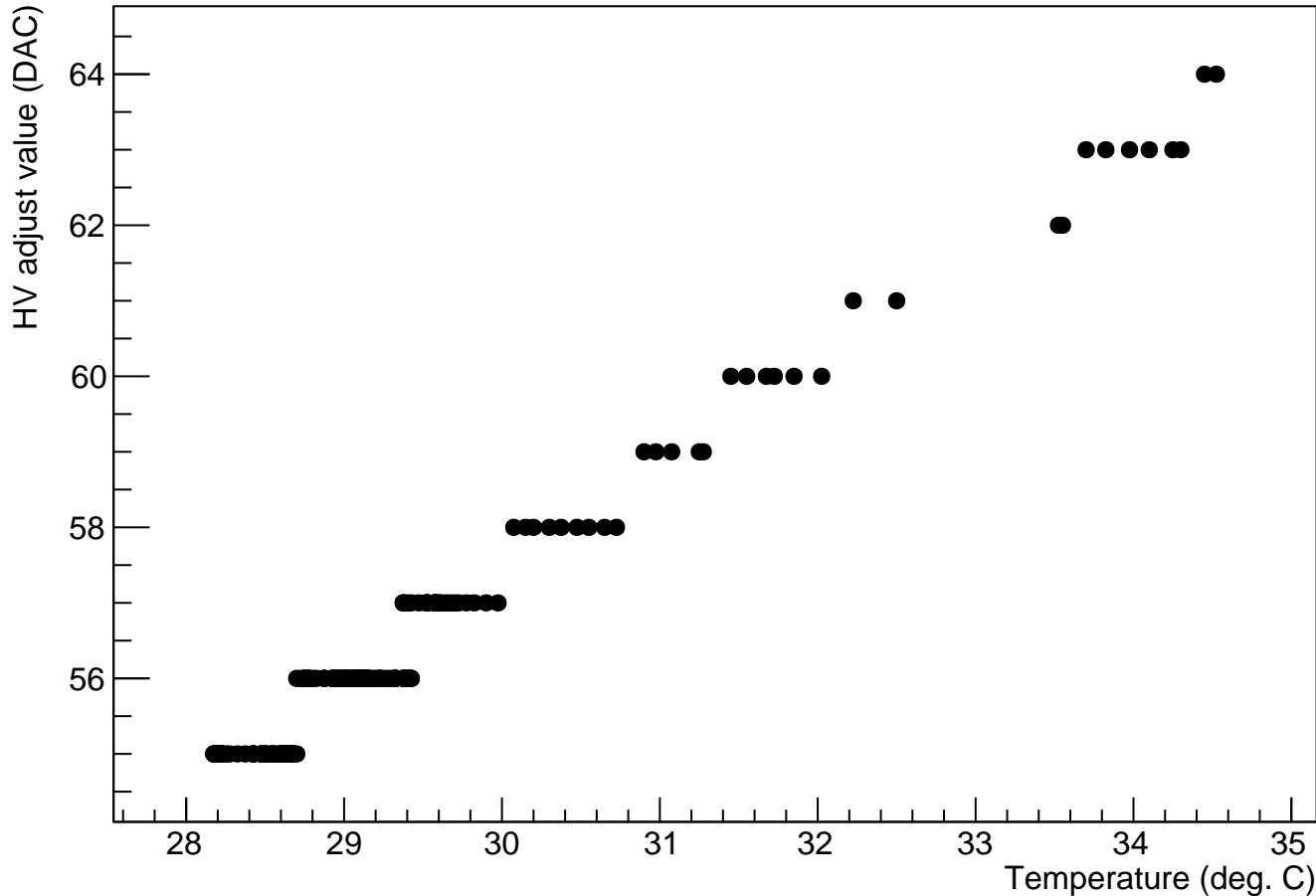
# gra\_hv\_temp\_lda0\_port35\_module12



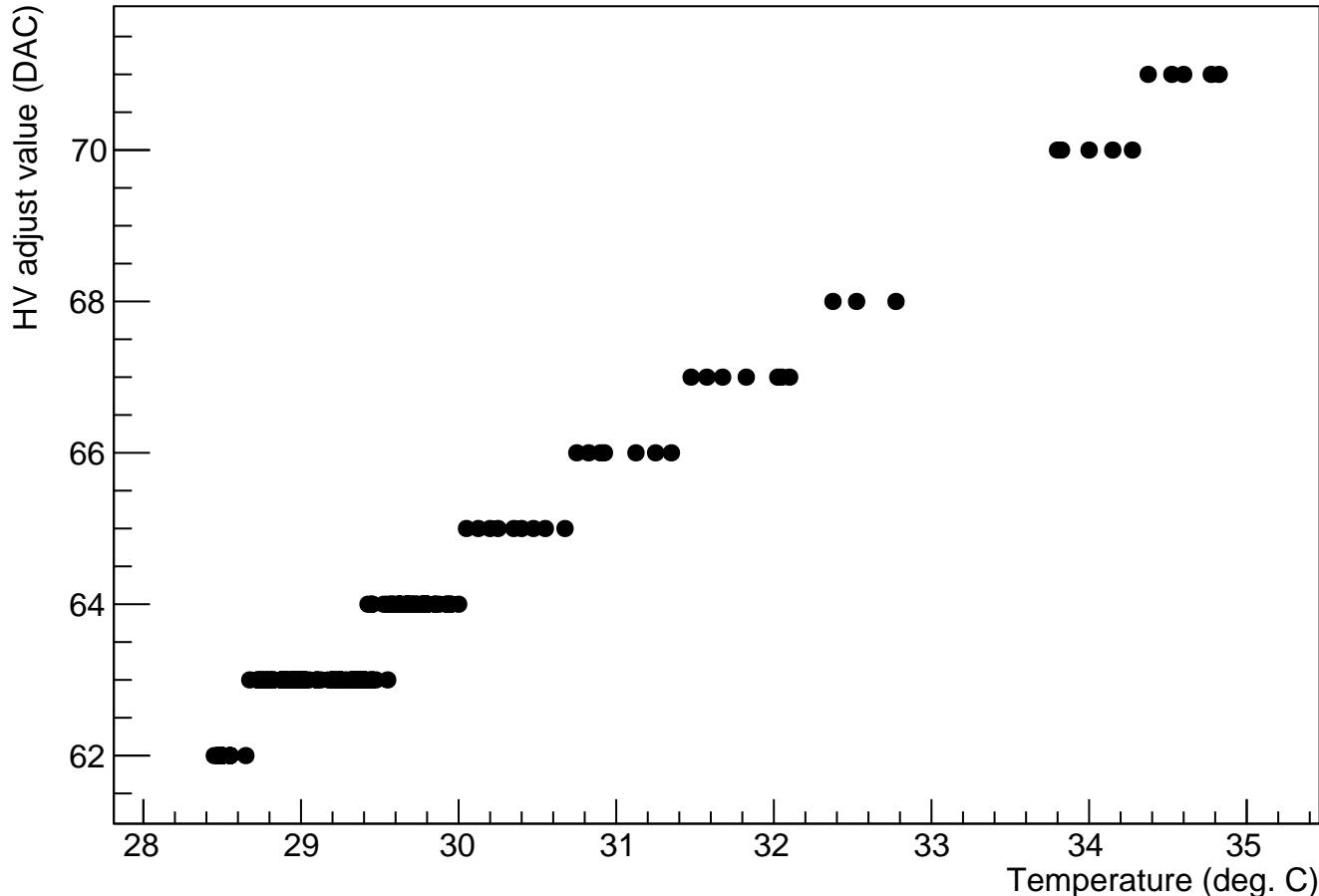
# gra\_hv\_temp\_lda0\_port36\_module15



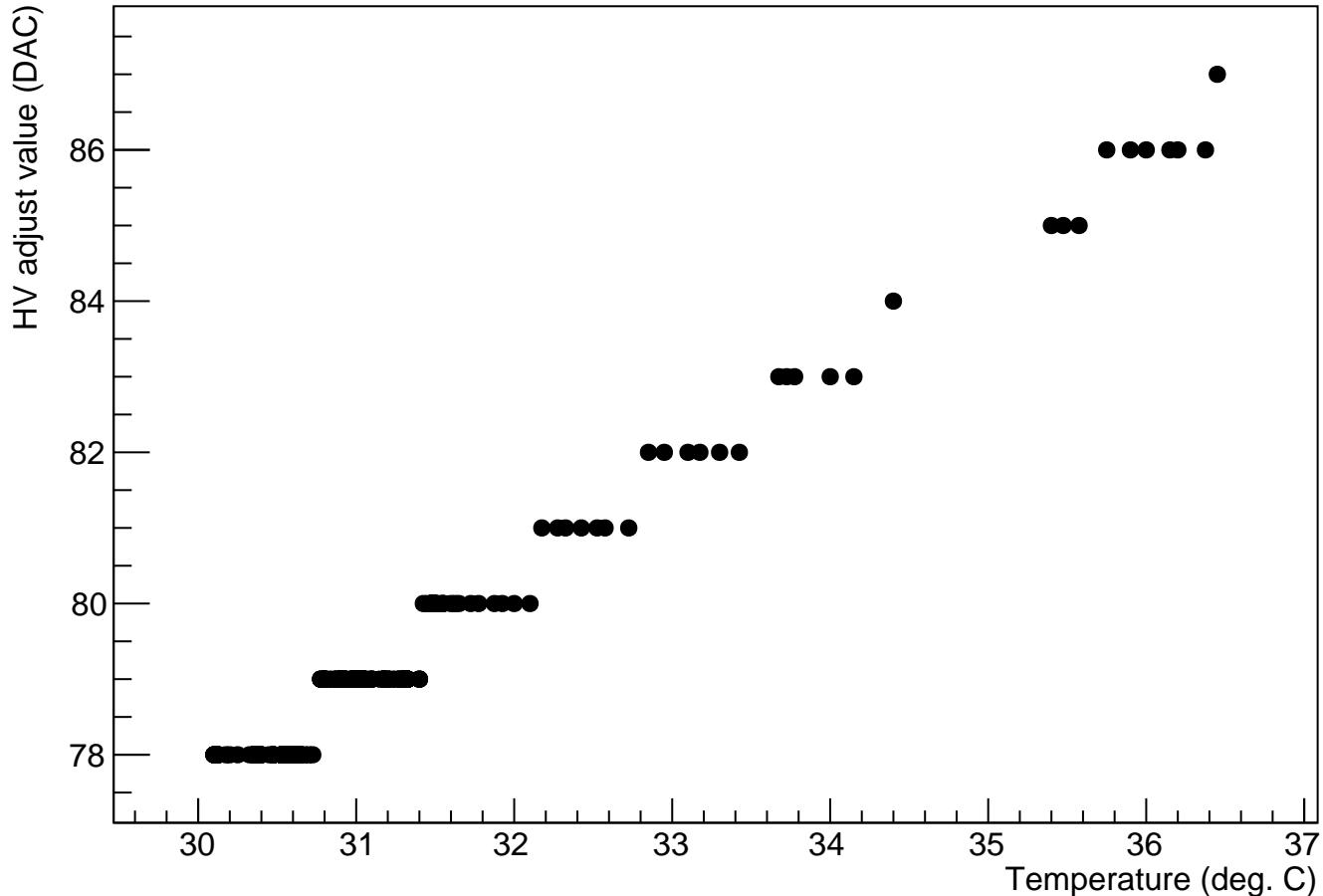
# gra\_hv\_temp\_lda0\_port37\_module16



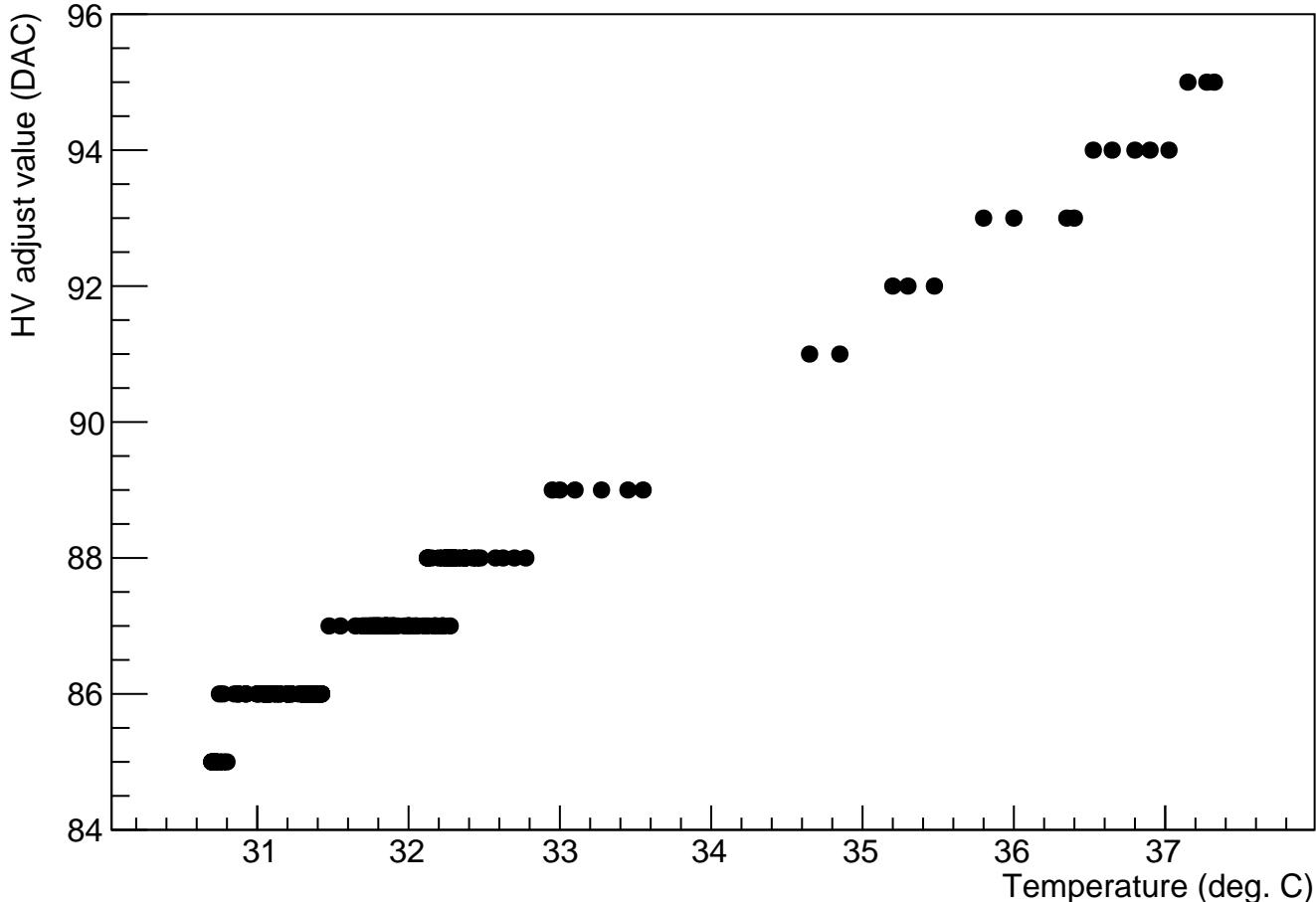
# gra\_hv\_temp\_lda0\_port38\_module17



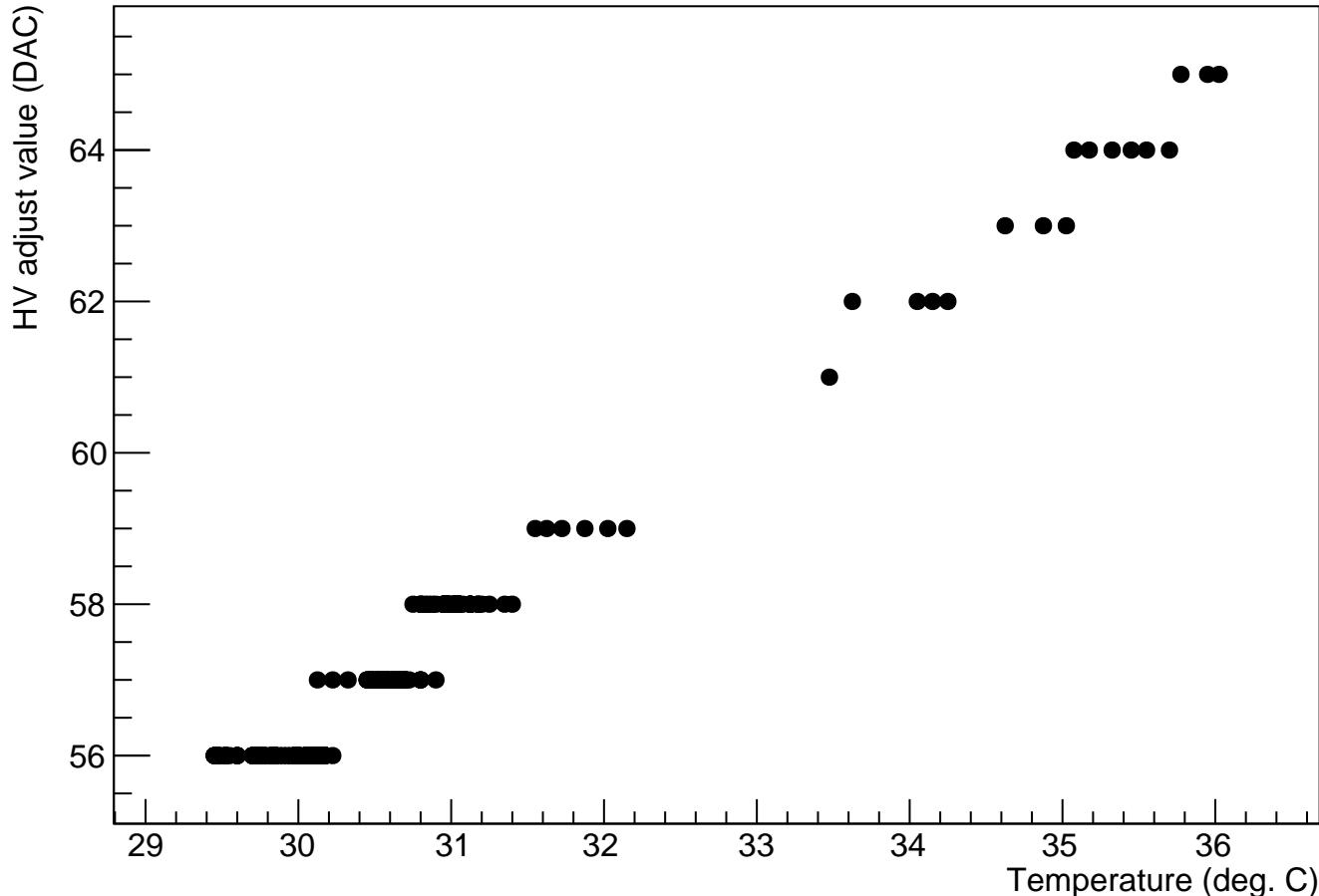
# gra\_hv\_temp\_lda0\_port39\_module18



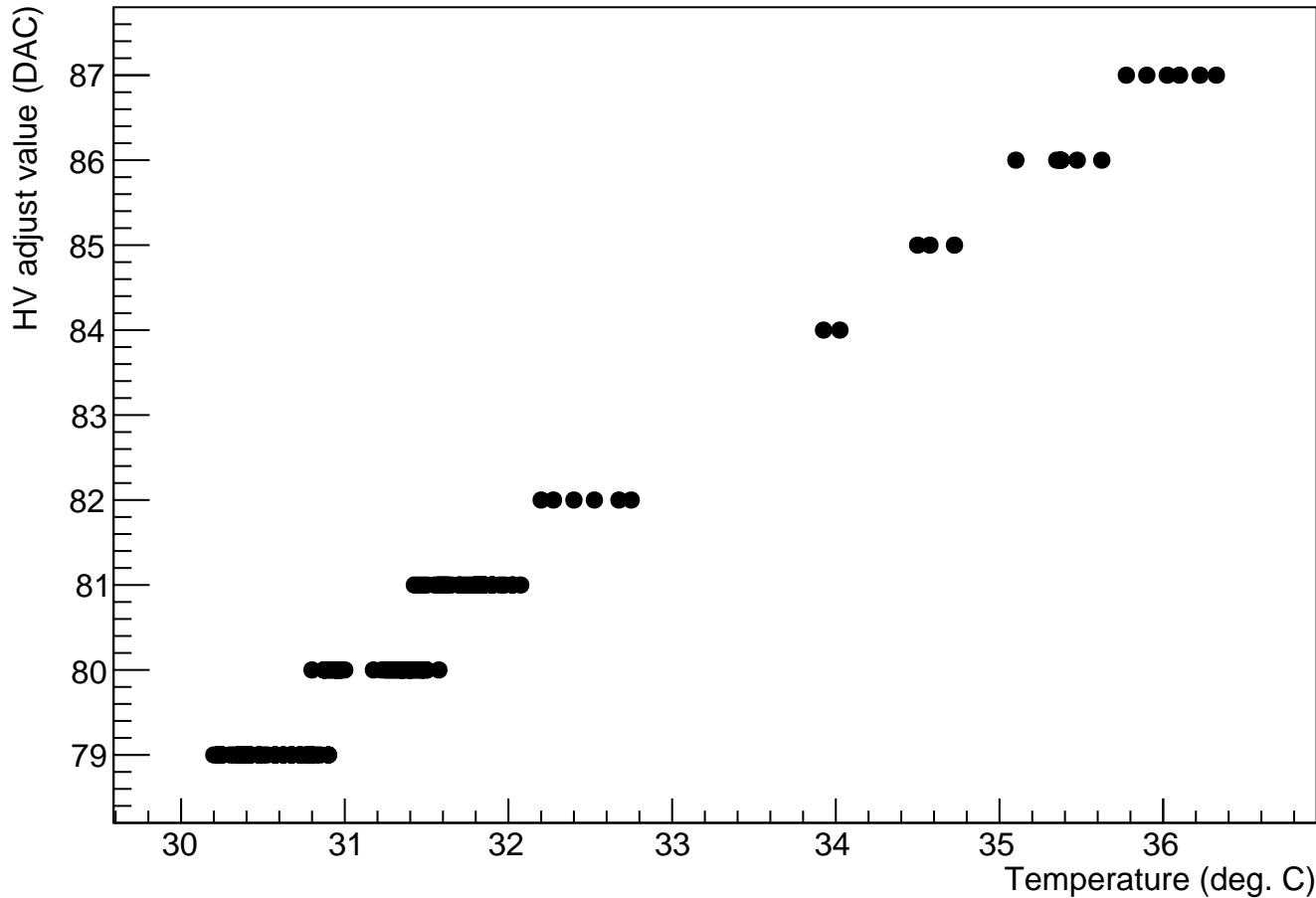
# gra\_hv\_temp\_lda0\_port40\_module37



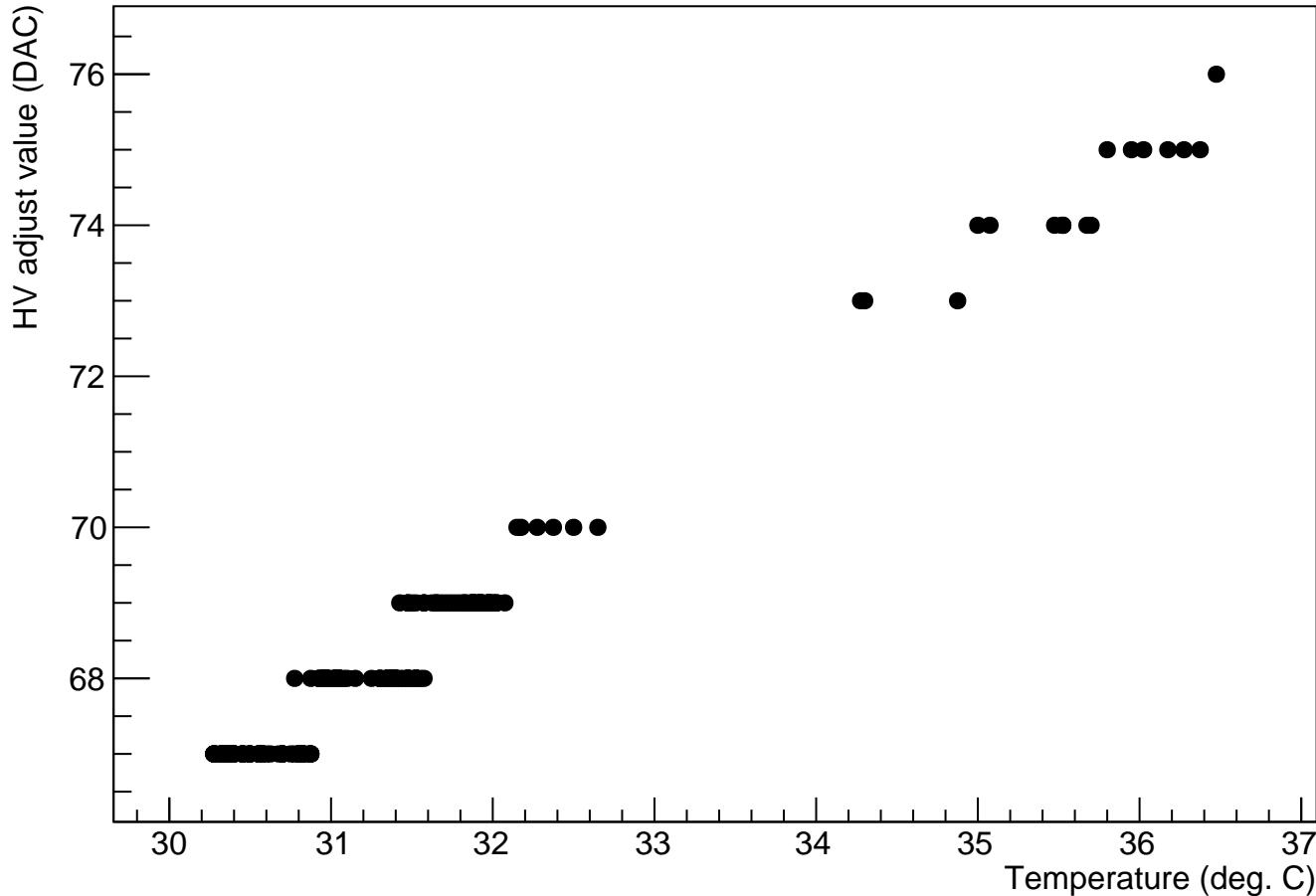
# gra\_hv\_temp\_lda0\_port41\_module29



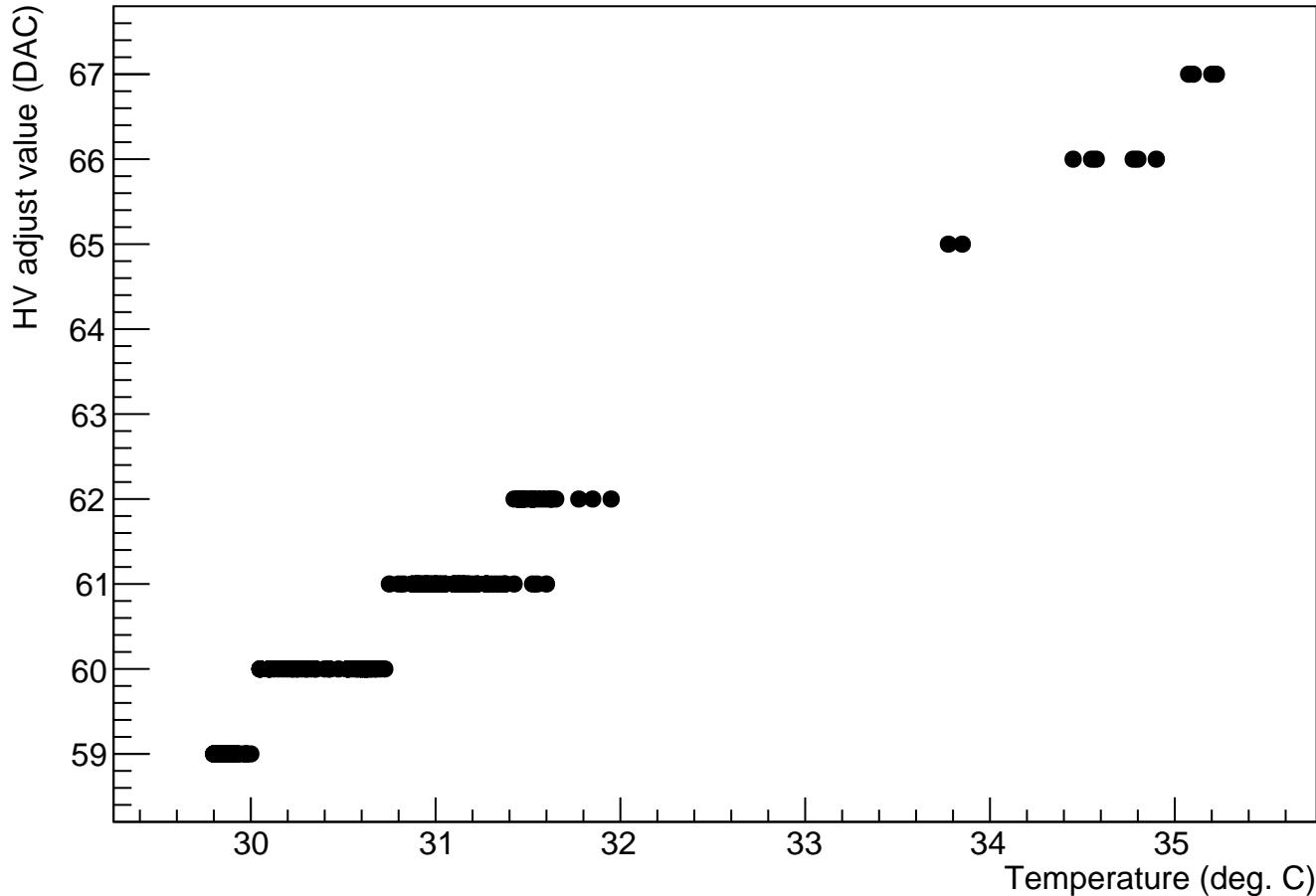
# gra\_hv\_temp\_lda0\_port42\_module33



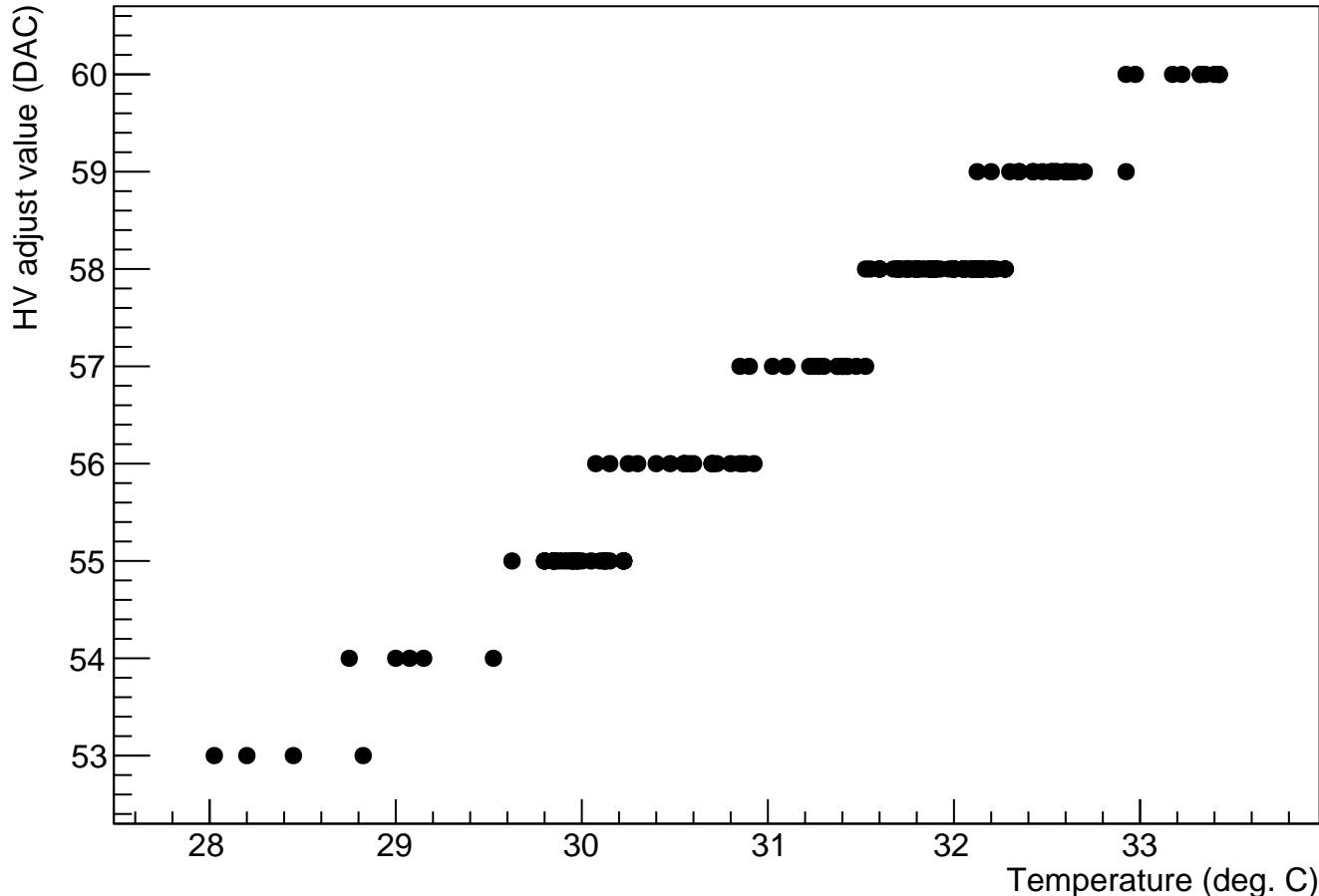
# gra\_hv\_temp\_lda0\_port43\_module34



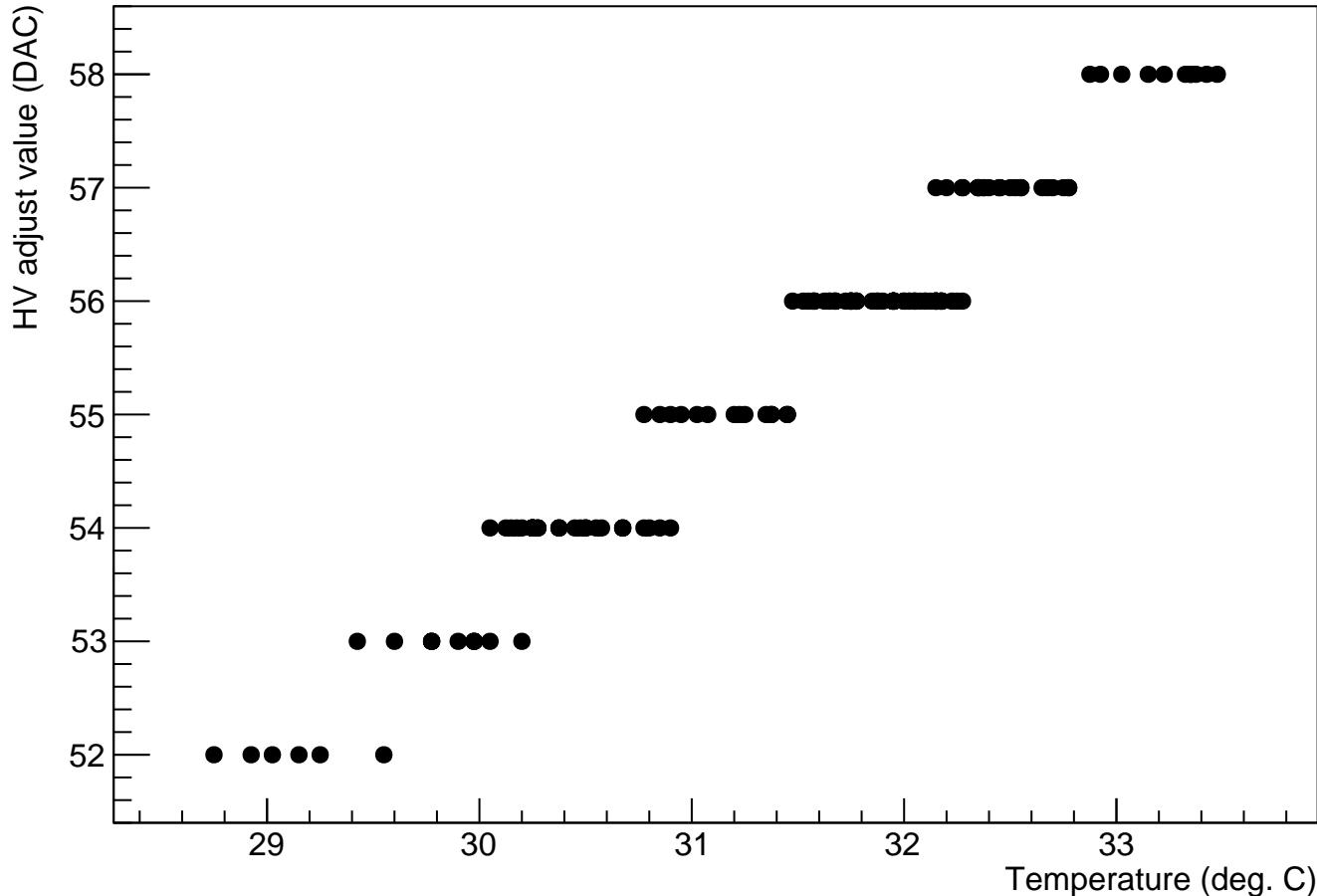
# gra\_hv\_temp\_lda0\_port44\_module41



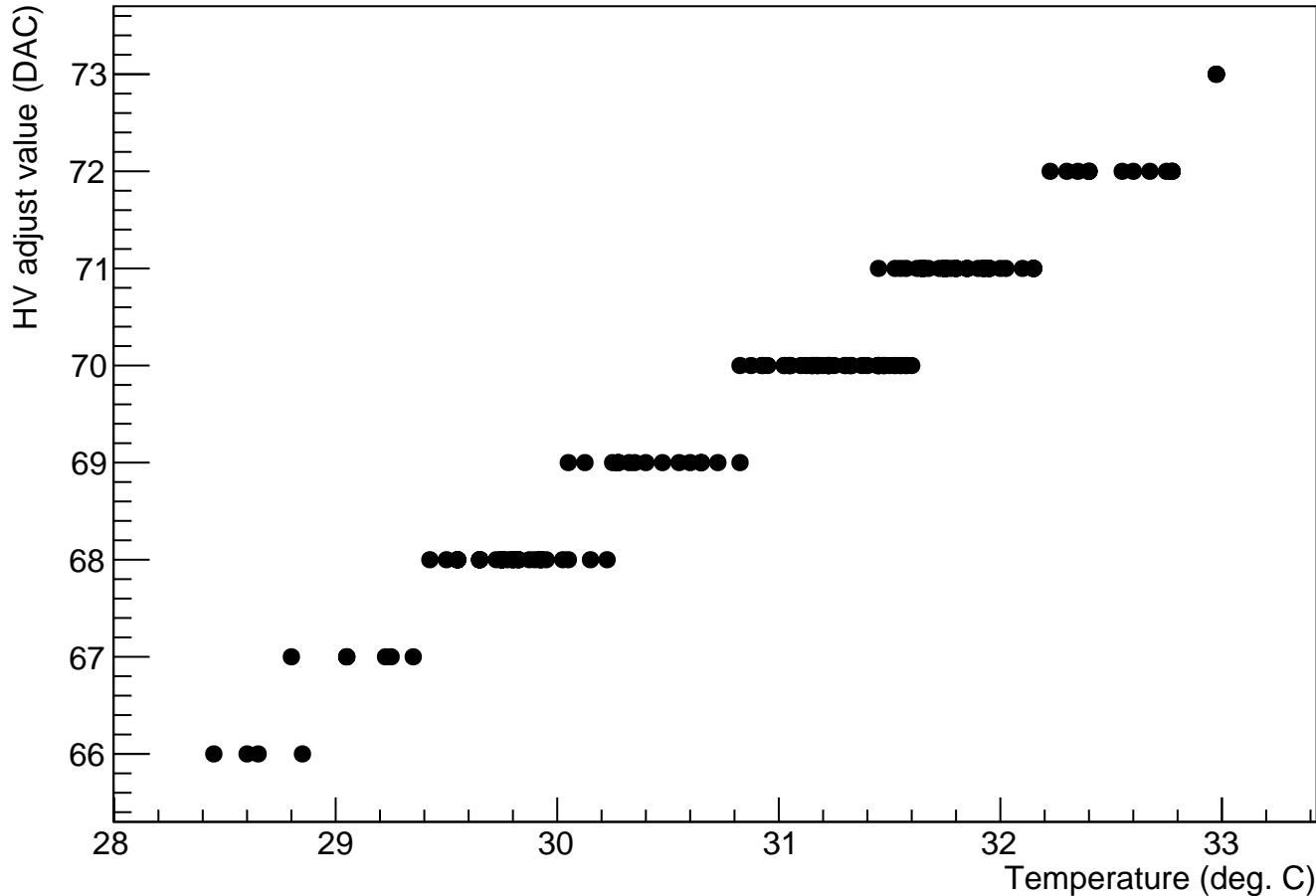
# gra\_hv\_temp\_lda1\_port0\_module43\_SM100



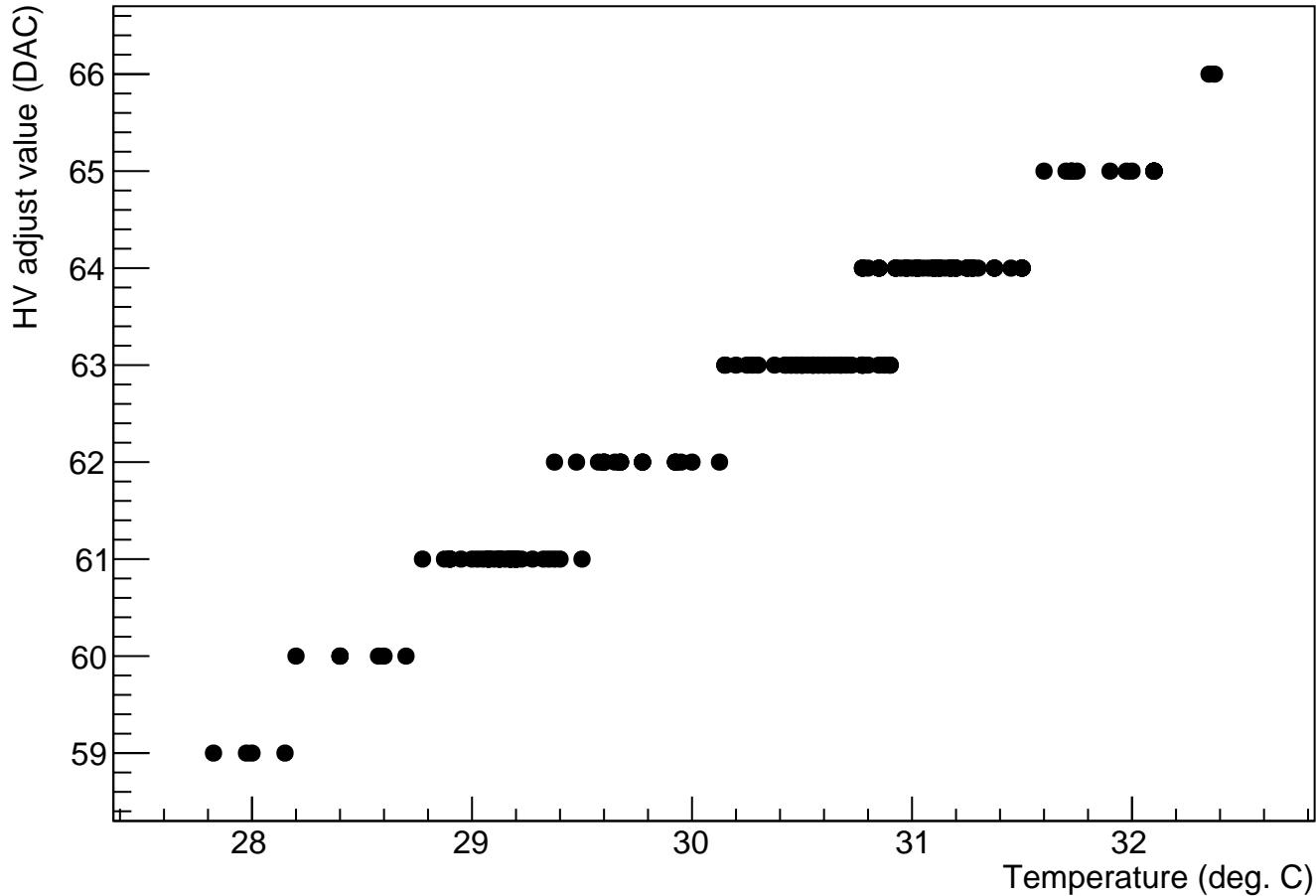
# gra\_hv\_temp\_lda1\_port1\_module44\_SM1



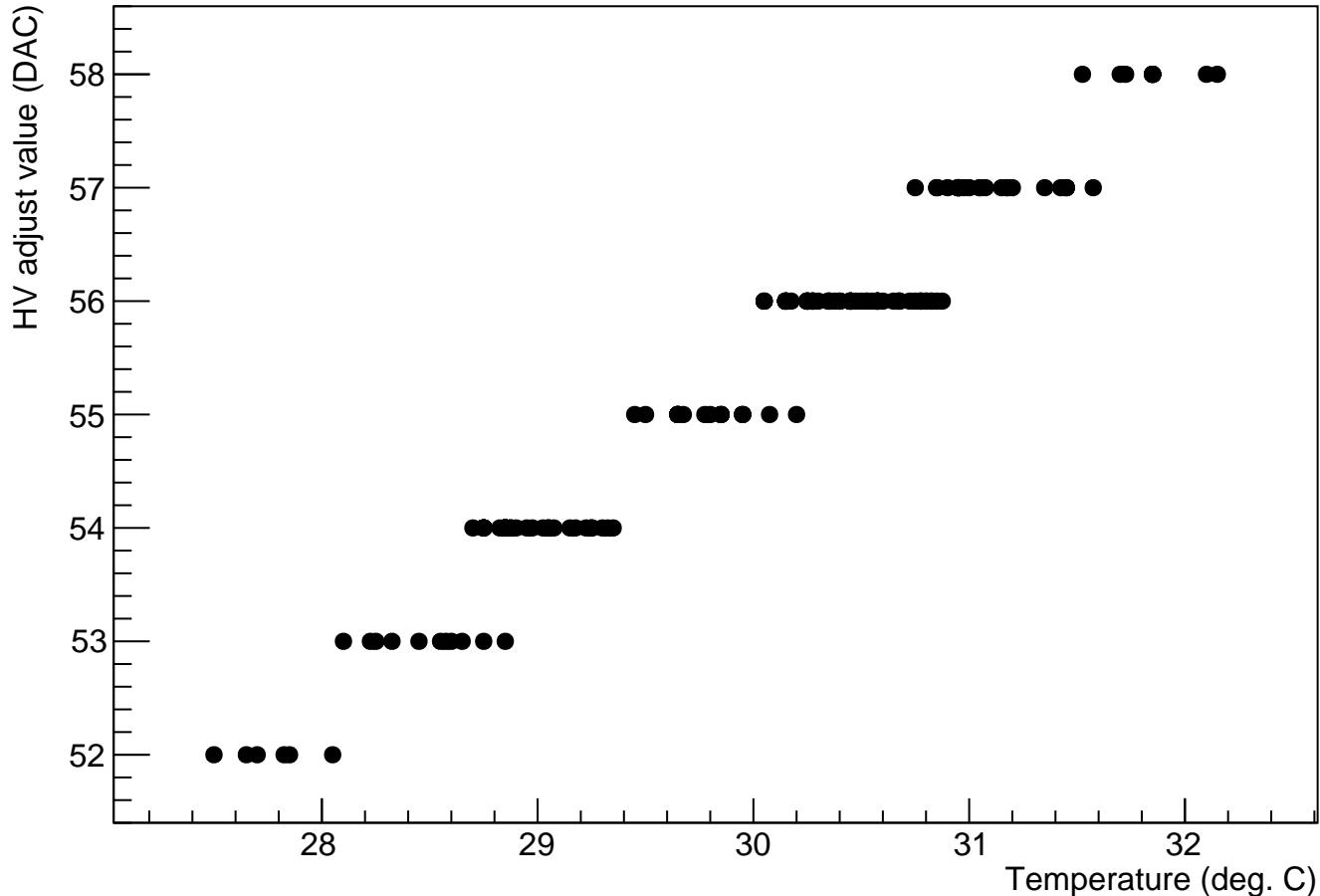
# gra\_hv\_temp\_lda1\_port3\_module46\_SM4



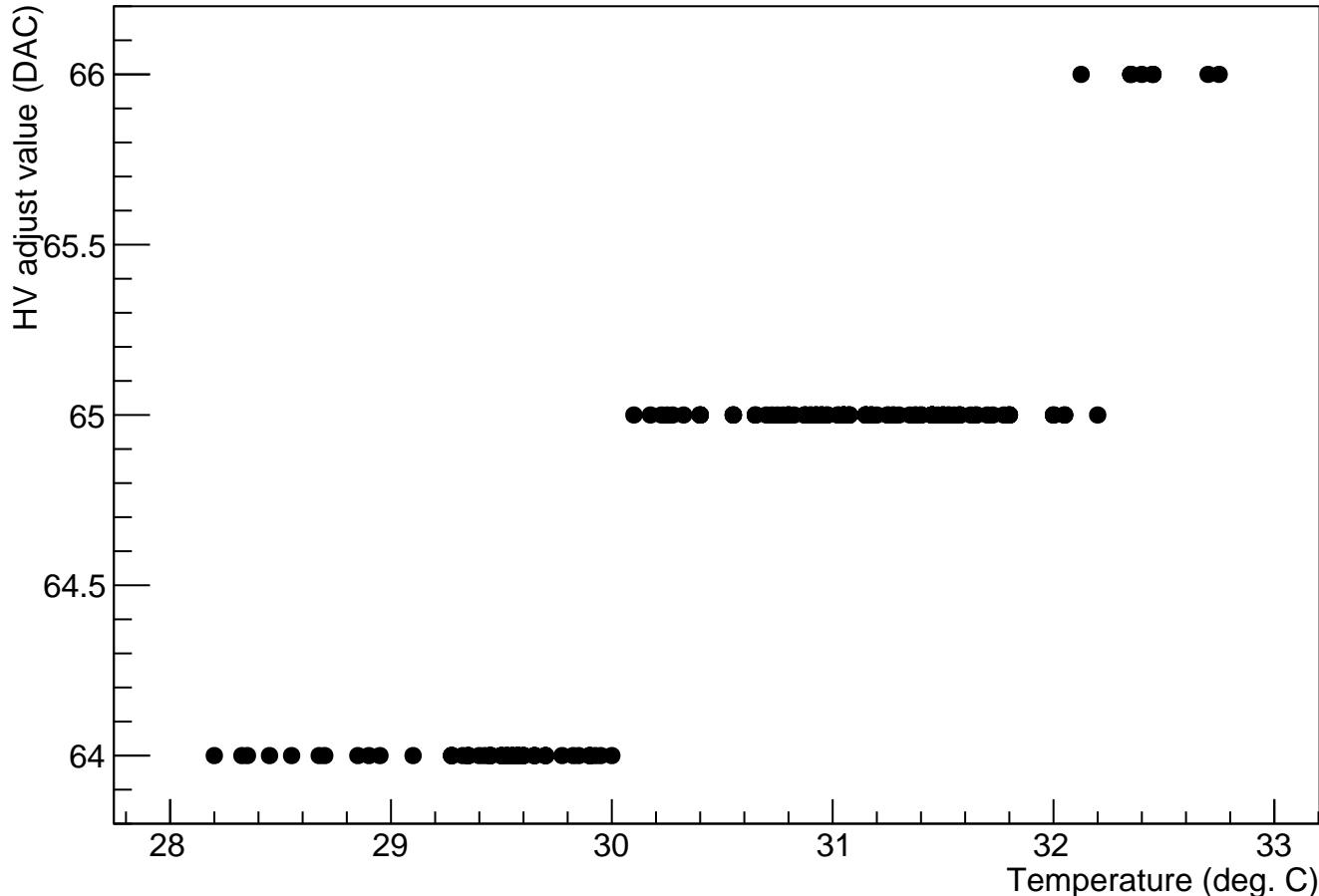
# gra\_hv\_temp\_lda1\_port4\_module47\_SM5



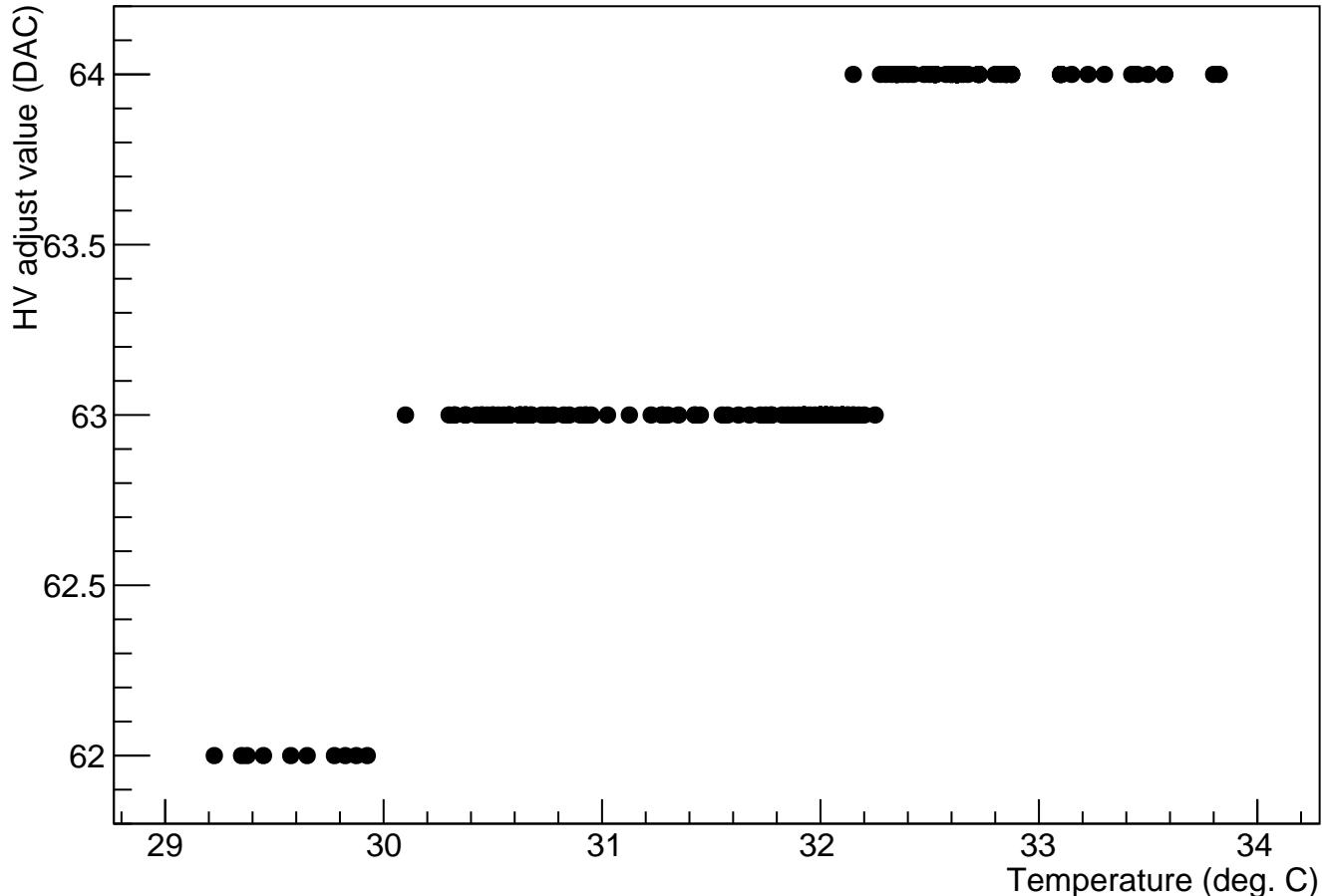
# gra\_hv\_temp\_lda1\_port5\_module48\_SM7



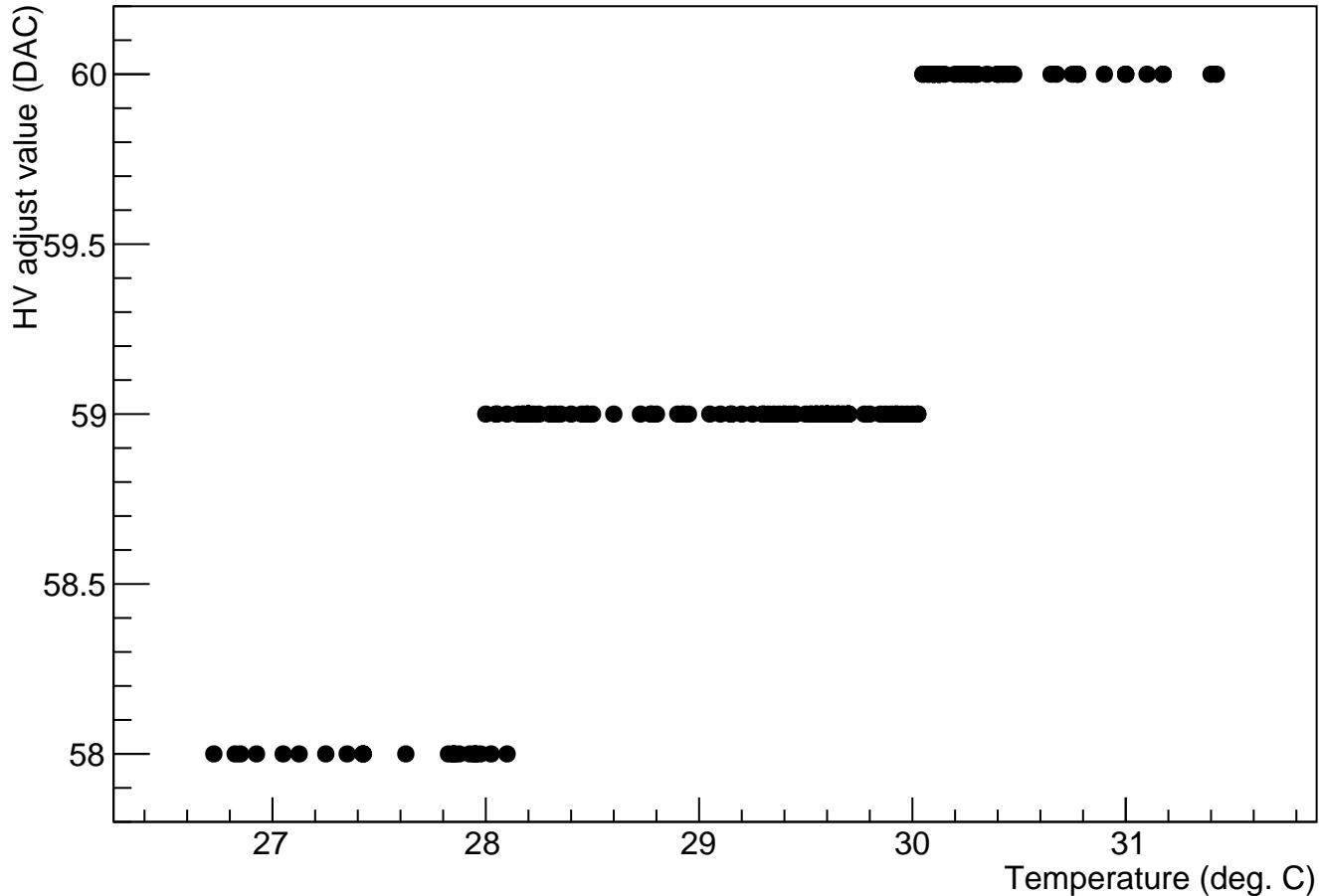
# gra\_hv\_temp\_lda1\_port6\_module49\_HBU3\_1



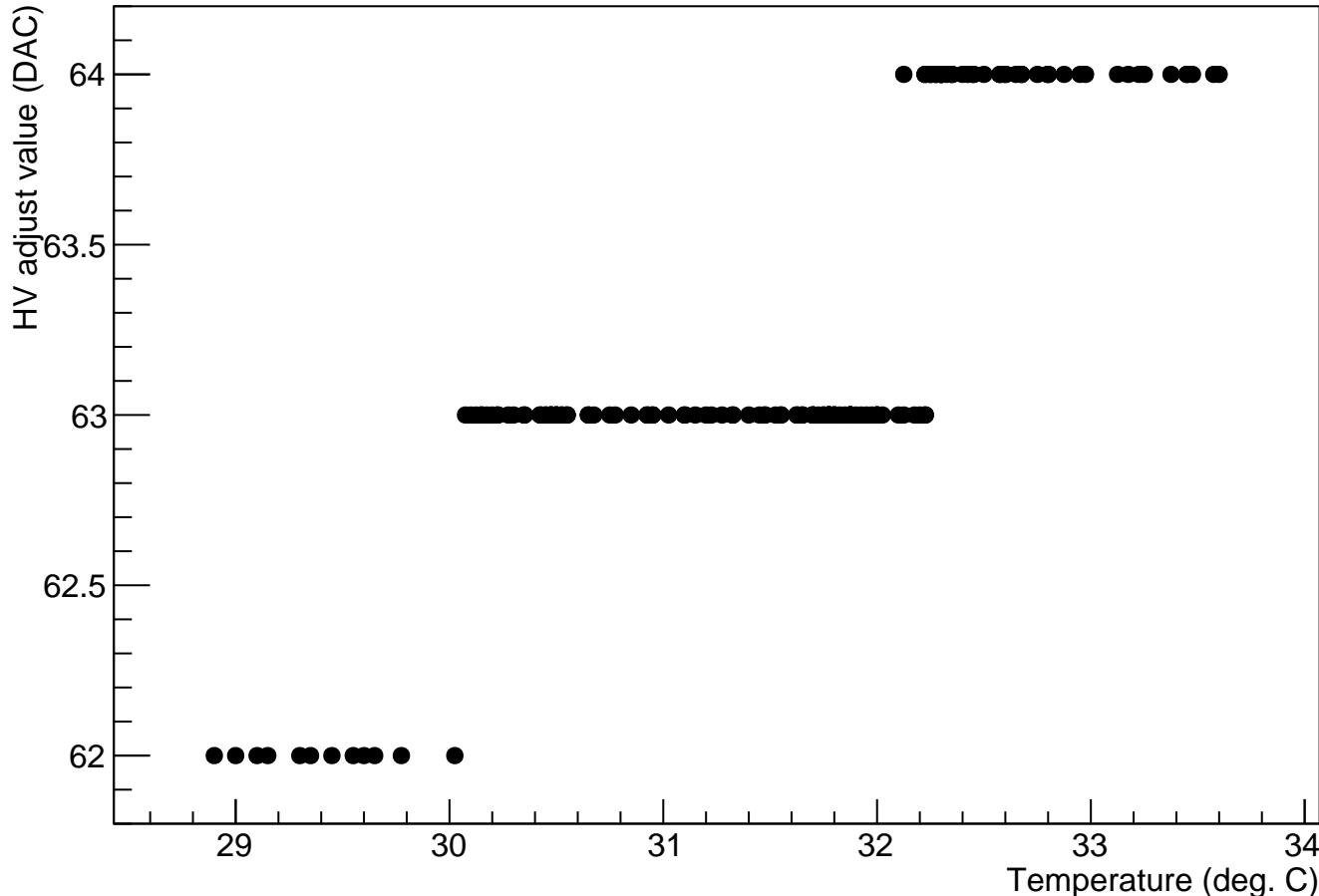
# gra\_hv\_temp\_lda1\_port7\_module50\_HBU3\_2



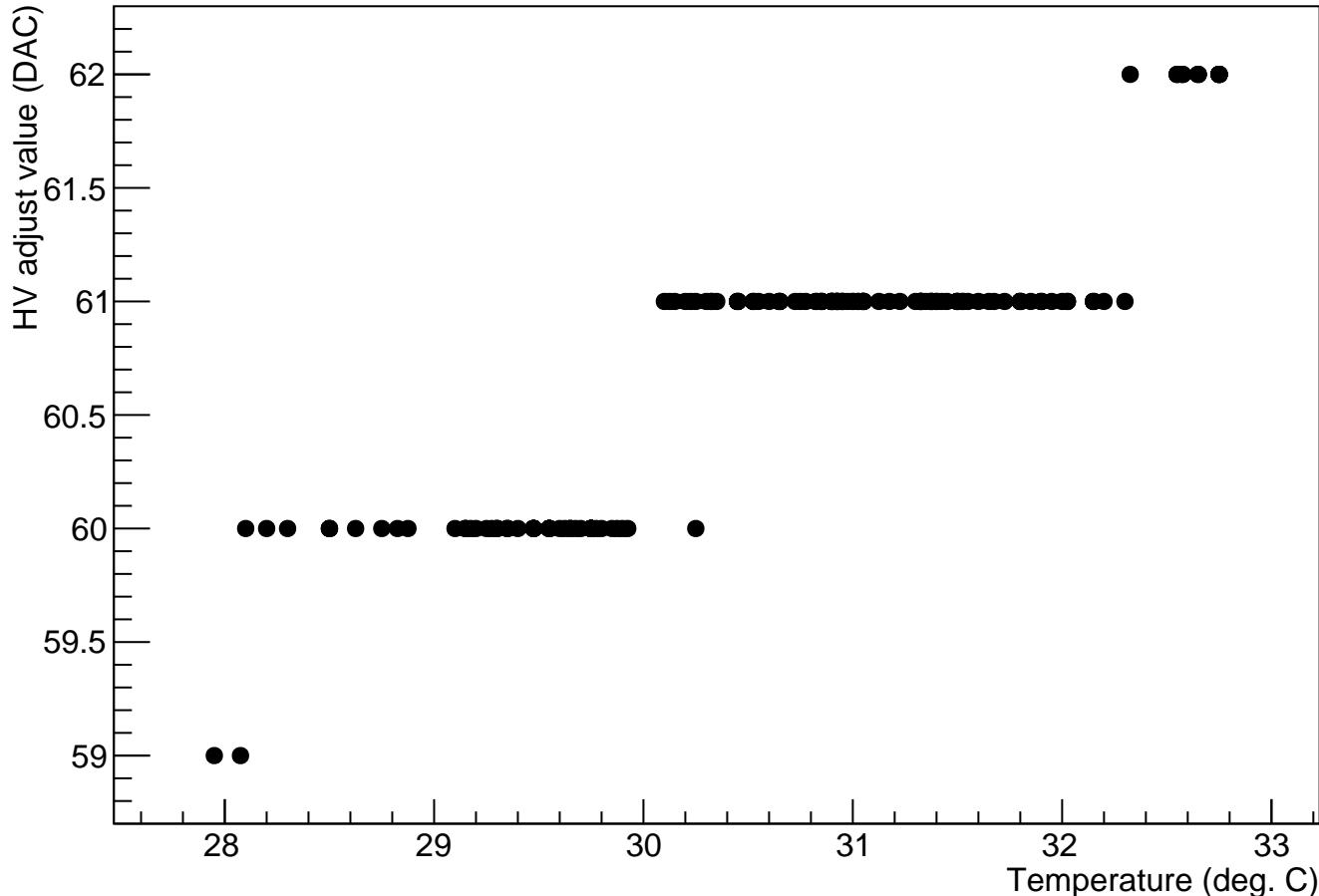
# gra\_hv\_temp\_lda1\_port8\_module51\_HBU3\_3



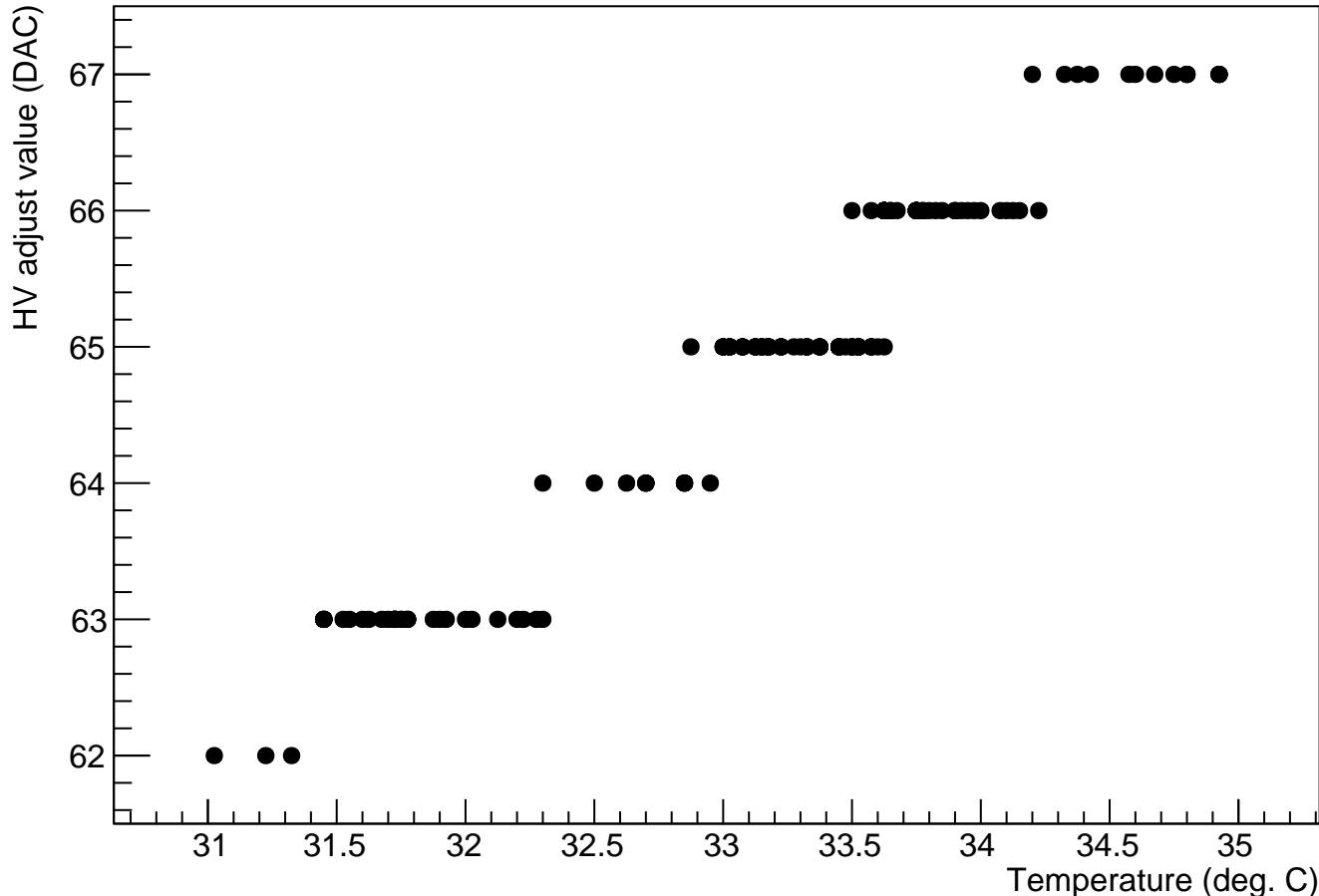
# gra\_hv\_temp\_lda1\_port10\_module52\_HBU3\_4



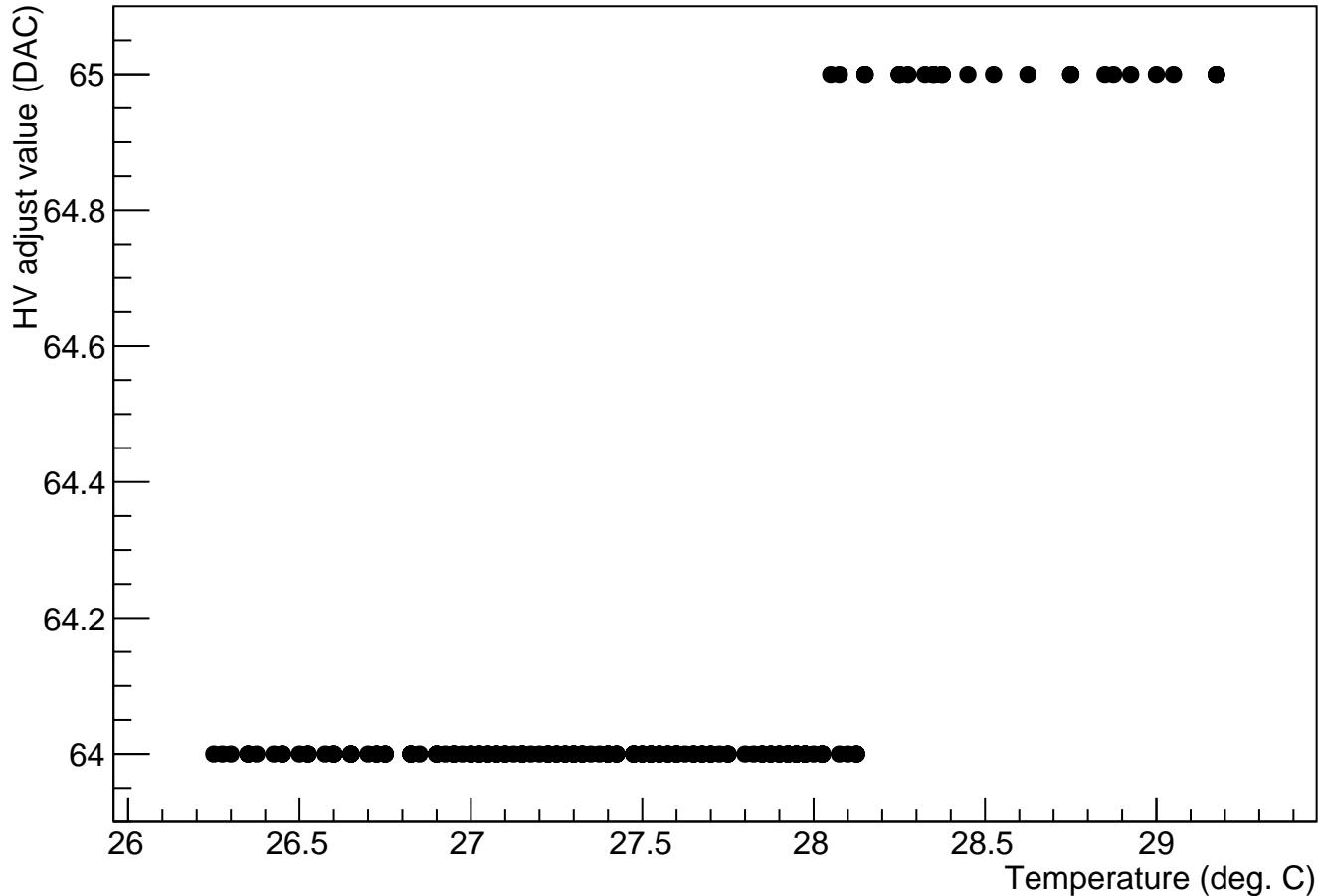
# gra\_hv\_temp\_lda1\_port11\_module53\_HBU3\_5



# gra\_hv\_temp\_lda1\_port12\_module45\_SM2



# gra\_hv\_temp\_lda1\_port14\_module54\_HBU3\_6



# gra\_hv\_temp\_lda1\_port16\_module42\_HBU5\_29

