

E-JADE AHCAL Tokyo analysis workshop

Thursday 23 August 2018

Summary: Summary part 1 - Room508 on 5th floor (17:00 - 19:00)

time	[id] title	presenter
17:00	[31] Timing in data	EMBERGER, Lorenz
17:30	[32] Timing in simulation	GRAF, Christian
18:00	[33] RootTreeWriter and Simulation studies	HUCK, Saiva

Friday 24 August 2018

Summary: Summary part 2 - Room508 on 5th floor (09:30 - 12:30)

time	[id] title	presenter
09:30	[36] AT pedestal and MIP	HEUCHEL, Daniel
10:00	[37] ET pedestal and gain calibration	PINTO, Olin
10:30	[38] Temperature compensation and HG/LG intercalibration	SUDO, Yuji
11:00	[39] Temperature measurement and electrons with and without PP	ROSMANITZ, Anna
11:30	[40] Wire chamber data	Mr LIU, Linghui

Summary: Summary part 3 - Room508 on 5th floor (14:00 - 18:15)

time	[id] title	presenter
14:00	[41] Tokyo module and pions with and without PP	Mr TSUJI, Naoki
14:30	[42] Event display and Tail catcher analysis	TORIMARU, Tatsuro
15:00	[43] PID as marlin processor	BOCHARNIKOV, Vladimir
16:00	[34] Data quality checks for pions	BUHMANN, Erik
16:30	[35] Data quality checks for electrons	ELKHALII, Amine