



AGH UNIVERSITY OF SCIENCE
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Status of FLAME-based setup for beam-test

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- FLAME ASIC
 - Fabricated, preliminary tests done, enough chips for ≥ 5 planes
- FLAME PCB board
 - Hosting 8 FLAME ASICs (16 high speed serial links)
 - Designed&Fabricated - Ready
- FPGA readout board
 - 1 board per plane, PCIe express connection to backplane board
 - Schematic ready, layout to be done...
- Backplane board
 - Hosting 4-8 FPGA readout boards
 - To be done done...

Thank You for Attention