

LCTPC Collaboration Meeting - Day 1 & 2

Monday 11 January 2021

Session 2: General Reports - 961 8001 3254 (15:15 - 17:00)

time	[id] title	presenter
15:15	[8] dE/dx measurements and laser results	Dr QI, Huirong
15:40	[14] low power consumption ASIC chip R&D in 65nm CMOS	ZHI, Deng WEI, Liu
16:10	[5] Development of a TPC readout electronics based on the SALTRO ASIC. A status report.	JONSSON, leif