



ILD: a Detector for the International Linear Collider

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The International Linear Collider



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- Energy: Z-mass 1 TeV
- Electron and positron polarisation
- TDR in 2013
 - + DBD for detectors
- Initial Energy 250 GeV Footprint ~20km



Under discussion in Japanese Government and international community

International Development Team (IDT)

Pre-preparator	Phase	Main Preparatory Phase		Construction Phase	
2020.8	(20	22) About 4 years	(2026)	About 9 years (2035)
LCB/LCC Dev	r ternational opment Team	ILC Pre-Lab		ILC Laboratory	



Higgs factory (and EW, and top-quark and...)



All Standard Model particles within reach of the ILC project

High precision tests of Standard Model over wide range to detect onset of New Physics

- Machine settings can be "tailored" for specific processes \rightarrow straightforward at the ILC
 - Centre-of-Mass ener

is polarisation (±80% e⁻, ±30%e⁺)

Higgs factories but also...

• "light" qq factory

(and Z-factory at Z-pole)

- WW factory
- Top-quark factory
- ttH factory
- ...





Detector Requirements

A comprehensive test of the SM and BSM (specially in the Higgs sector)



· Results in close-by / overlapping electromagnetic and hadronic showers

How?

By designing a

Particle Flow Detector

$\tau^+ \rightarrow 2\gamma(\pi^0) + \pi^+ + \bar{\nu}_{\tau}$

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Single particle separation

 \blacktriangleright Excellent energy resolution of ~3%







Particle Flow Concept





- Base the measurement on the subsystem with best resolution for a given particle type (and energy)
- Separation of signals by charge and neutral particles in the calorimeters
- Single particle separation

Technological Challenges

- Need extremely granular calorimeters (100 of millions of cells...!)
- Require very low material budget in front of the calorimeters and excellent tracking systems

Limitations

- Complicated topology by (hadronic) showers
- Overlap between showers compromises correct assignment of calorimeter hits -> Confusion term
- Need to minimize this term as much as possible









lain features of the ILD

- Particle flow as the key design driver
- Excellent vertexing very close to the IP
- Hybrid tracking system optimized for excellent resolution at high energies and ultimate efficiency over a broad momentum range
- High granular calorimetry
- Up to and including the HCAL, all inside solenoidal coil of 3-4 T





Optimized layout Particle Flow Detector





ILC offers a favorable experimental framework



Linear Lepton Colliders favor fully optimized PFA detectors

- Possible since experimental environment at ILC very different from LHC/LEP:
 - much smaller beam spot and beam pipe (first tracking layer at ~1cm of the IP)
 - much lower backgrounds
 - much less radiation
 - Pulsed beam structure

Power pulsed electronics → low material budget ! triggerless operation ! -> ALL events are recorded





The ILD performance



Different ILD models have been optimized to maximize the performance: **Full simulation & realistic reconstruction**



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The ILD performance: timing ?



TOF in the ECAL – Particle ID

- Standard" silicon sensors could reach O(100-300ps)
- LGAD sensors could get us to O(10ps) Drawback: high power consumption.



Impact in the physics reach?

- Could be a game changer for s-quark measurements
 - $Z/\gamma/Z' \rightarrow s\bar{s} \text{ or } H \rightarrow s\bar{s}$



Timing measurements for shower developments

- Neutral and slow components
 - Require ~ns precision
 - Reachable today with "standard" silicon, scintillators calorimeters
- ~0.1 ns scale: near the corner
- An even lower with GRPC (20ps)



The ILD performance: timing ?









Status of the R&D



R&D status

- ILD has a concept of the detector,
 - well defined
 - with technological options where sensible
- The main components of ILD
 - have been validated and beam-tested.
- A coherent System design has been developed.

Application of our technologies: CMS Calo upgrade, Belle VTX, T2K TPC, ALICE TPC



Slide borrowed from T. Benhke (ILD spokeperson)



Silicon Tracking Systems: R&D status



Forward Tracking Petal Thermomechanical mock-up







DEPFET VXD @ Belle2

- Silicon tracking conceptual studies
 - VTX extensive technology studies
- Several technology options remain open
 - CMOS / FPCCD / DEPFET / 3D-integrated / SOI



microstrip sensor



TPC Tracking System: R&D status





Resistive Micromegas:







PCMAG: B < 1.2 T, bore diameter: 85 cm Electron test beam: E = 1-6 GeV

Large Prototype has been built to compare different detector readouts under identical conditions and to address integration issues

HG Calorimetry: Technological solutions I



SiW Ecal



Analogue Scintillator HCAL and ECAL



Semi-conductor readout Typical segmentation: 0.5x0.5 cm²

Optical readout Typical segmentation: 3x3cm²

Semi Digital HCAL



Gaseous readout Typical segmentation: 1x1cm²

Integrated front end electronics

No drawback for precision measurements NIM A 654 (2011) 97





HG Calorimetry: Technological solutions II



C



Analogue Hcal and Scintillator Ecal



Semi-digital Hcal



Semi-conductor readout

Optical readout

- Realistic detector dimensions
 - Structures of up to 3m in length (more than 10000 cells)
 - With compact external components
- Challenge for the power pulsing techniques (for the power consumption management)



Calorimeter for ILC

HG Calorimetry: Technological solutions II





- With compact external components
- Challenge for the power pulsing techniques (for the power consumption management)



HG Calorimetry: Technological solutions III



Current detector interface card - AHCAL

Current detector interface card and thin detection unit - SiW Ecal



Current detector interface card - SDHCAL



- "dead space free" granular calorimeters put tight demands on compactness
- Current developments within CALICE meet these requirements

Unique successes in worldwide detector R&D

- Can be applied/adapted wherever compactness is mandatory
- Components will/did already go through scrutiny phase in beam test

HG Calorimetry: Technological solutions III



Current detector interface card - AHCAL

Current detector interface card and thin detection unit - SiW Ecal



Software



ILD has done a lot on the software and reconstruction side:

- We are a central player in pushing community wide software solutions in particular with iLCSoft (LCIO, DD4hep, etc) developed over 15 years
- We are collaborating with other communities (linear, circular, FCC-hh) to modernize our software stack: key4hep (DD4hep, EDM4hep,...)

Accessing ILD simulated data





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The ILD status: Interim Design Report



The work of ILD over the last years has been documented in the IDR and published this year. Signed by 302 authors from 62 institutes



What promising direction of R&D do we see to further improve ILD

- Timing
- Forward
- ..



Technology scouting

INTERIM DESIGN REPORT 2020

The International Large Detector ILD Concept Group



https://arxiv.org/abs/2003.01116

The ILD concept group



Very exciting moment to join ILD

Joining ILD

We welcome new members

- No resource commitments needed
- Key contributions are possible in many areas
- Full participation in the shaping of the ILD program and future

Guest membership

- Very simple access mechanism
- Access to ILD simulated data and tools
- · Great to do a study or a feasibility study in the ILD context
- Limited duration



http://www.ilcild.org



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ILD as an organisation

ILD executive team (2021-2023)



Software working group

Ties Behnke

Technical working group



Mary-Cruz Fouz, Karsten Buesser



Frank Gaede, Daniel Jeans

Kiyotomo Kawagoe

Physics working group



Keisuke Fujii, Filip Zarnecki

+ four elected members from the ILD community. Elections for these positions are in progress.

LCWS 2021 | ILD: Status and Plans



3/17/2021













single Interaction Point, shared by 2 detectors: "push-pull"

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Technologies for pixelated FTD 1 & 2





-All technology options remain open

CMOS FPCCD DEPFET **3D-integrated** SOI

...

Most studies for inner disks FTD 1&2 were inspired by DEPFET vertex detector developments for ILC and Belle II

Incorporate new developments: - IFIC activity in CMOS for Belle II PXD upgrade - advent of LGAD sensors

-Conceptual adaption of barrel design to disk geometry for some technologies, but no fully engineered design -This talk: develop a realistic prototype (mock-up) and simulation

-Assess thermo-mechanical performance











Three Baseline Technologies

GEMs: copper-insulator- copper sandwich, with holes

- 2 configurations are being tested:
- triple GEMs with 'standard CERN GEMs'
- double GEMs with 100 μ m LCP insulator

Resistive Micromegas: Bulk-Micromegas with 128 μm gap size between mesh and resistive layer

<u>GridPix:</u> Micromegas with 1 µm Al-grid over Pixel readout ASIC

- 55µm pitch of readout pixels
- resistive layer needed for protection of ASIC











J. Kaminski ILD phone meeting