Measurement of the time resolution of the KLauS6 ASIC: -Testbeam Report-

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Testbeam Setup 06-2021

- Unpackaged KLauS6b-ASIC on updated K6b-Testboard
- New Chip KLauS6b with fixed bugs
 - TDC bit flip
 - Reset bug in TDC fine counter latches
- 4 sensor layers with 9 channels each
 - 2 types of SiPMs (15 & 25 μ m)
 - Central row equipped with tiles \rightarrow 12 channels (1 broken)
- Previous testbeam in October 2020: Timing resolution <1ns achieved with KLauS6("a") and wrong SiPM bias network





Goals and Program

Focus on timing performance in beam with AHCAL tiles

- Main goals in this testbeam:
 - Full calibration of chip for two types of SiPMs
 - Parameter sweeps over HV, Threshold, Input stage bias current to find good working point
 - Cross check results from last testbeam (10-2020)
 - \rightarrow Expect higher impact of timewalk correction on timing performance
- Additional program:
 - Energy resolved timing studies with tungsten absorbers
 - Power pulsing with full chip (to be analysed)

Calibration steps

ASIC parameter tuned using LED system & in-beam measurements:

<u>Compromise between noise trigger rates,</u> power and timing performance

- ADC/TDC DNL correction
- Hold Delay optimization
- SPS Gain uniformization
- Auto Gain inter-calibration
- HV&Threshold tuning



Timewalk Correction



Timewalk Correction – Comparison



- Timewalk correction greatly improves timing
- Tails of peak not perfectly described by single Gauss function
 - Time resolution is energy-dependent
 - Timewalk hard to correct for small signals (<<1MIP)

Absorber Runs – Energy-resolved timing



- 2 x 2.5mm tungsten plates
- Channel by channel MIP scaling
- From 0.3 to 16.3 MIP in 0.4 MIP slices
- Energy-dependent time resolution well described by function with three terms:

A: Statistical term from Poisson distribution of photon generation

B: Energy-dependent electronic noise

C: Intrinsic resolution of time measurement (TDC)

• Limited number of tile pairs with unknown quality, no clear trend favouring one type of SiPM

Absorber Runs – Comparison



• Similar measurement from MPI Munich

↗Lorenz and Fabian's talk @AHCAL session

- Same tile geometry & SiPMs
- Different readout
 - Fast PicoScope with full waveform digitization
- KLauS results close to MPP results

Conclusion

- Dedicated testbeam measurement to assess timing performance of KLauS6b with AHCAL tiles
- Full calibration possible for common usage of 2 types of SiPMs
- Without absorber total time resolution around 600 ps
- Absorber runs show total time resolution of 450 ps
 - No favour for any type of compared SiPM
 - Scintillator is limiting factor (photon statistics)
- At high light yield (>15 MIP): Intrinsic resolution of setup (KLauS6 + SiPM + AHCAL tile) ≈95 ps
- Results close to SiPM-on-tile studies from MPI Munich
- Future studies:
 - Power pulsing of KLauS (data to be analysed)
 - Telescope integration for position scans

Backup

Workflow and Overview

Calibration:

- ADC/TDC DNL correction
- Hold Delay optimization
- SiPM Bias uniformization
- Parameter sweeps to find operating point

Measurements in beam with and without absorber

Analysis:

- Timewalk correction
- Timing for first runs without absorber

DNL correction



- ADC DNL correction needs extra measurement with slow ramp over full dynamic range of ADC
 - Done in Heidelberg beforehand
- TDC DNL correction (in-situ)
- KLauS calibration object delivers calibrated data for ADC & TDC

Hold Delay Optimization in Beam

- Only digitize the maximum amplitude of waveform
- "Waiting time" after Trigger
- Global and fine adjustment
 - Need to find a common value
- Use mean value of MIP spectrum





SPS Gain uniformization



- Adapt bias voltage of the SiPM channel-wise
- Make use of integrated LED system
 → Single photon spectra



Threshold CEC scans



- Channel Event Counter (CEC) scans for rising threshold
- Relation #fired pixel threshold
 - Uniformity between channels
- Here: Run only with DCR
 - Reduction up to the 2.5th pixel

Auto Gain Calibration

- Extract scaling between branches HG LG for later timewalk correction
 - Measure MIPs in beam with different branch selections and look for efficiencies



Modus operandi

Sigma of TDiff Spectrum vs HV & Threshold 59 Ę 13.1205 13.0907 22 58.5 13.5971 20 13.1425 13.7633 14.1066 58 18 12.659 13.9457 15,1510 14.8001 16.8036 £ 609 57.5 16 13.0679 13.5709 16.3086 17.5341 18.5626 17.862 57 14 12.3346 13.6683 14.8648 16.1624 19.1466 19.7.25 19.088 19.4638 20.2338 56.5 12 14.2531 17.7859 22.2373 21.4906 22.0734 22 1983 22,1244 10 56 6 7 10 11 12 13 15 9 14 threshold MIP Spectrum Thresh = 7 hE1 corr MIP spectrum Thresh = 8 hE1_corr Entries 46668 800 28690 Entries 18000 711.5 Mean 747.7 Mean Std Dev 25.42 28.83 Std Dev 16000 700 14000 600 12000 500 10000 400 8000 300 6000 200 4000 100 2000 950 1000 800 1000 750 900 950 ADC bin ADC bin

Input stage bias current, Threshold and SiPM bias voltage are most crucial parameters for timing

Criteria for selection of parameters

- 1. Noise-free + no cut into MIP spectrum
- Reasonable HV/Overvoltage on SiPMs (2-6.5 V investigated)
- 3. Operate both types of SiPMs together
- 4. Preliminary good timing performance (w/o timewalk correction)



Timewalk correction

TDiff of CH1-Ch10 vs charge of CH10 with one Fit slice



Combinatorics in continuous readout

Flat background in ΔT spectrum from combinatorics

- Length of readout cycle not well defined, but long
- Necessary for continuous readout of data
- @TB: increased readout speed → Less combinatorics

Timing Fit: Gauss + Constant

• Resolution = $\frac{\sigma_{gauss}}{\sqrt{2}} \cdot 195 ps$



Timing performance @ 1 MIP



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